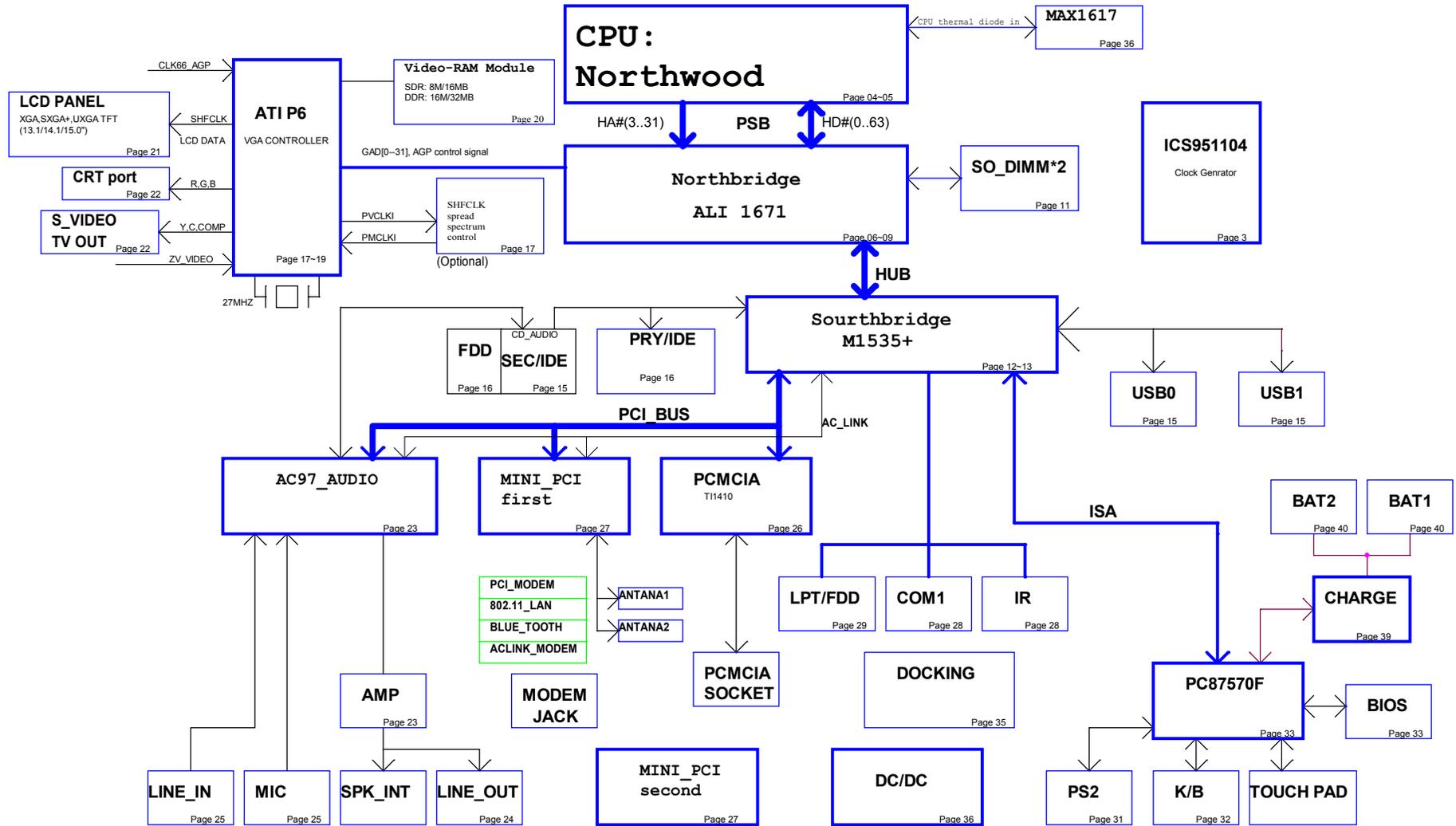
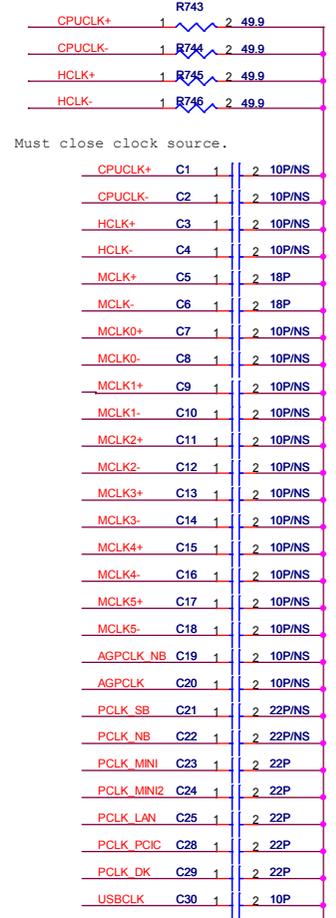
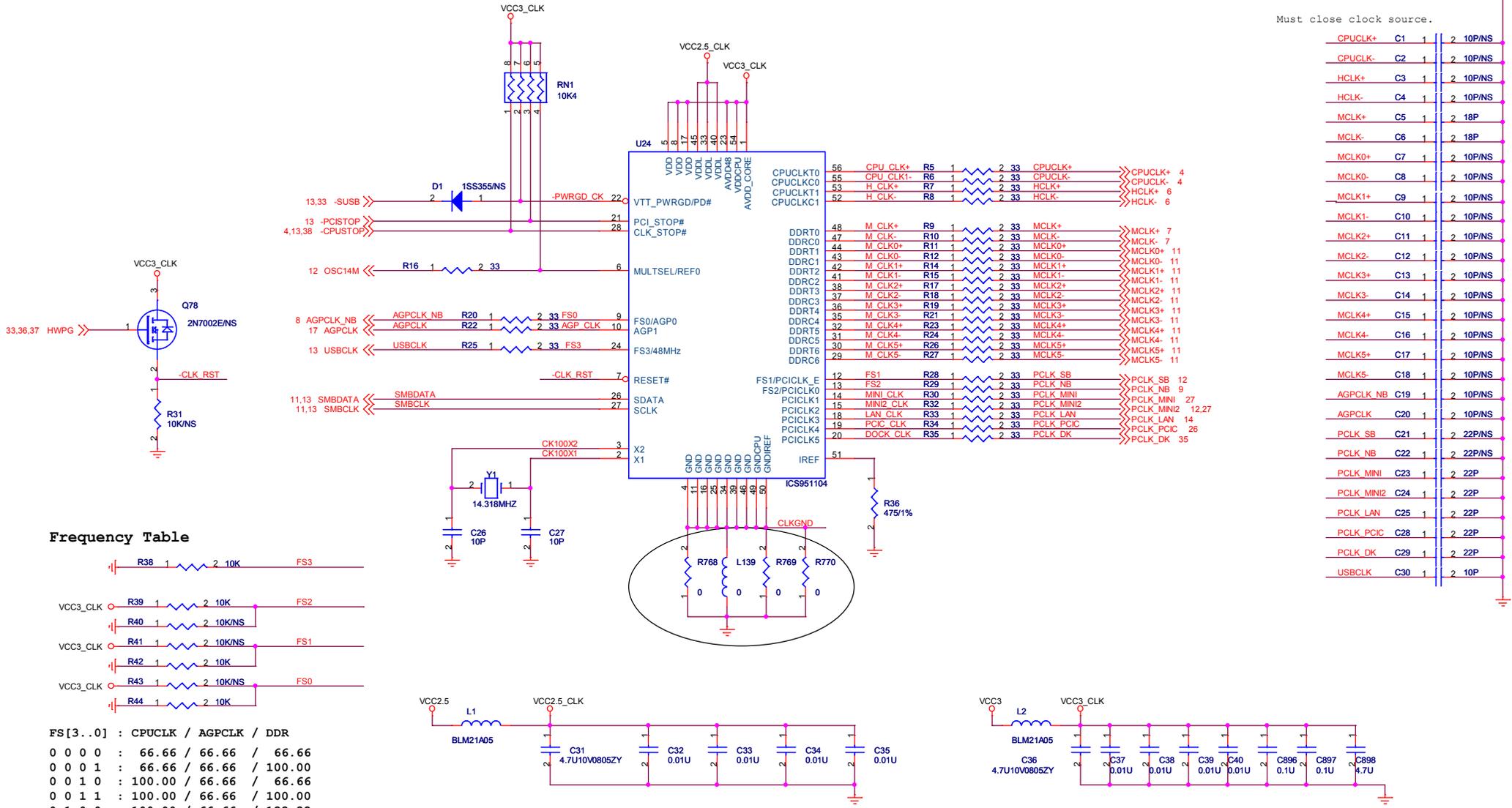


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2	PAGE INDEX		28	COM/IR interface, LID_SW	
3	Clock Generator		29	LPT/FDD interface	
4	NORTHWOOD CPU(HOST BUS)-1		30	87570 IO	
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6	North Bridge ALi 1671 -2 (HOST)		32	Keyboard CONN., Ring indicator , RF CKT	
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10	Ali M1671 Configuration and DDR Pull-up resistors		36	DC/DC 3V/5V	
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12	South Bridge ALi M1535+ -1 (PCI, ISA)		38	DC/DC VCORE	
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19	ATI P6 (PWR/GND) -3				
20	Video DDR SDRAM				
21	LCD interface				
22	S Video/Composite, CRT				
23	AC97 Codec ALC201A				
24	AMP/Line out				
25	Line in/Mic in/CD audio in				
26	TI1410 Card Bus I/F				

# RT6.0





**Frequency Table**

FS3	10K
VCC3_CLK	20K
VCC3_CLK	20K/NS
VCC3_CLK	20K
VCC3_CLK	20K/NS
VCC3_CLK	20K

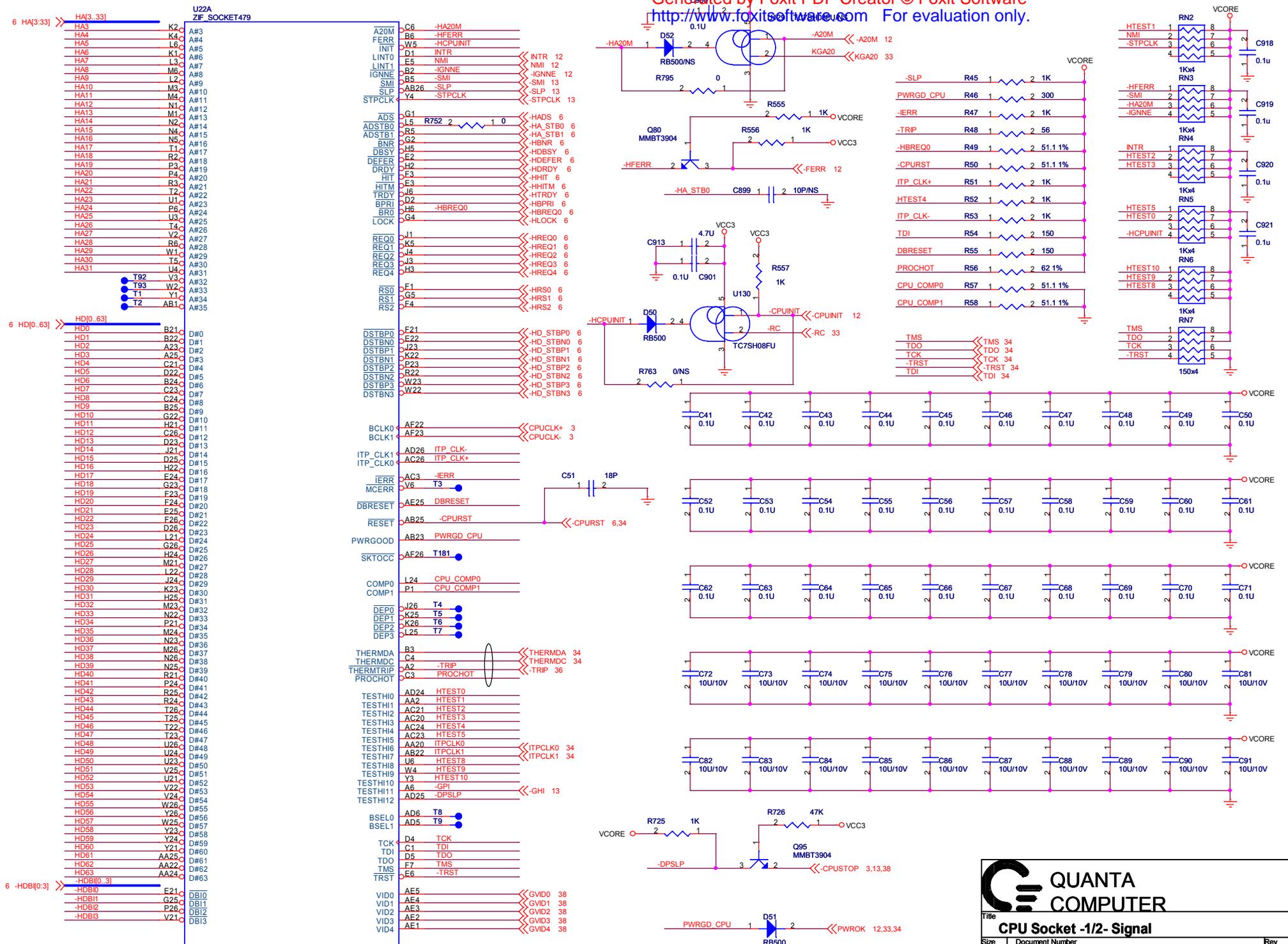
FS [3..0]	CPUCLK	AGPCLK	DDR
0 0 0 0	66.66	66.66	66.66
0 0 0 1	66.66	66.66	100.00
0 0 1 0	100.00	66.66	66.66
0 0 1 1	100.00	66.66	100.00
0 1 0 0	100.00	66.66	133.33

**QUANTA COMPUTER**

Title: **Clock Generator**

Size: Custom | Document Number: **ICS\_CLOCK GENERATOR** | Rev: A

Date: Tuesday, January 18, 2005 | Sheet: 3 of 41

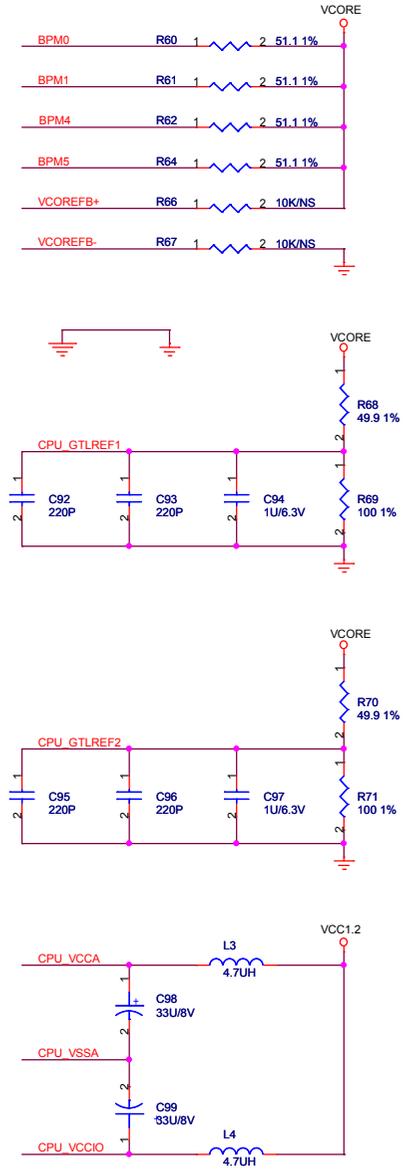
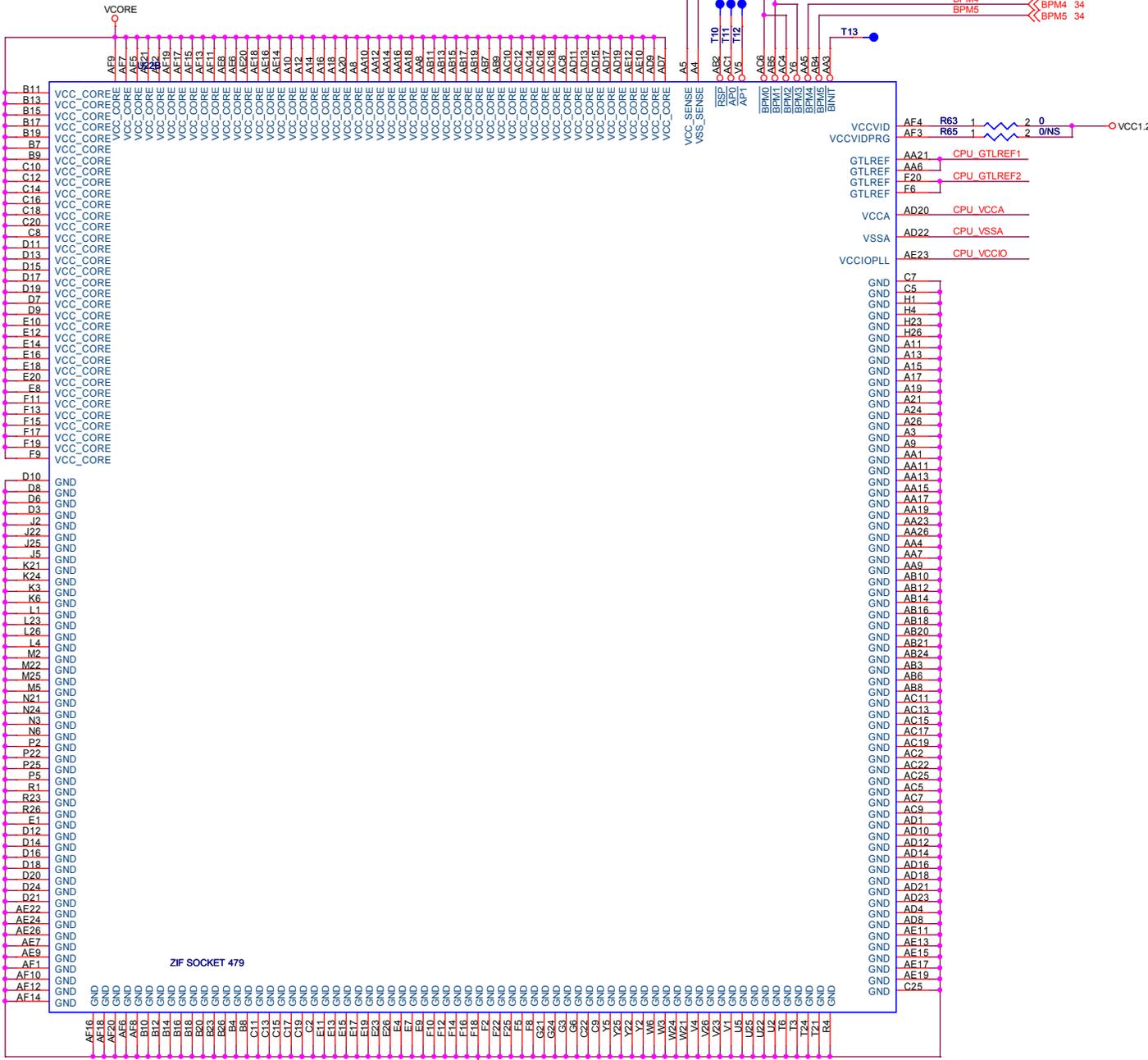


**QUANTA COMPUTER**

Title: **CPU Socket -1/2- Signal**

Size	Document Number	Rev
Custom	<b>DA0N1M1Bxxxx</b>	A

Date: Tuesday, January 18, 2005 Sheet 4 of 41

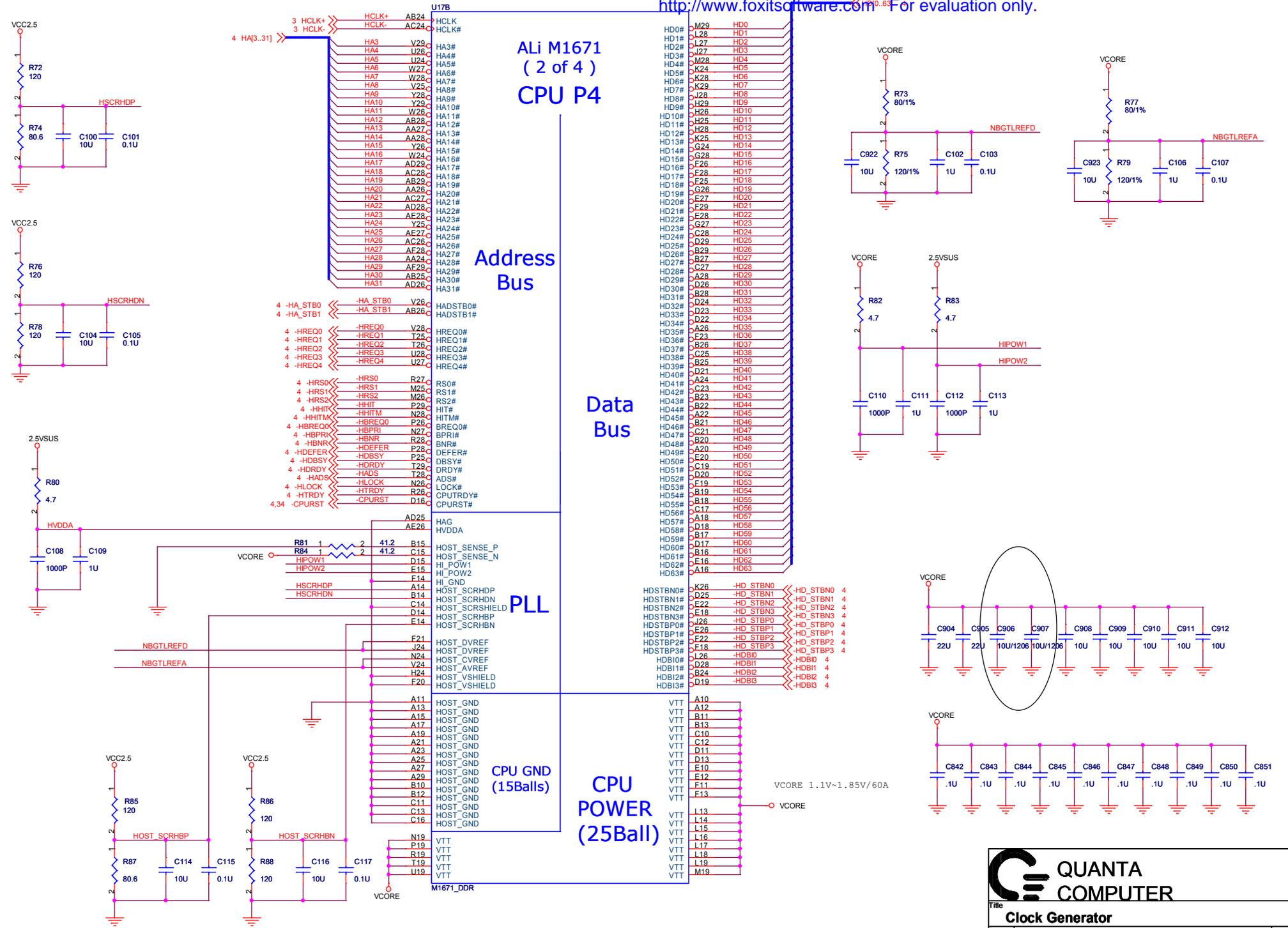


**QUANTA COMPUTER**

Title: **CPU Socket -2/- Power & Ground**

Size	Document Number	Rev
Custom	<b>NORTHWOOD - 2</b>	A

Date: Tuesday, January 18, 2005 Sheet 5 of 41



**ALi M1671  
( 2 of 4 )  
CPU P4**

**Address Bus**

**Data Bus**

**PLL**

**CPU GND  
(15Balls)**

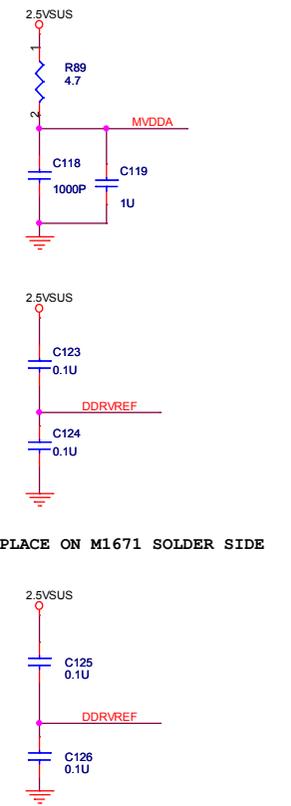
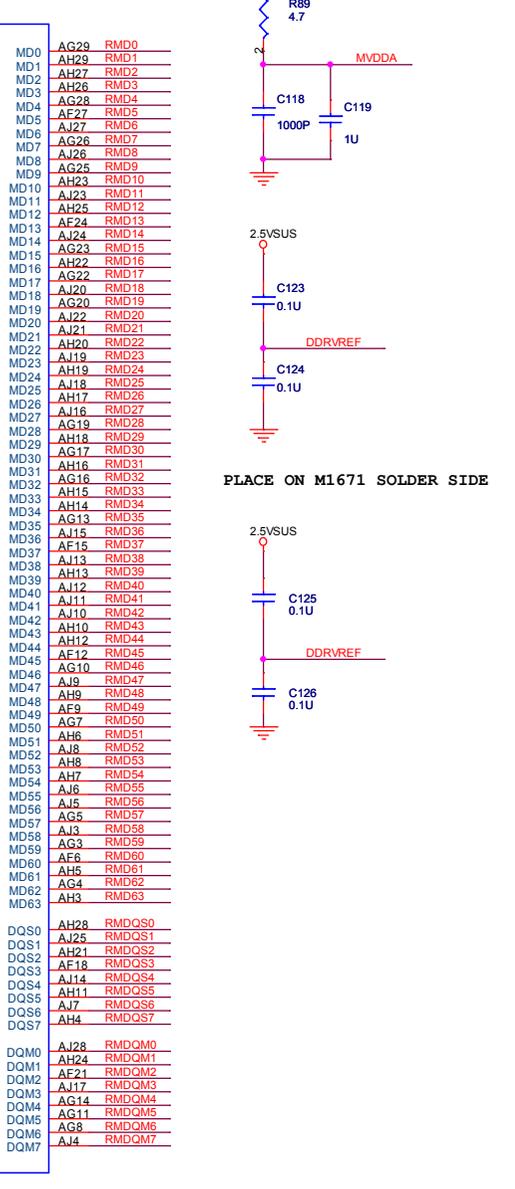
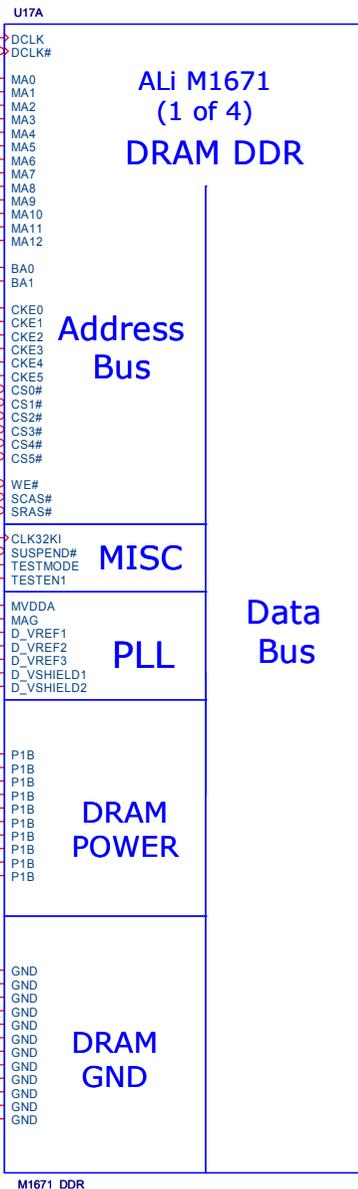
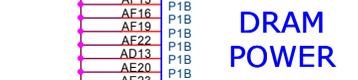
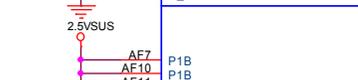
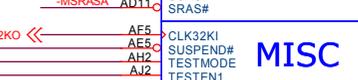
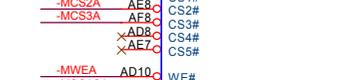
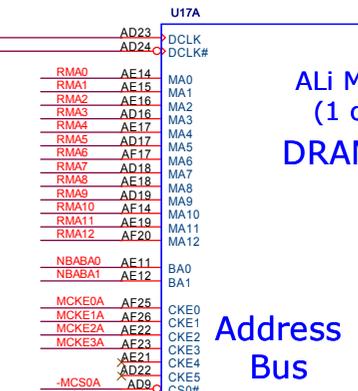
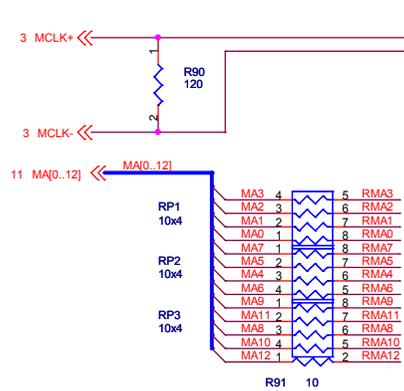
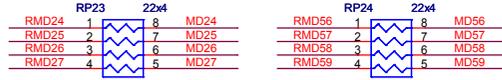
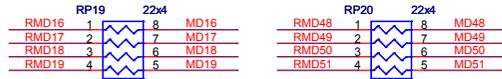
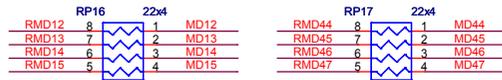
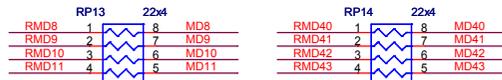
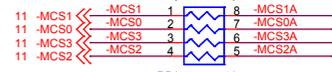
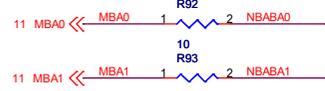
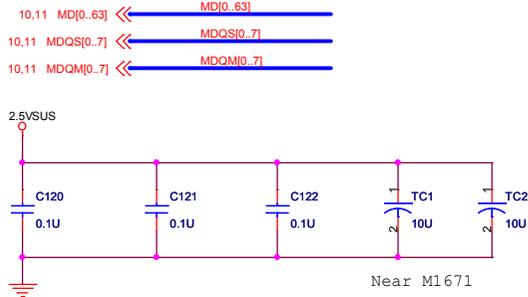
**CPU POWER  
(25Ball)**

**QUANTA COMPUTER**

Title: **Clock Generator**

Size: Custom Document Number: **M1671-Host** Rev: A

Date: Tuesday, January 18, 2005 Sheet: 6 of 41

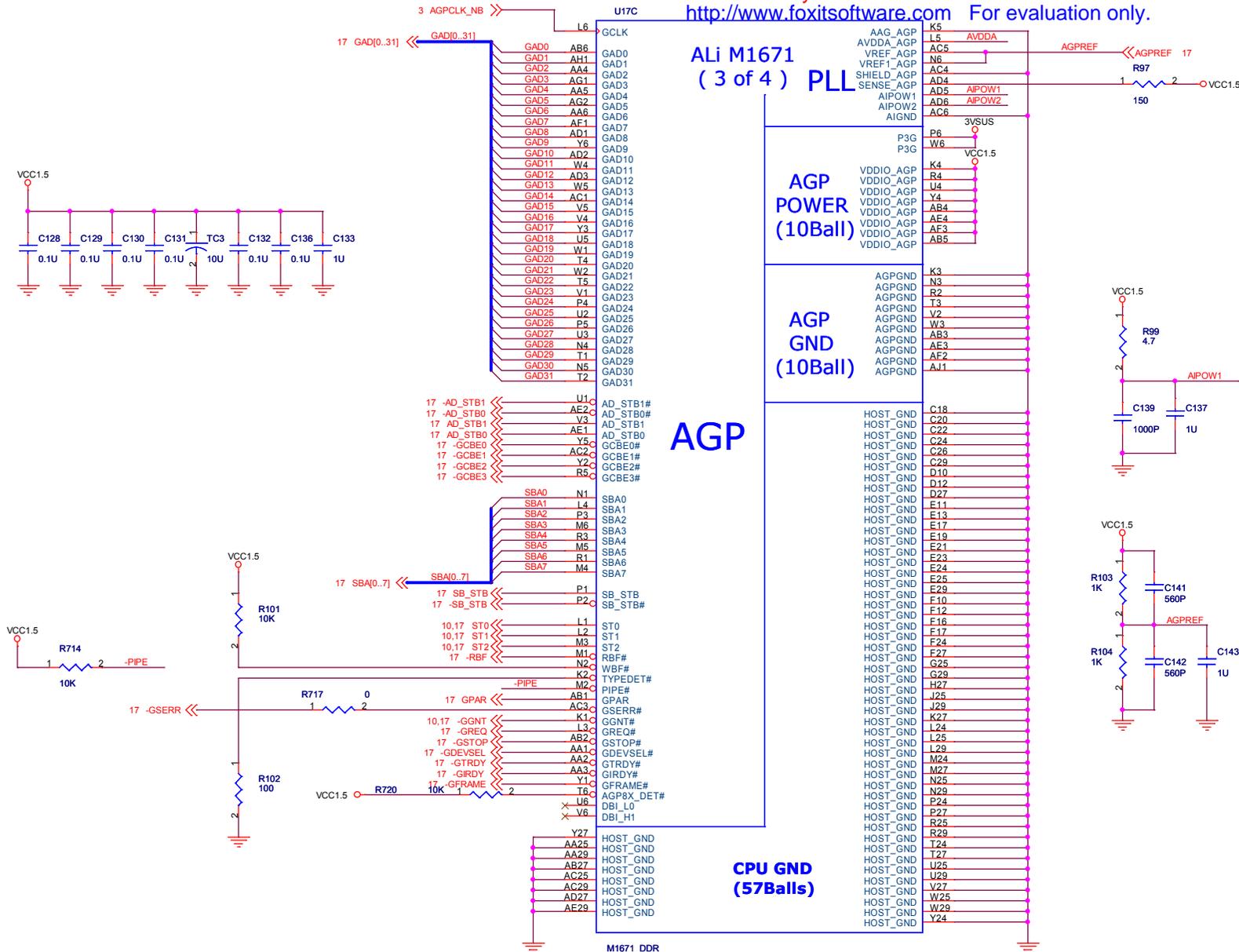


**QUANTA COMPUTER**

Title: **Clock Generator**

Size: Custom Document Number: **M1671-DDR** Rev: A

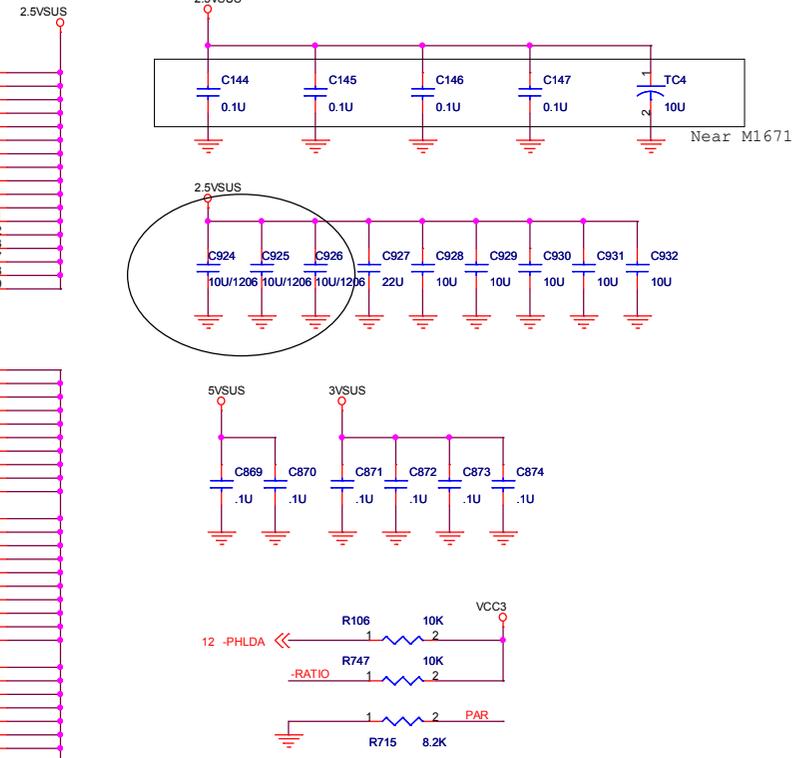
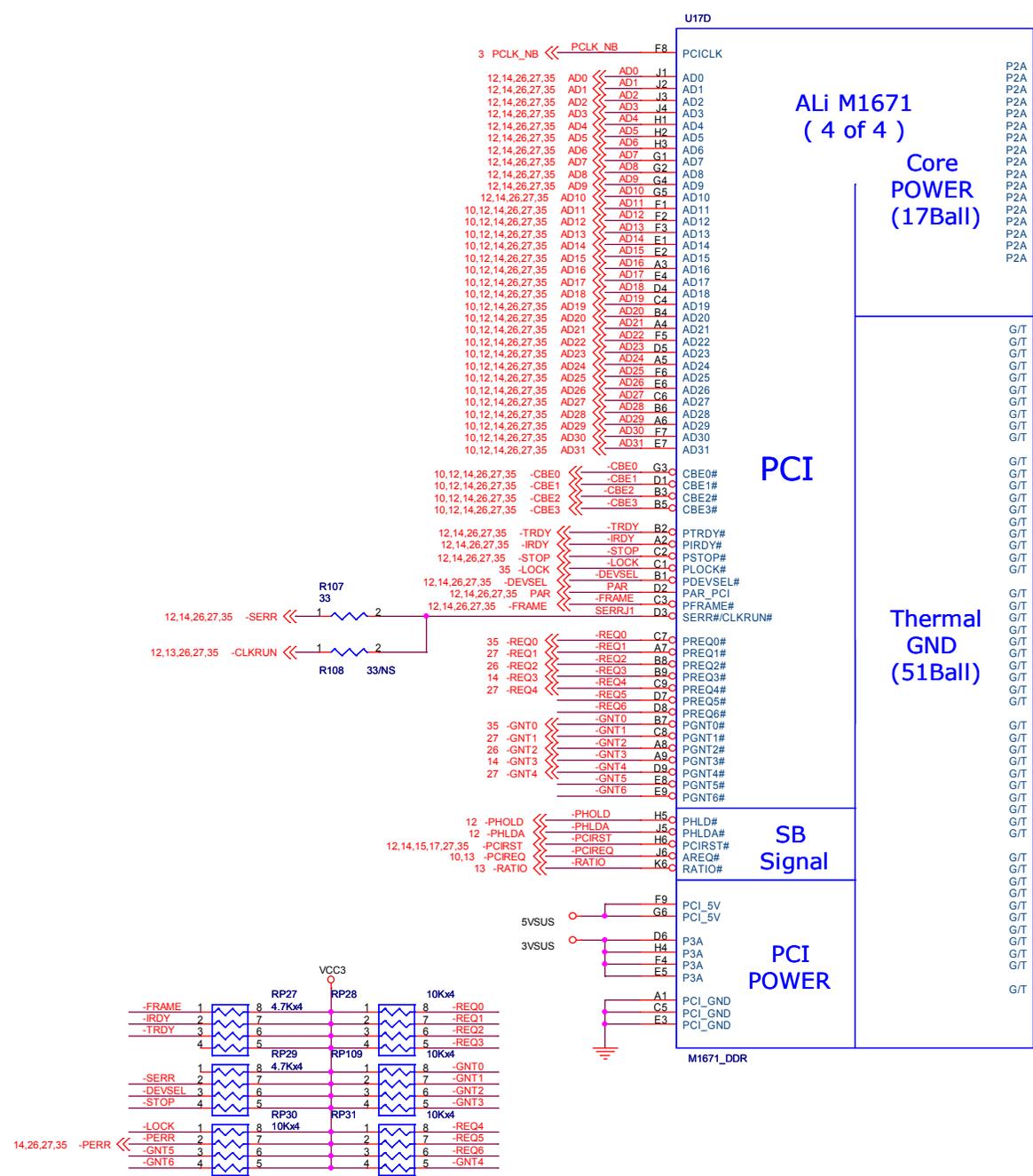
Date: Tuesday, January 18, 2005 Sheet: 7 of 41



**QUANTA COMPUTER**

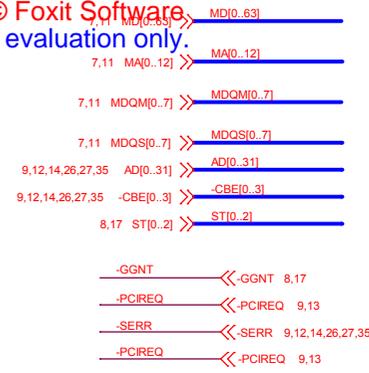
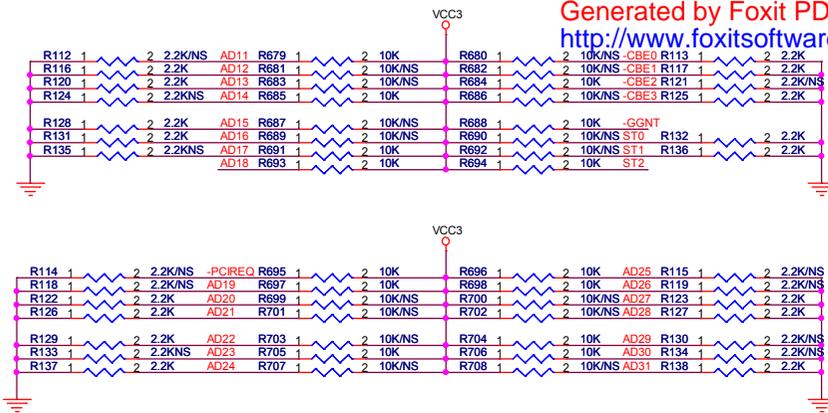
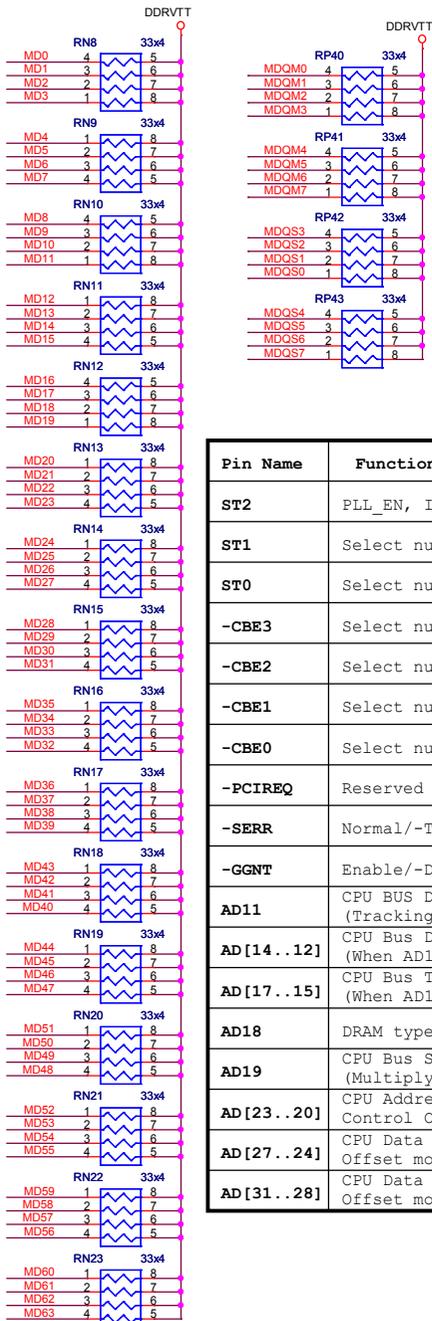
Title: **Clock Generator**

Size: Custom	Document Number: <b>M1671-AGP</b>	Rev: A
Date: Tuesday, January 18, 2005	Sheet: 8 of 41	

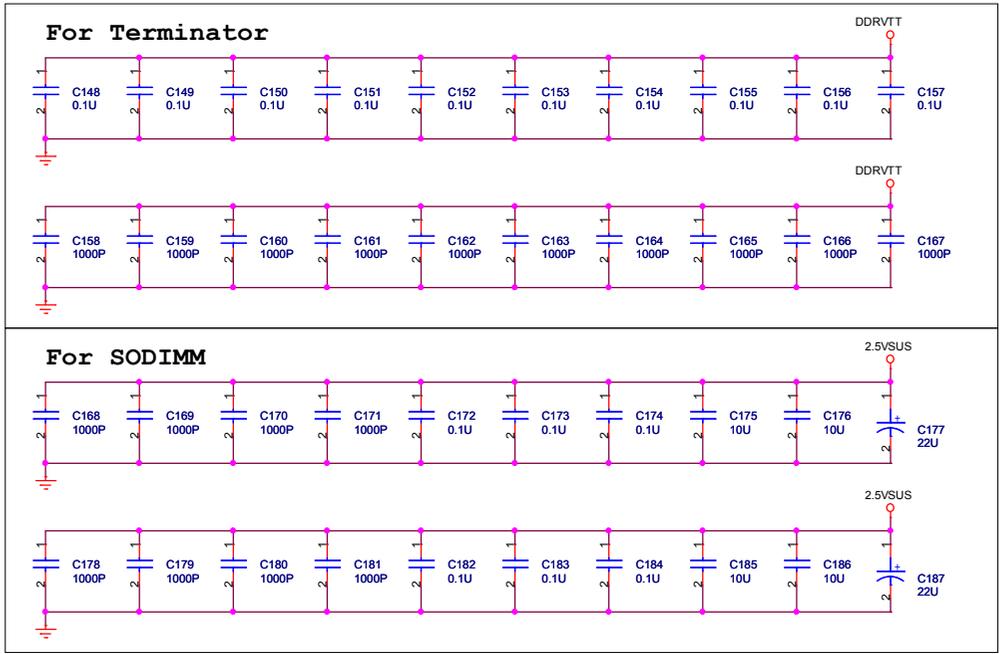


LAN	AD19	-REQ3 / -GNT3	-INTC
PCMCIA	AD21	-REQ2 / -GNT2	-INTA
PCI MINI1	AD20	-REQ1 / -GNT1	-INTB / D
PCI MINI2	AD22	-REQ4 / -GNT4	-INTD / B
DOCK		-REQ0 / -GNT0	-INTC / D





Pin Name	Function	Pull-Low	Pull-High
ST2	PLL_EN, Internal PLL Enable/-Disable	NC	YES
ST1	Select number of PLL stage compensation	YES	NC
ST0	Select number of PLL stage compensation	YES	NC
-CBE3	Select number of PLL stage compensation	YES	NC
-CBE2	Select number of PLL stage compensation	NC	YES
-CBE1	Select number of PLL stage compensation	YES	NC
-CBE0	Select number of PLL stage compensation	YES	NC
-PCIREQ	Reserved	NC	YES
-SERR	Normal/-Test mode	NC	YES
-GGNT	Enable/-Disable internal PLL test mode	NC	YES
AD11	CPU BUS Drivint Control mode. (Tracking circuit/-By AD[17..12])	NC	YES
AD[14..12]	CPU Bus Driving Low Strength Control. (When AD11 pull low)	100	
AD[17..15]	CPU Bus Termination Strength Control. (When AD11 pull low)	100	
AD18	DRAM type selection (DDR/-SDR)	NC	YES
AD19	CPU Bus Strobe Timing Control (Multiply/-Offset)	NC	YES
AD[23..20]	CPU Address/Command Bus Strobe Timing Control Offset mode	1000	
AD[27..24]	CPU Data Bus Strobe (N) Timing Control Offset mode	0110	
AD[31..28]	CPU Data Bus Strobe (P) Timing Control Offset mode	0110	



**QUANTA COMPUTER**

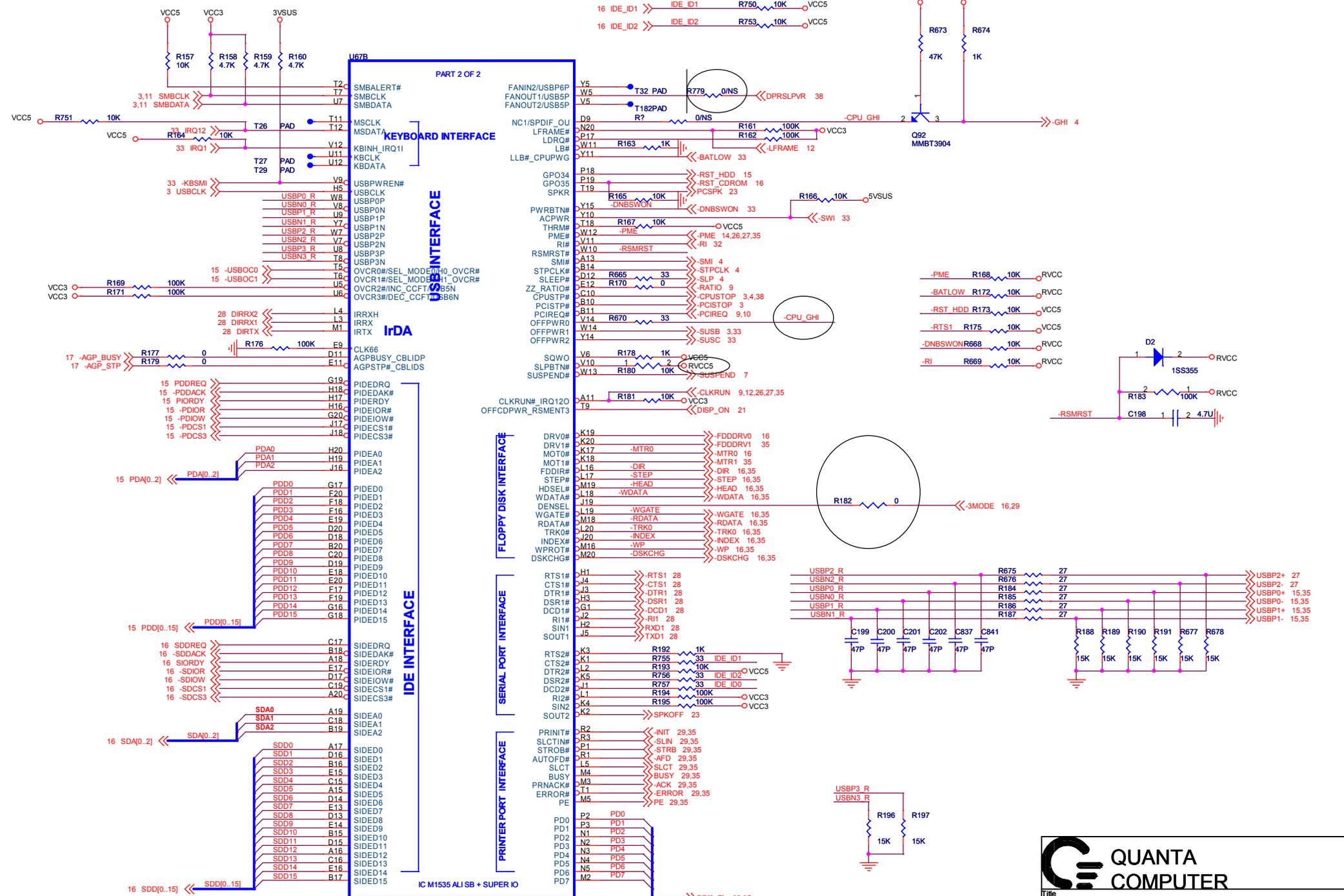
Title: **Memory -1/2- Damping & Terminator**

Size: Custom | Document Number: **CONFIGURATION SETTING** | Rev: A

Date: Tuesday, January 18, 2005 | Sheet: 10 of 41





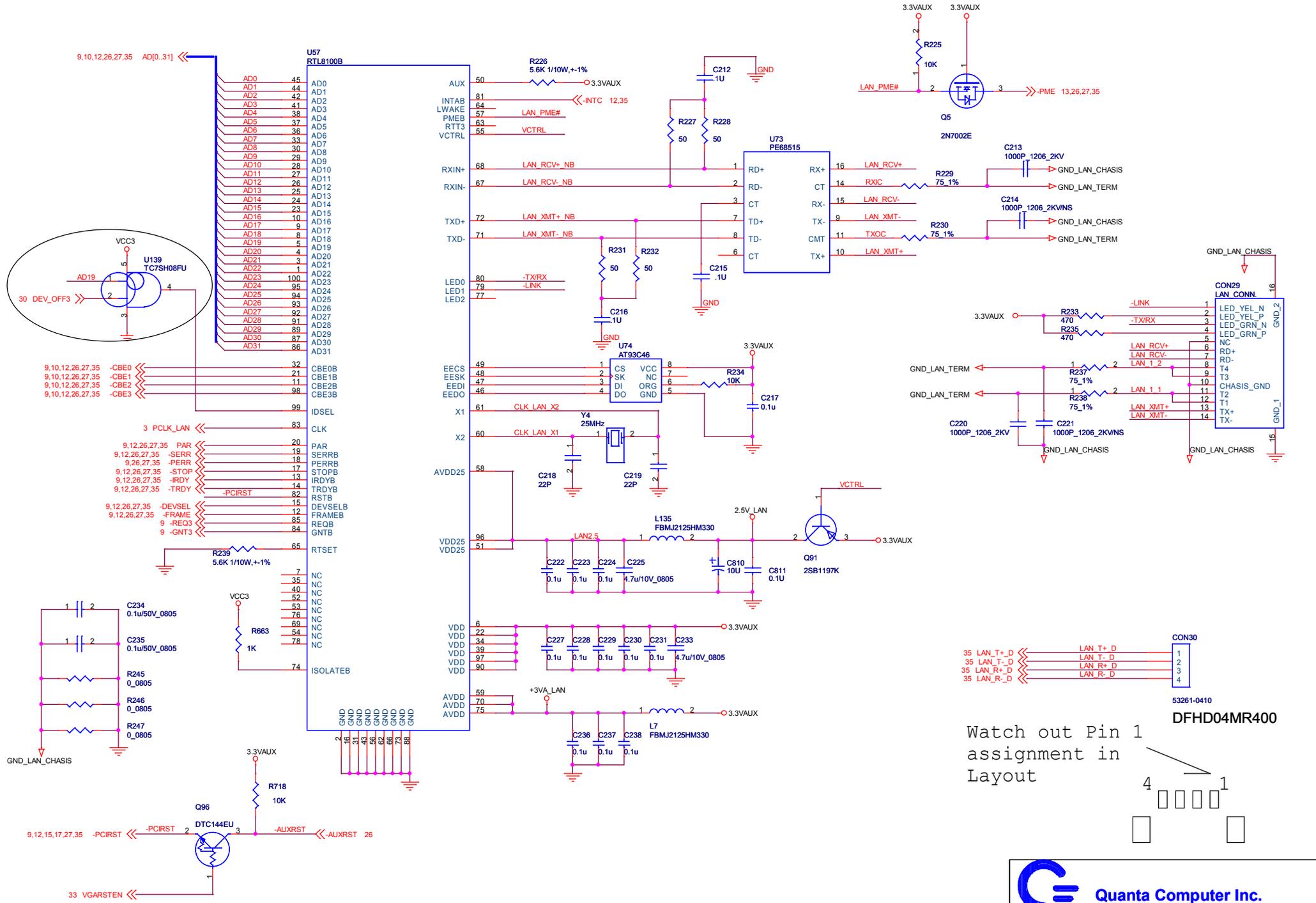


**QUANTA COMPUTER**

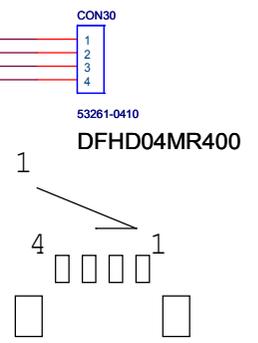
Title: **1535+ 2/2**

Size	Document Number	Rev
Custom	<b>DA0N1M1Bxxxx</b>	A

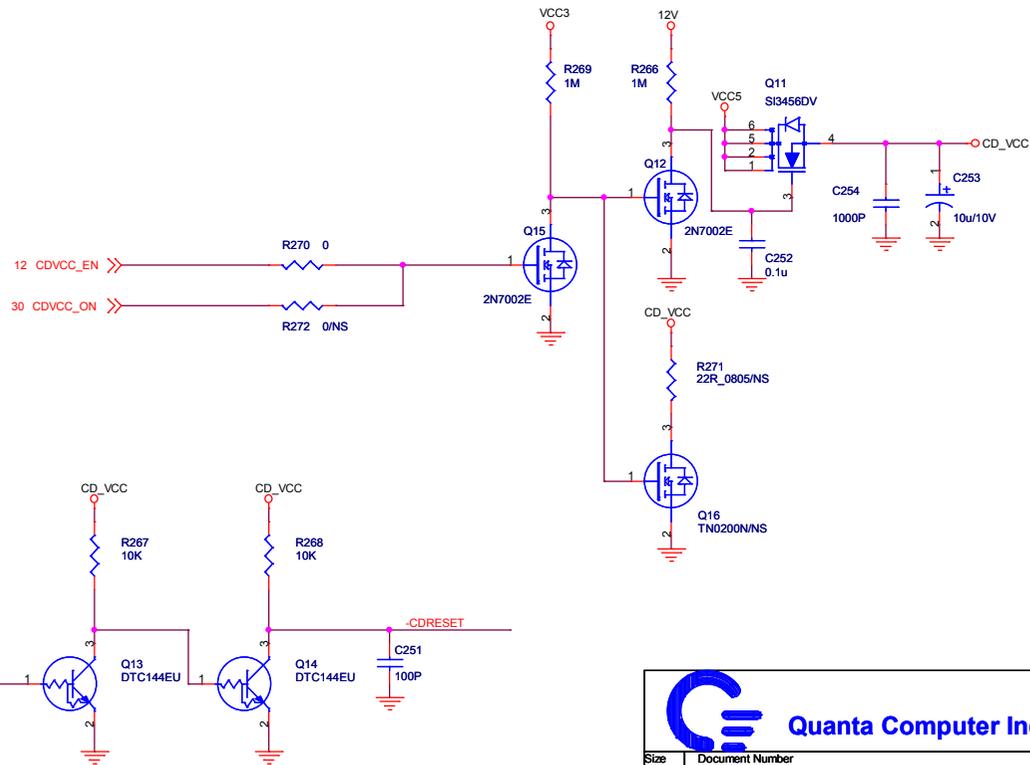
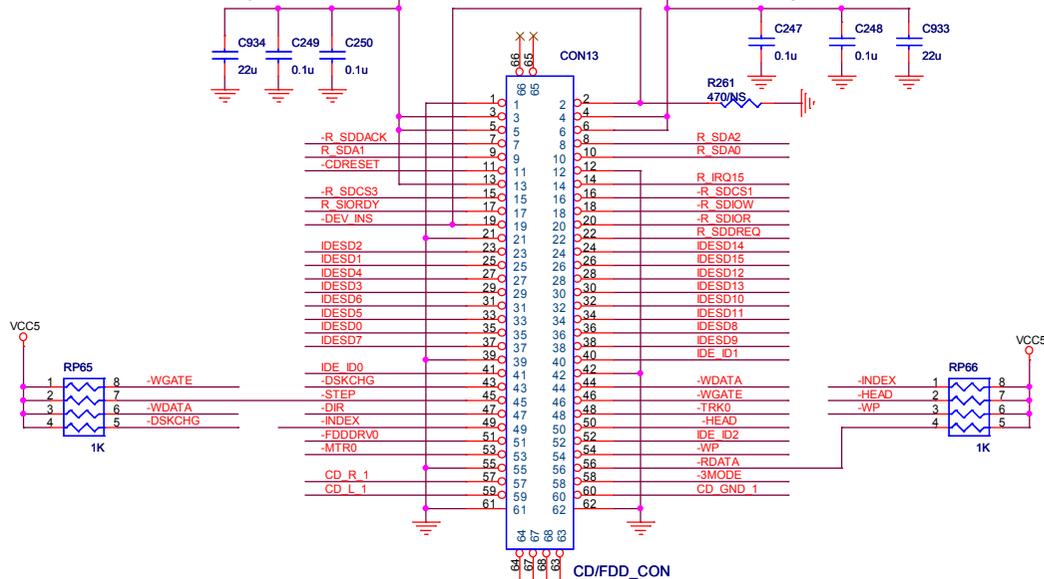
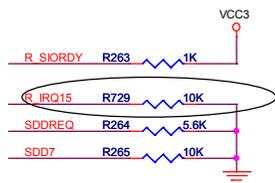
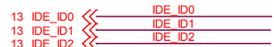
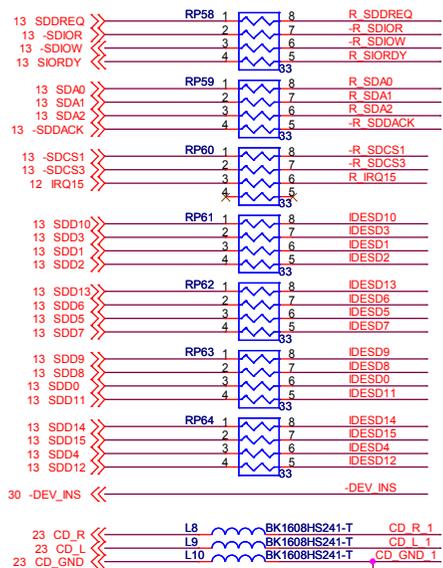
Date: Tuesday, January 18, 2005 Sheet 13 of 41

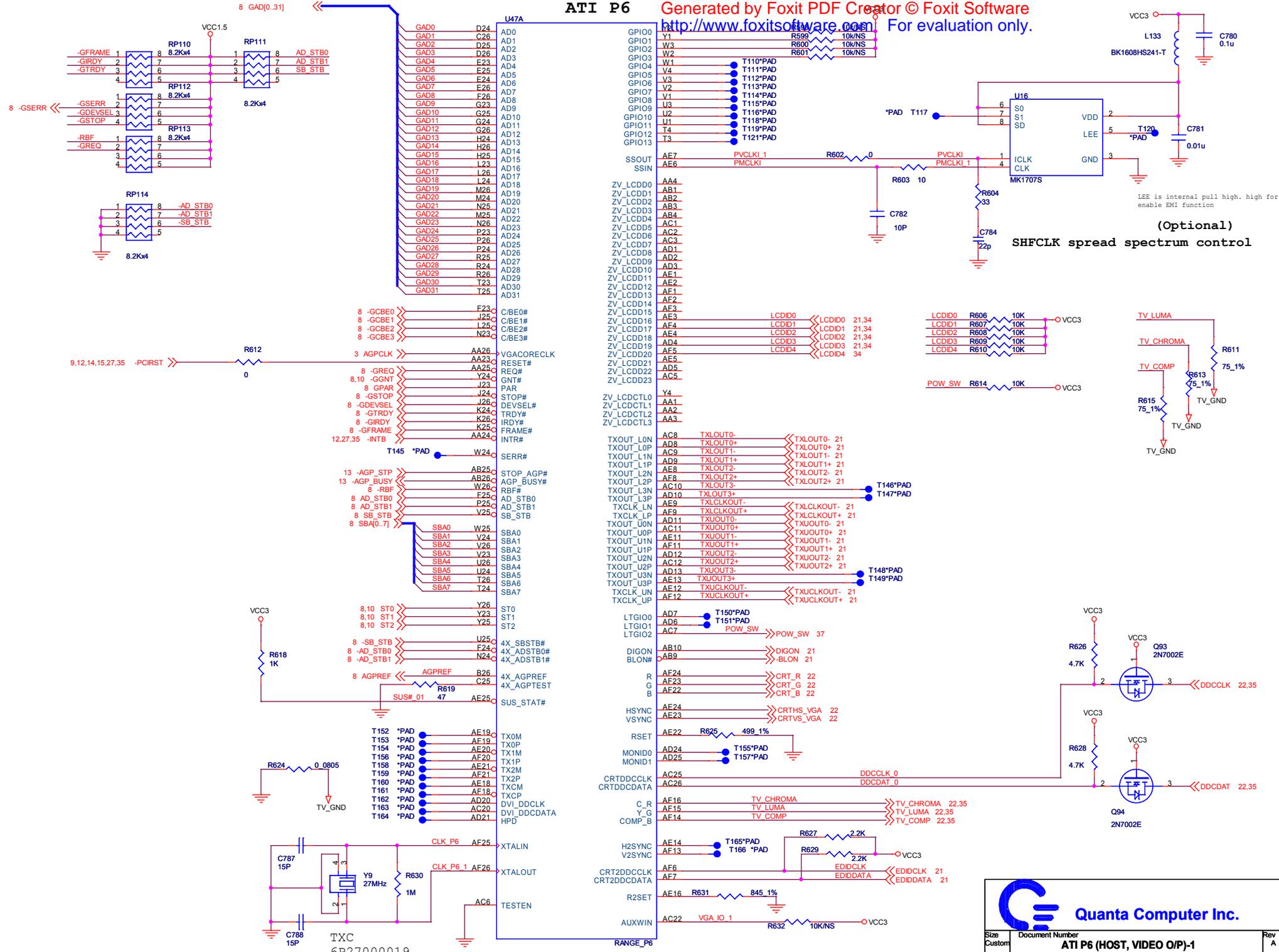


Watch out Pin 1 assignment in Layout

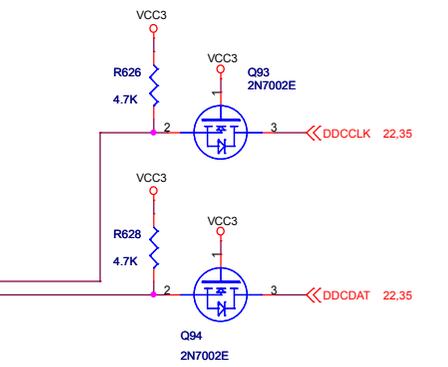
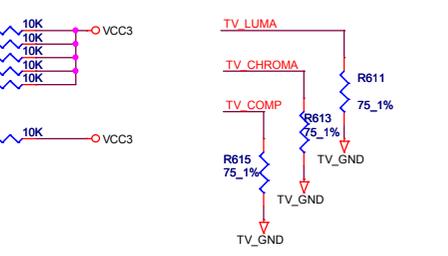




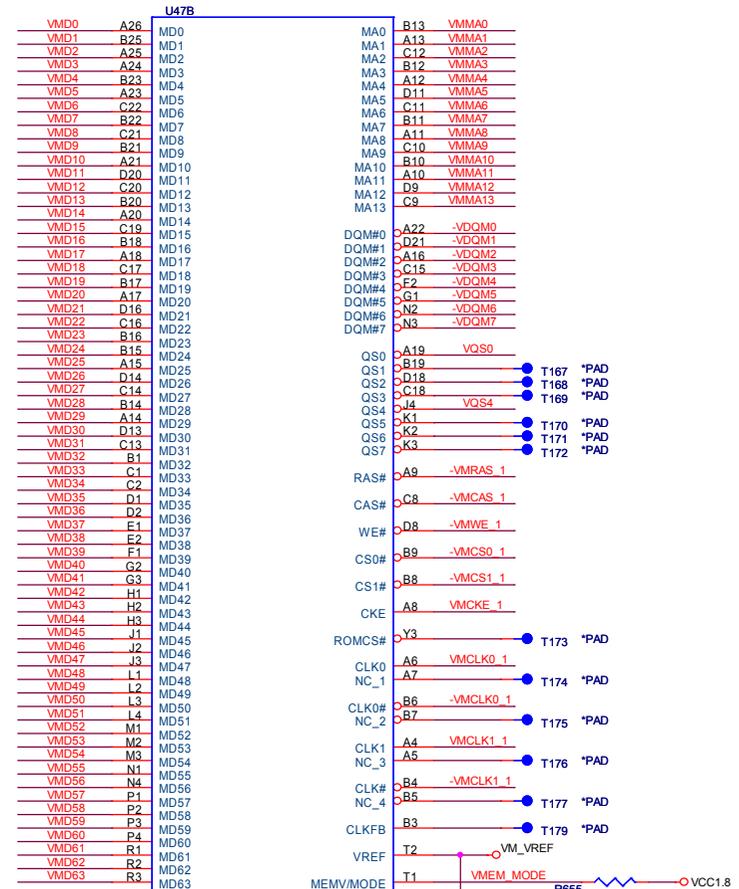
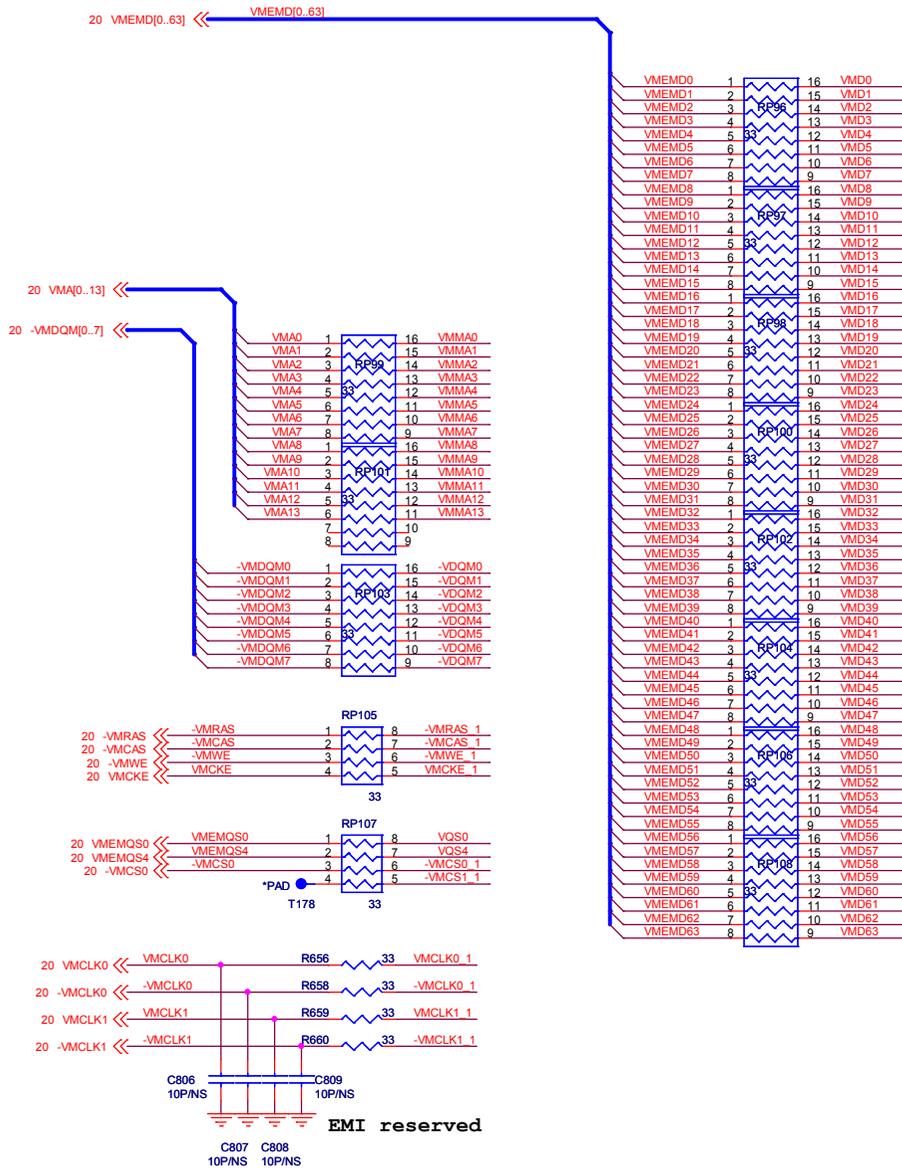


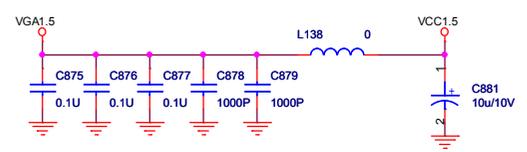
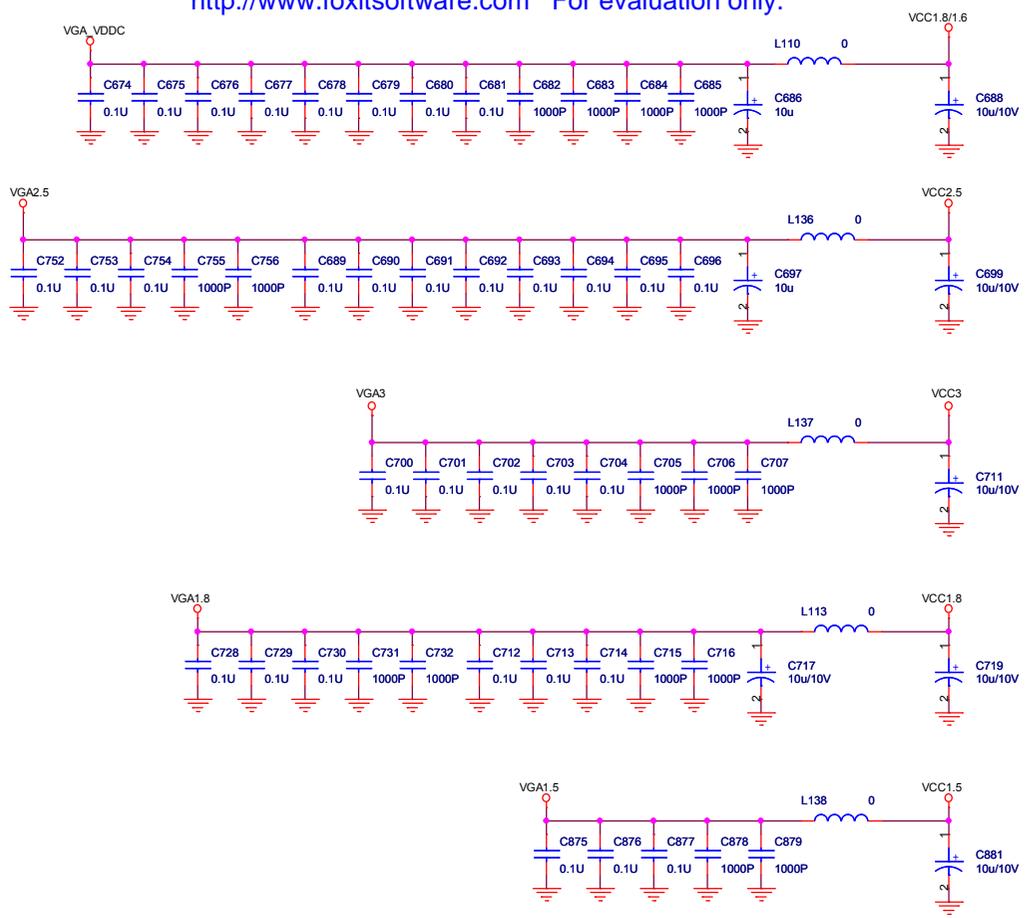
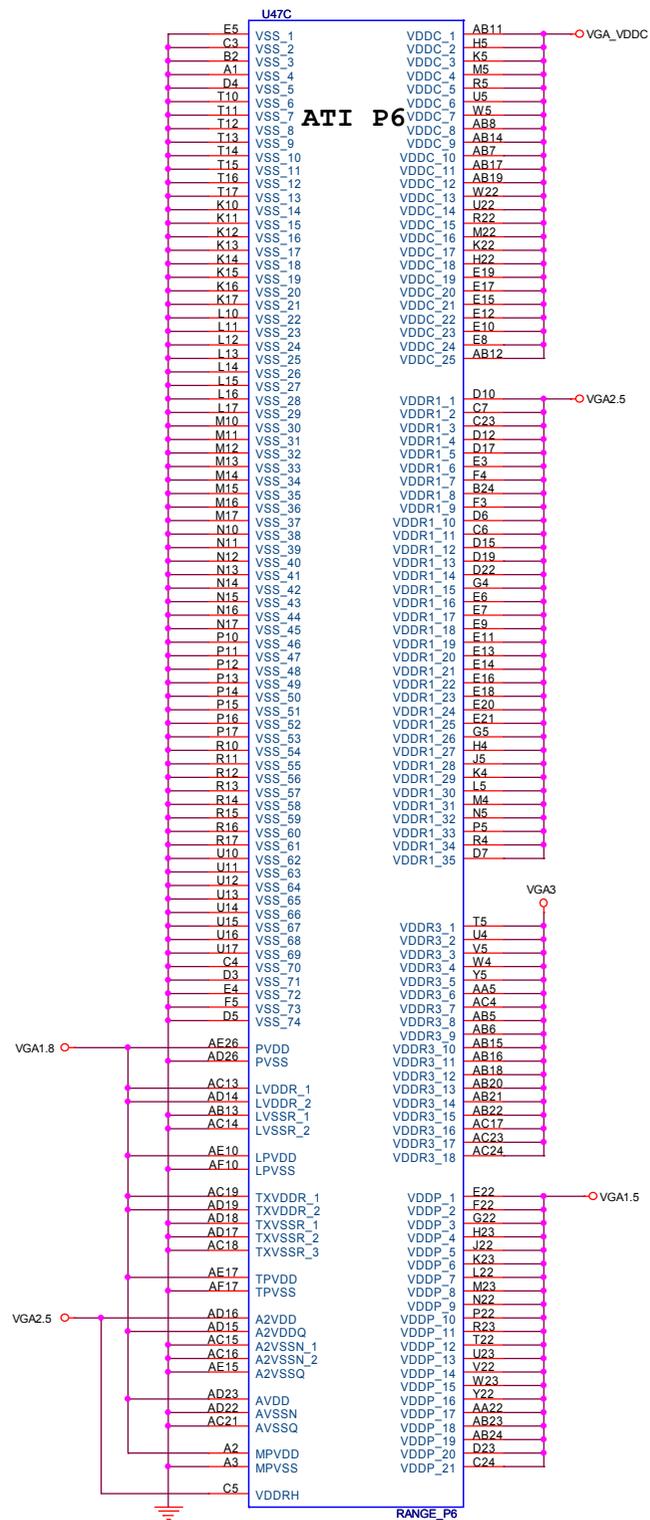


(Optional)  
SHFCLK spread spectrum control



ATI P6





# 8/16/32MB DDR 2/4MX32 SDRAM

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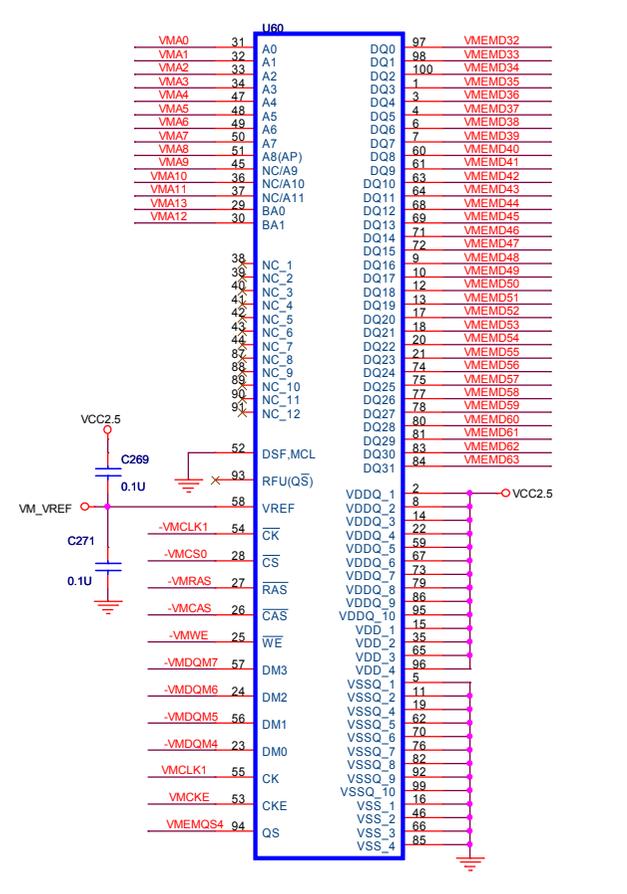
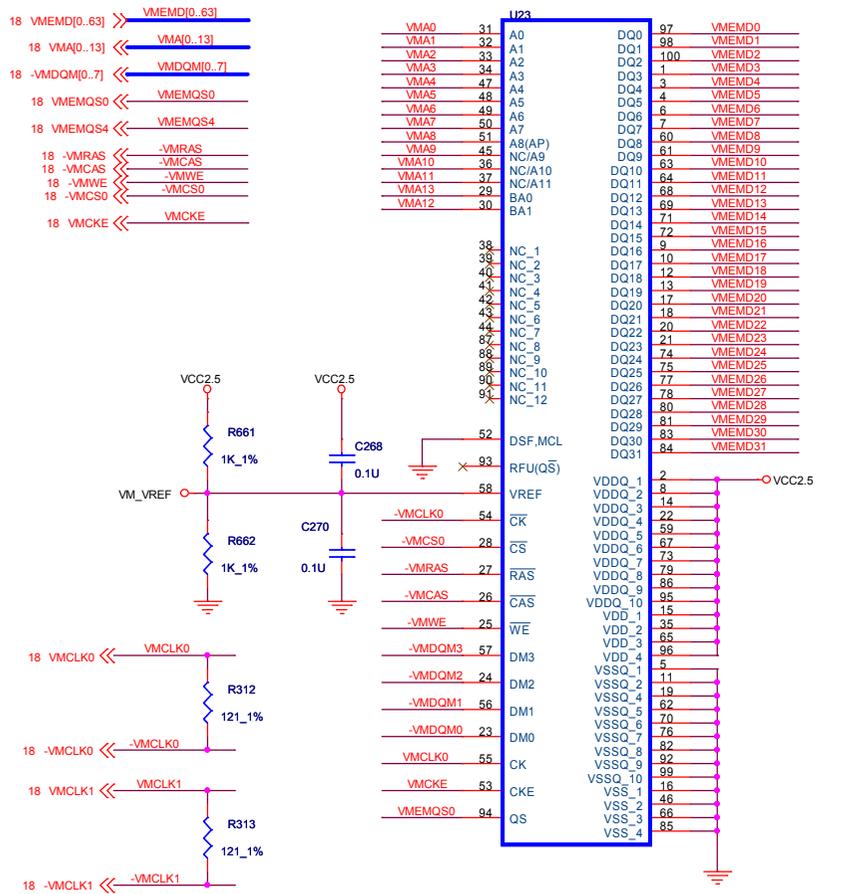
8MB 32 BIT INTERFACE WITH ONE PIECE 2MX32 (U23)

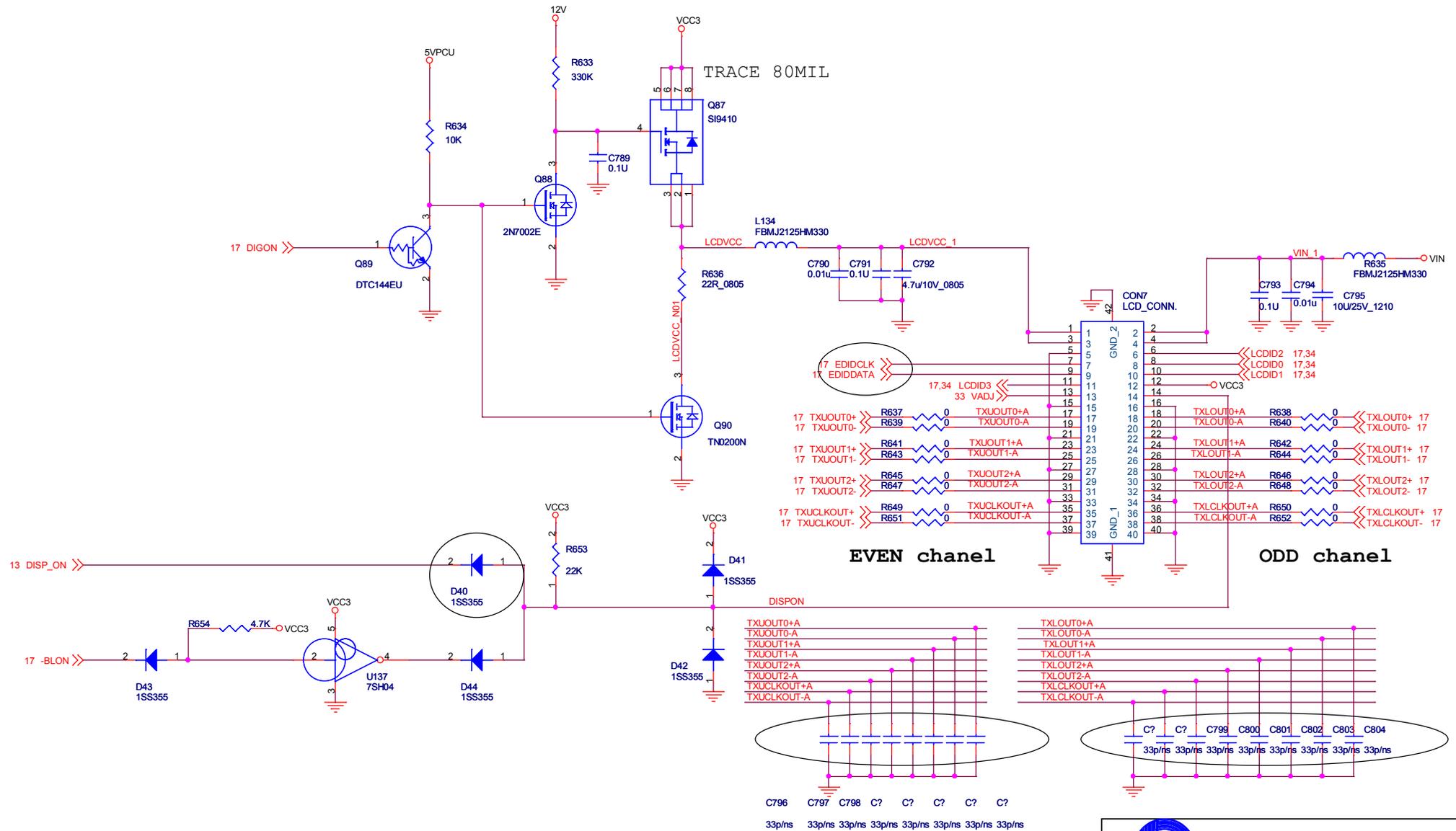
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8MB 64 BIT INTERFACE WITH TWO PIECES 2MX32 (U23,U60)

16MB 32 BIT INTERFACE WITH ONE PIECE 4MX32 (U23)

32MB 64 BIT INTERFACE WITH TWO PIECES 4MX32 (U23,U60)

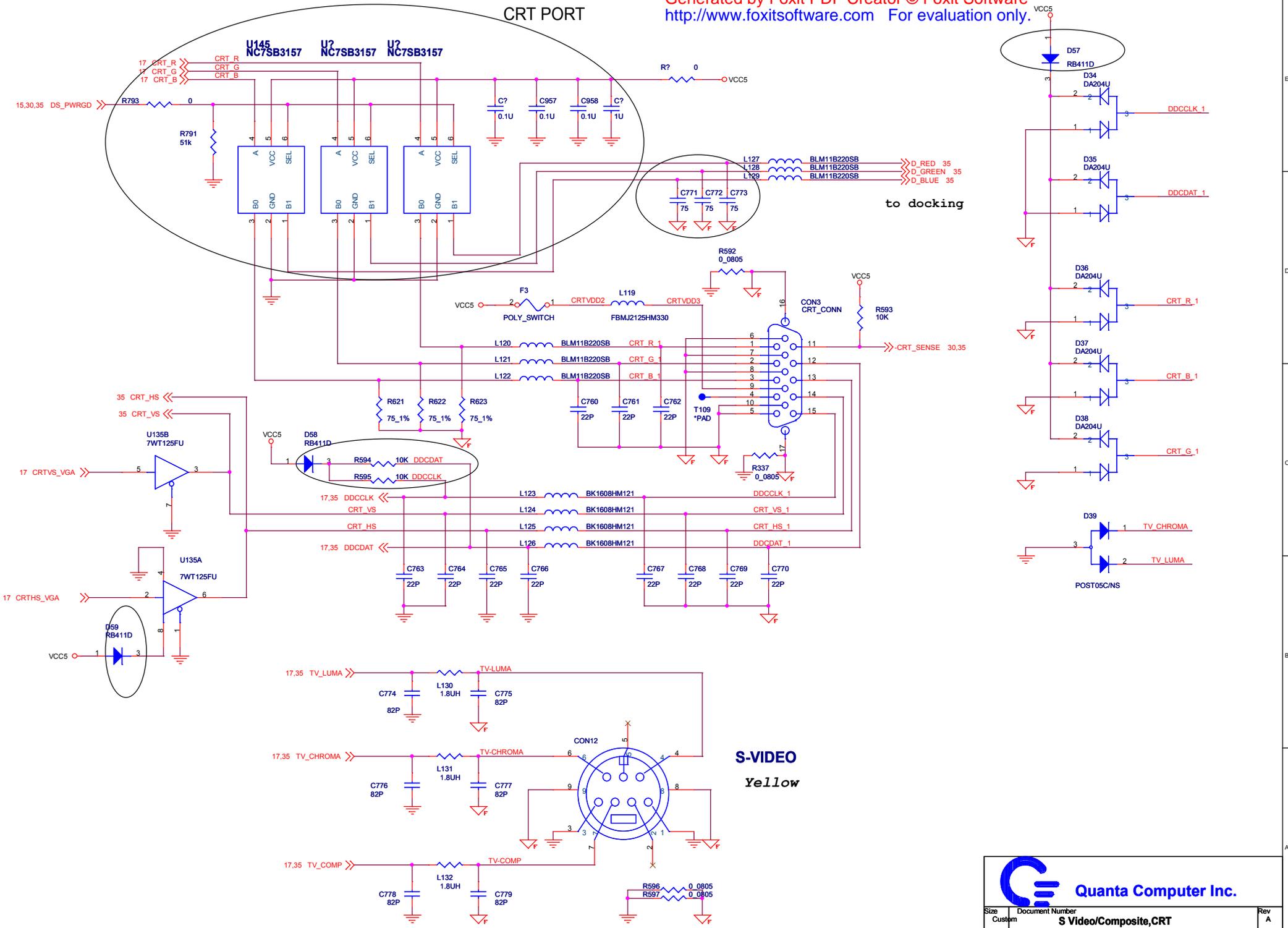




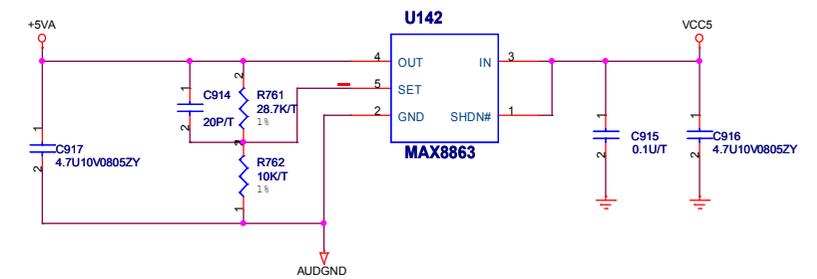
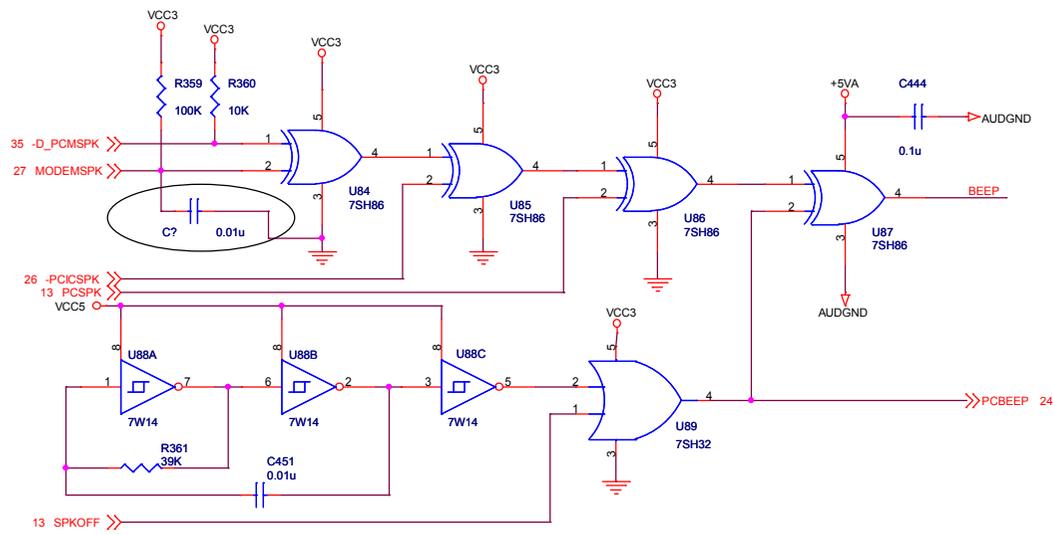
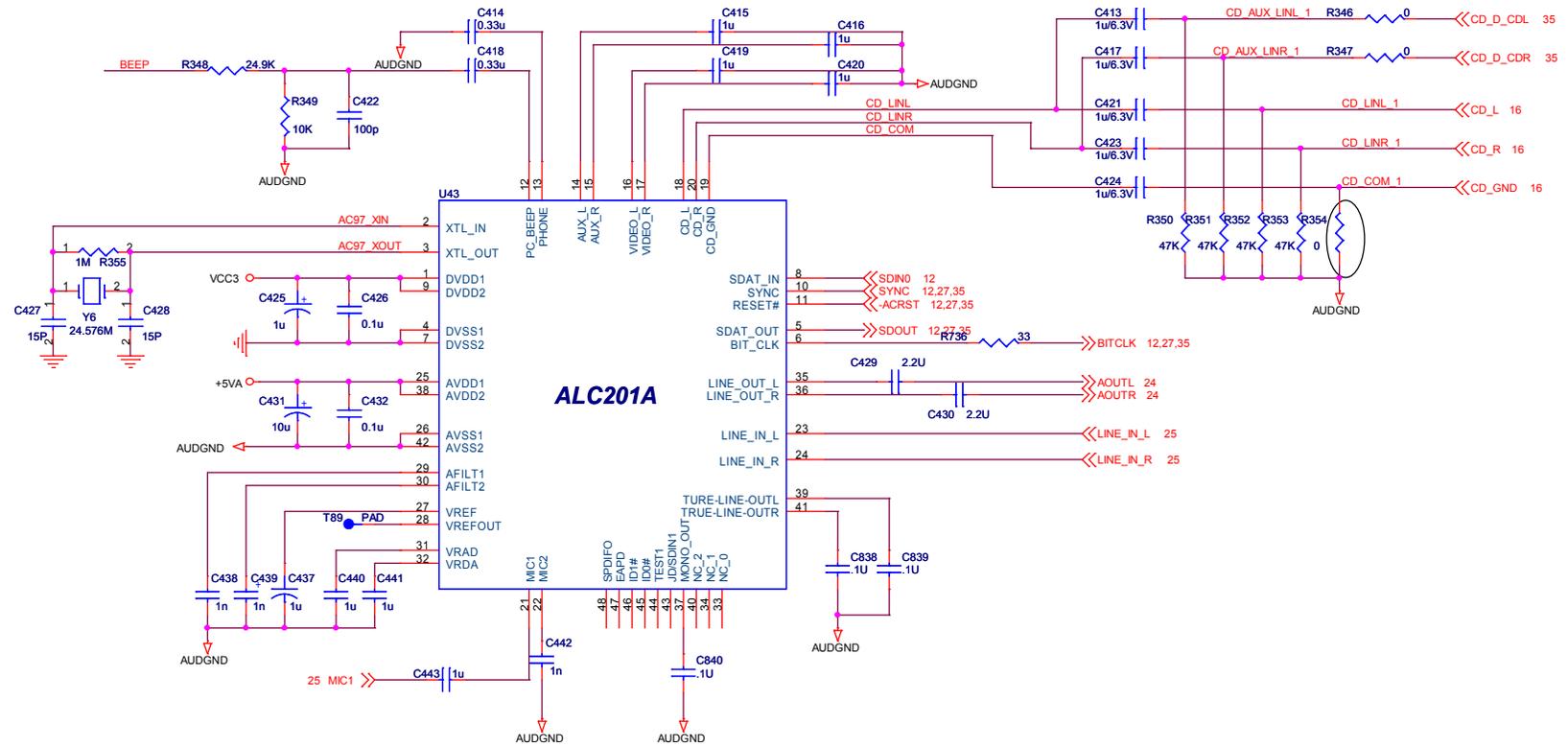
**Quanta Computer Inc.**

Size B	Document Number <b>LCD interface</b>	Rev A
Date:	Thursday, July 18, 2002	Sheet 21 of 41

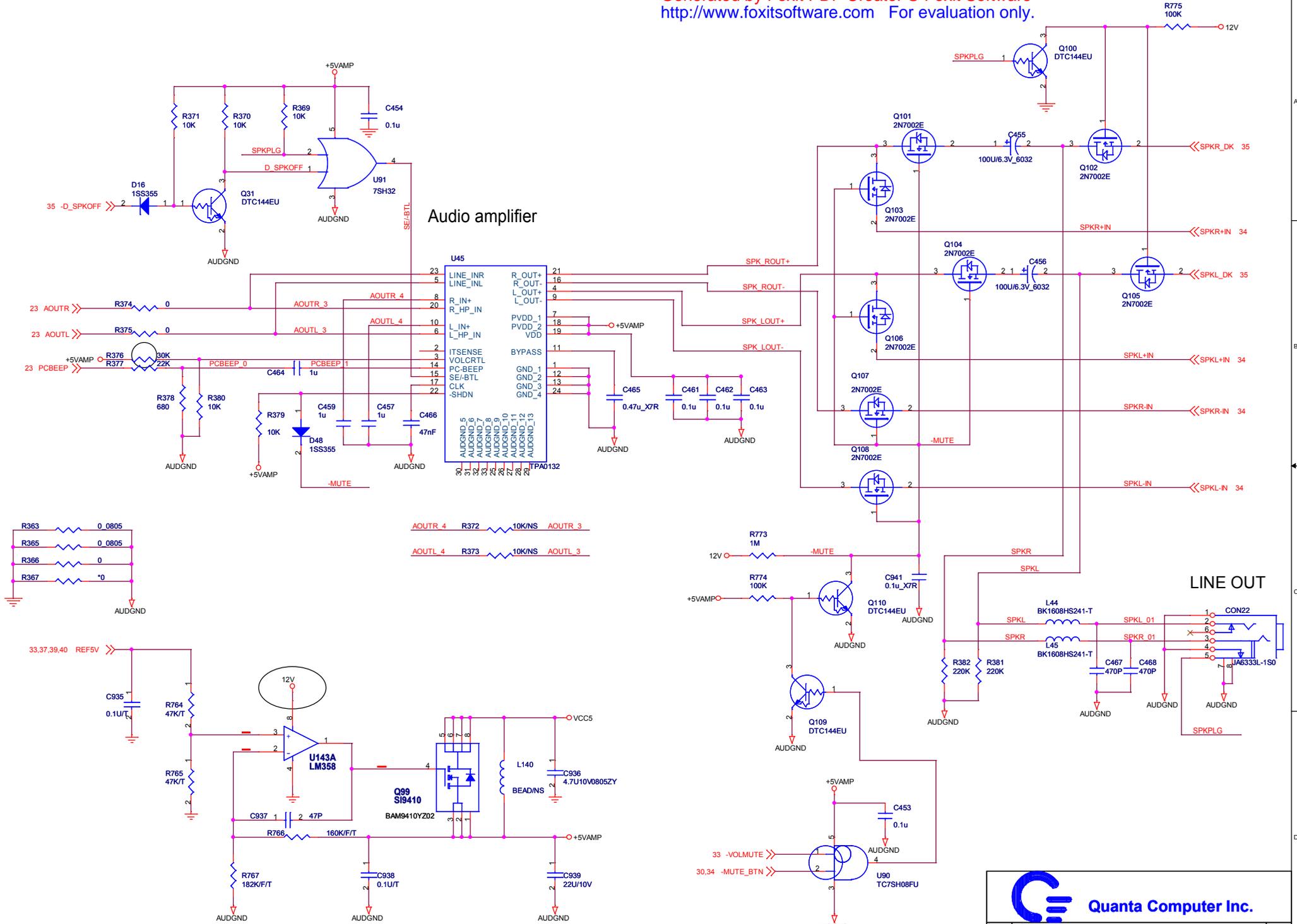
CRT PORT

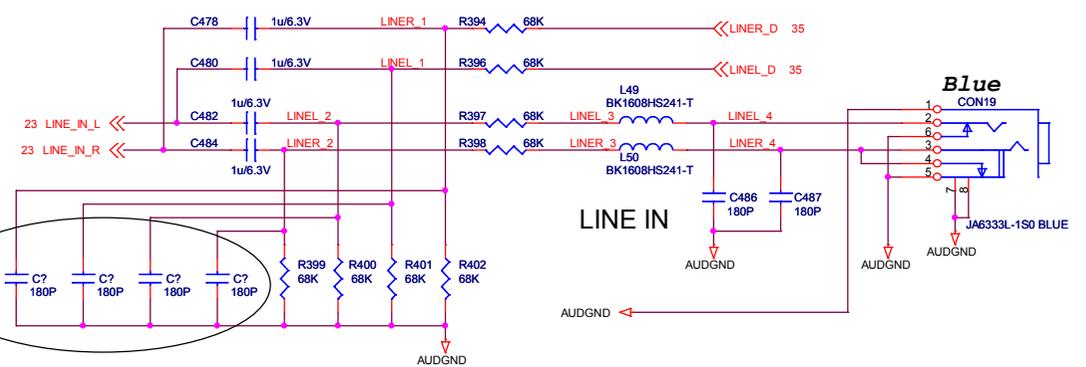
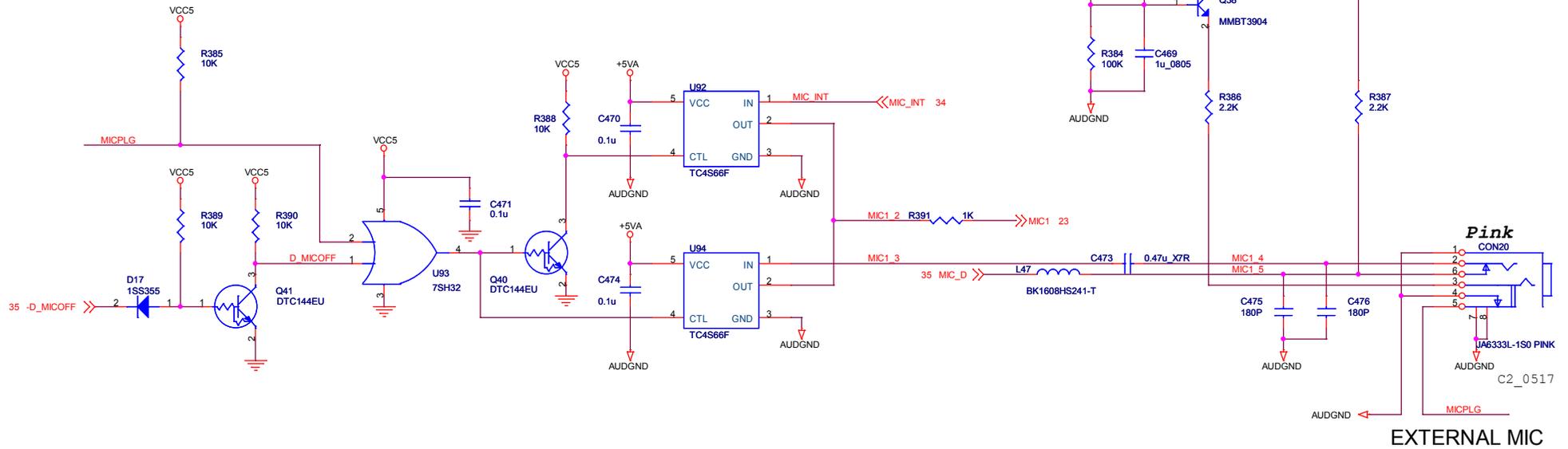


**S-VIDEO**  
**Yellow**

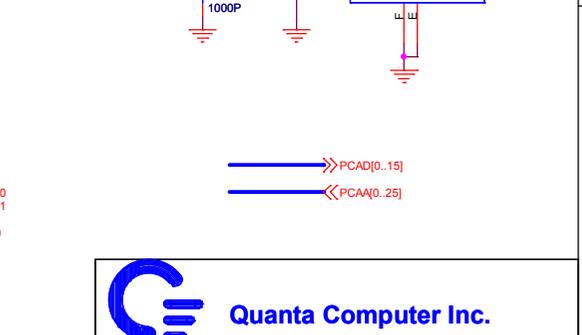
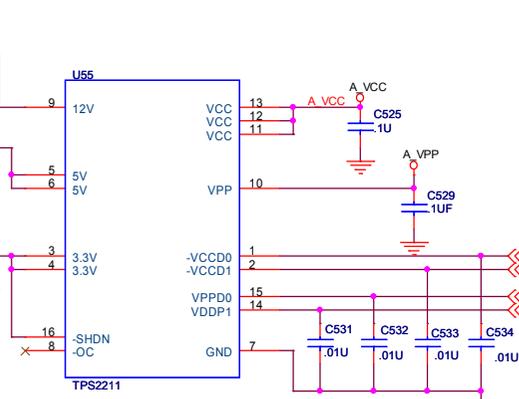
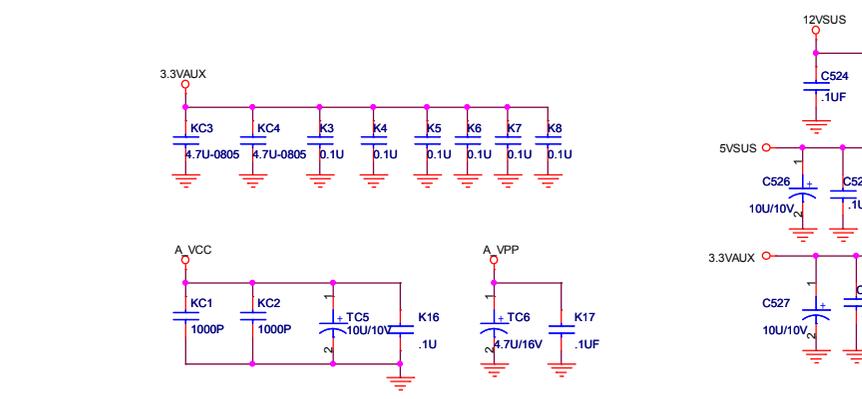
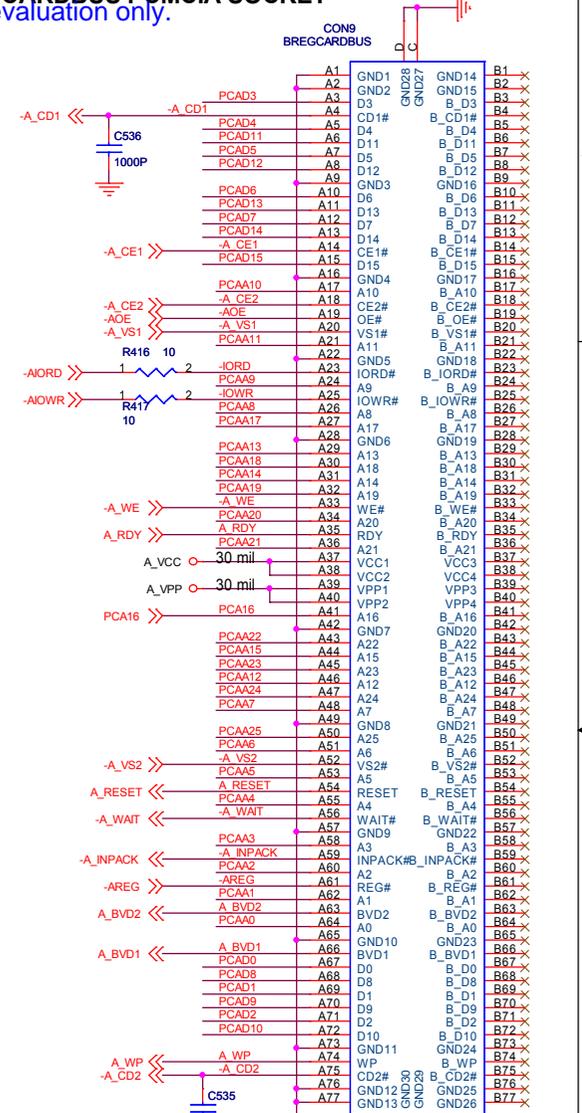
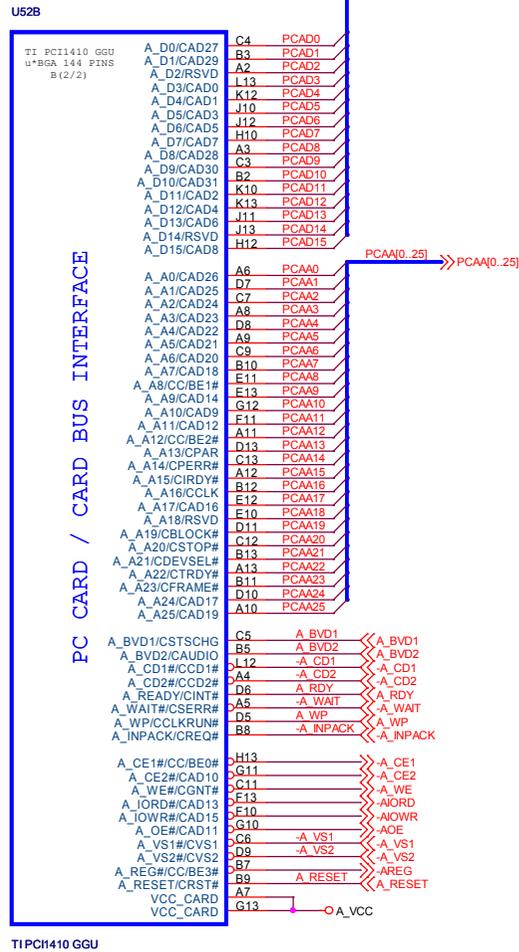
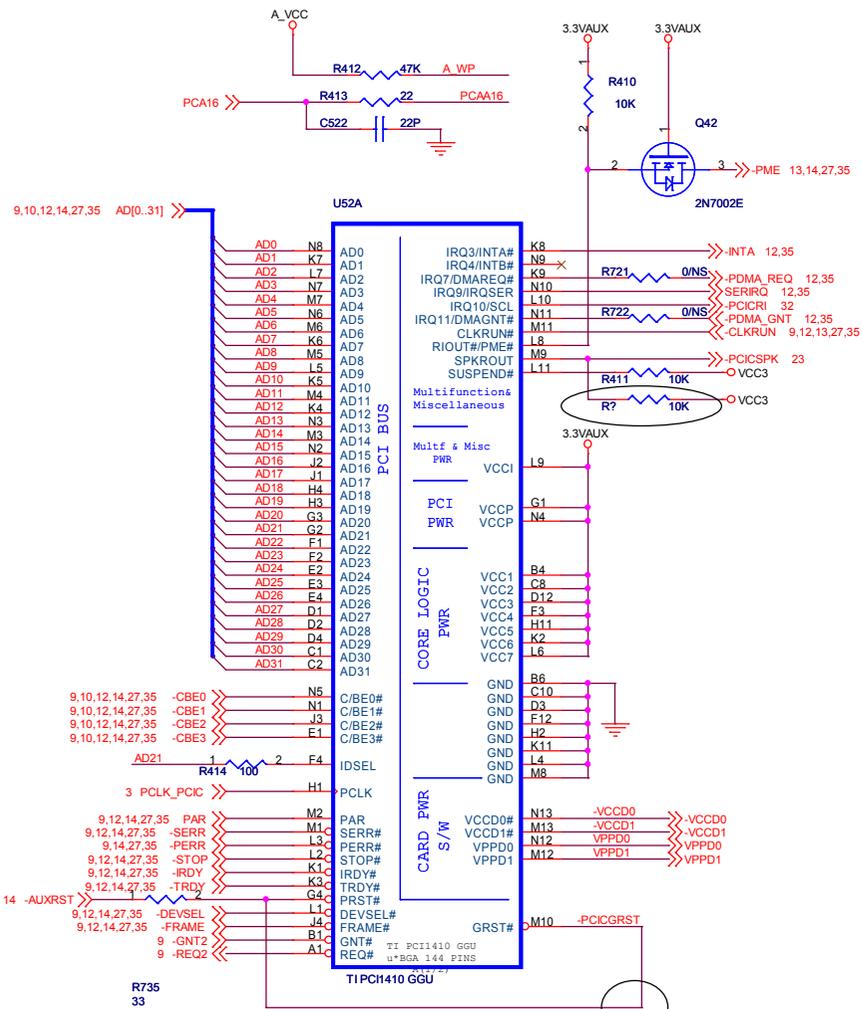


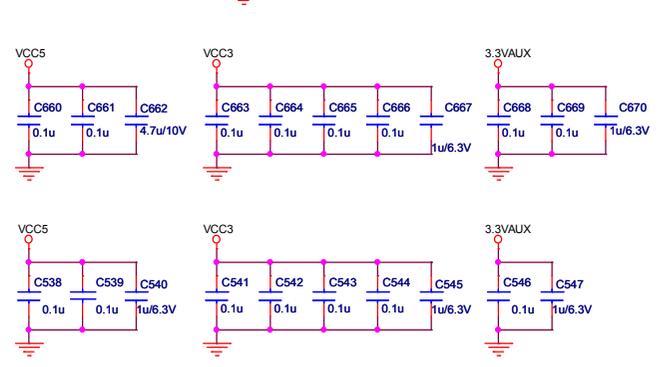
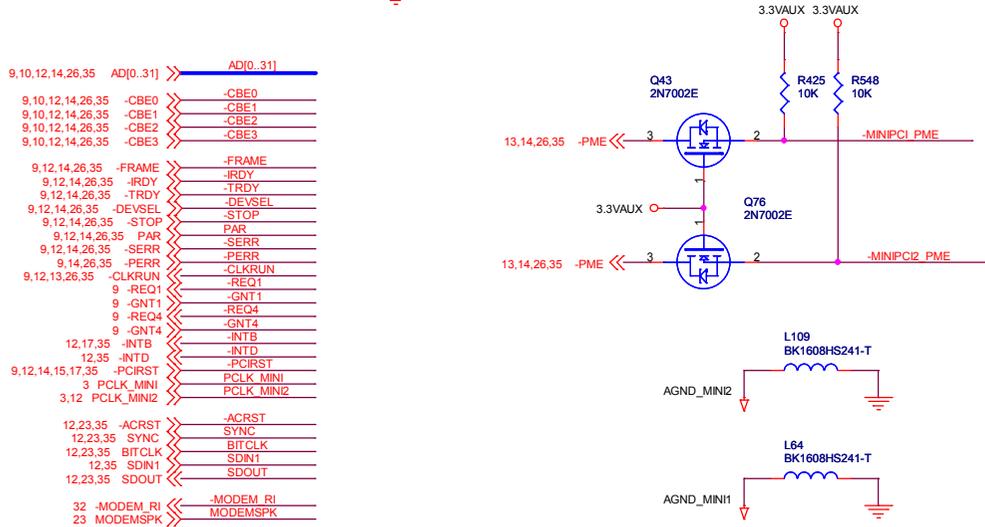
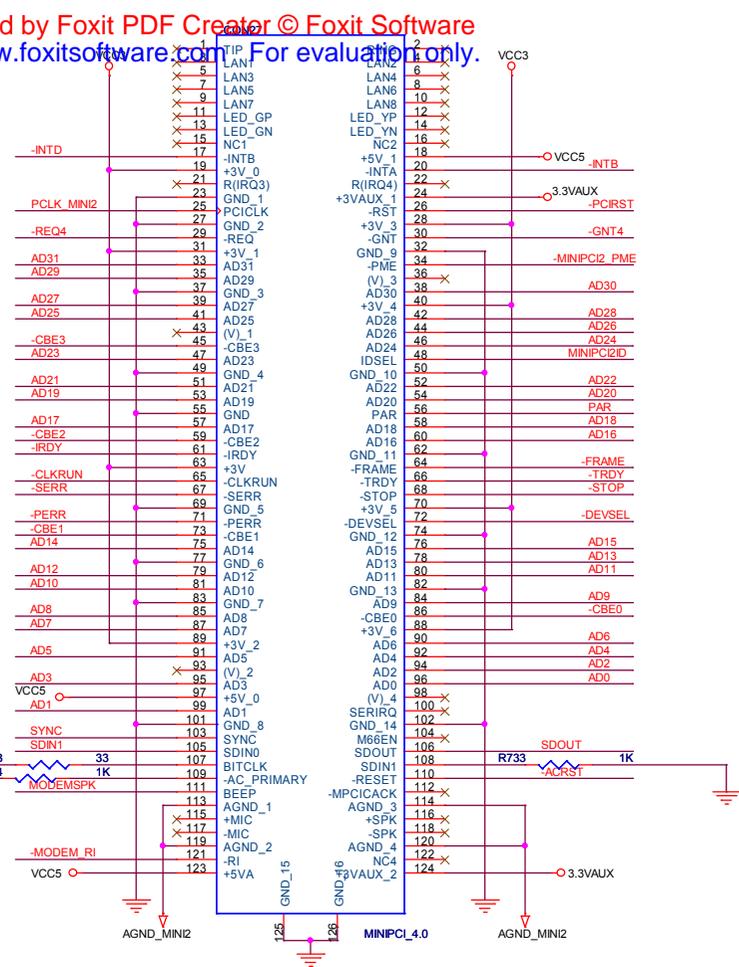
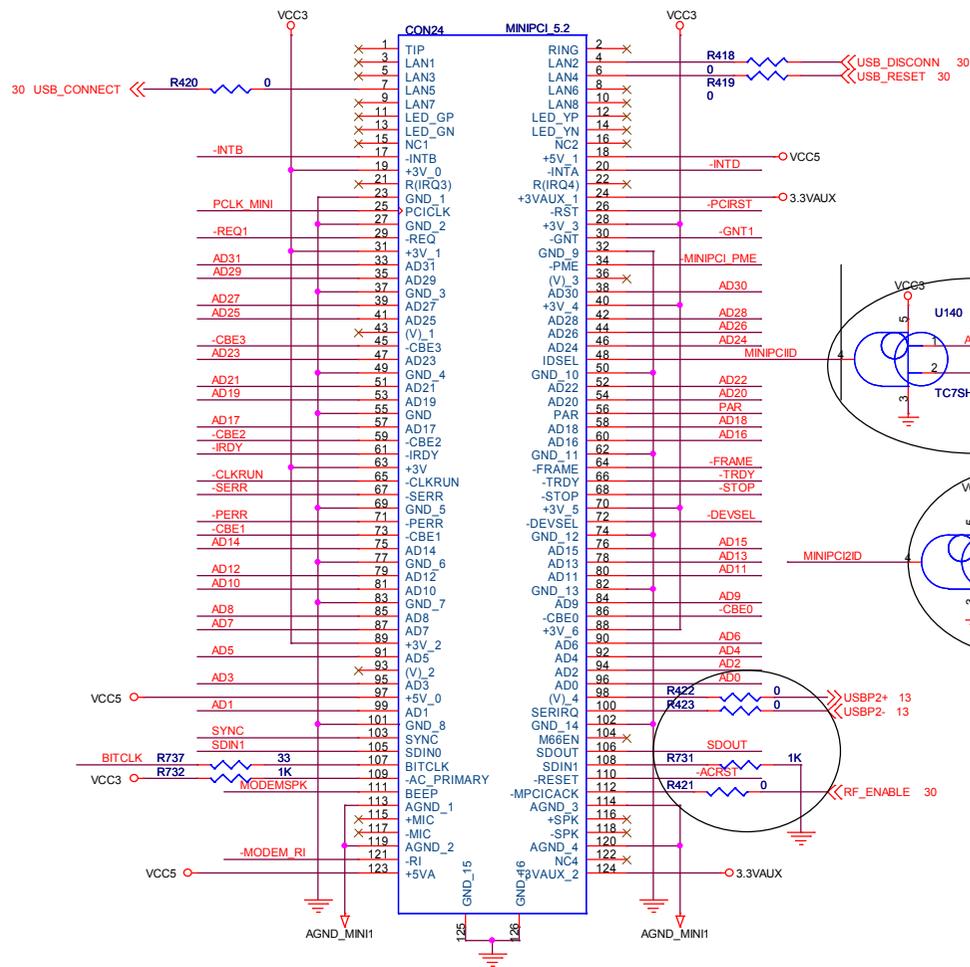
Audio amplifier





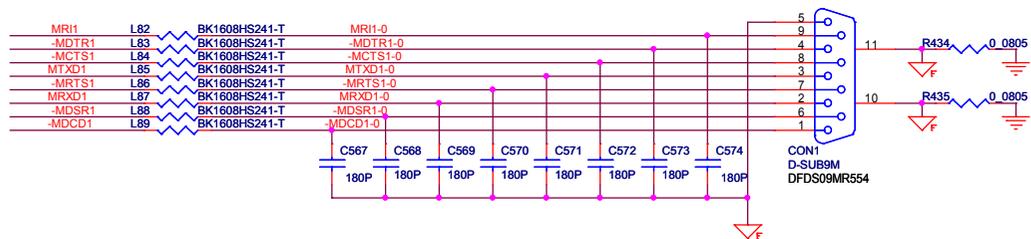
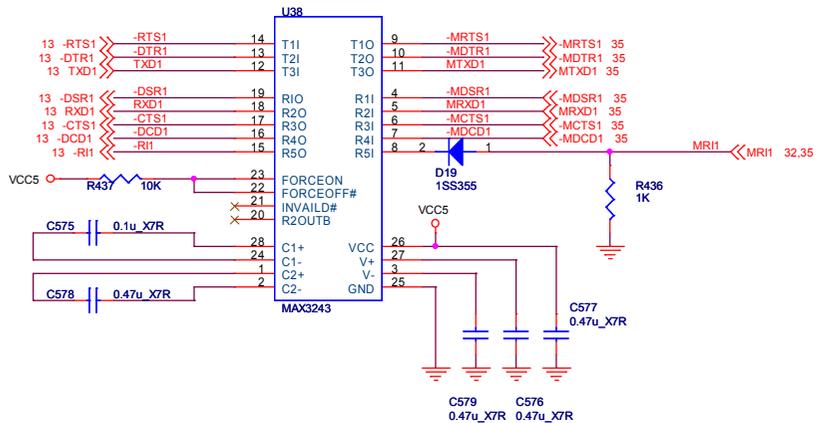
**CARDBUS PCMCIA SOCKET**



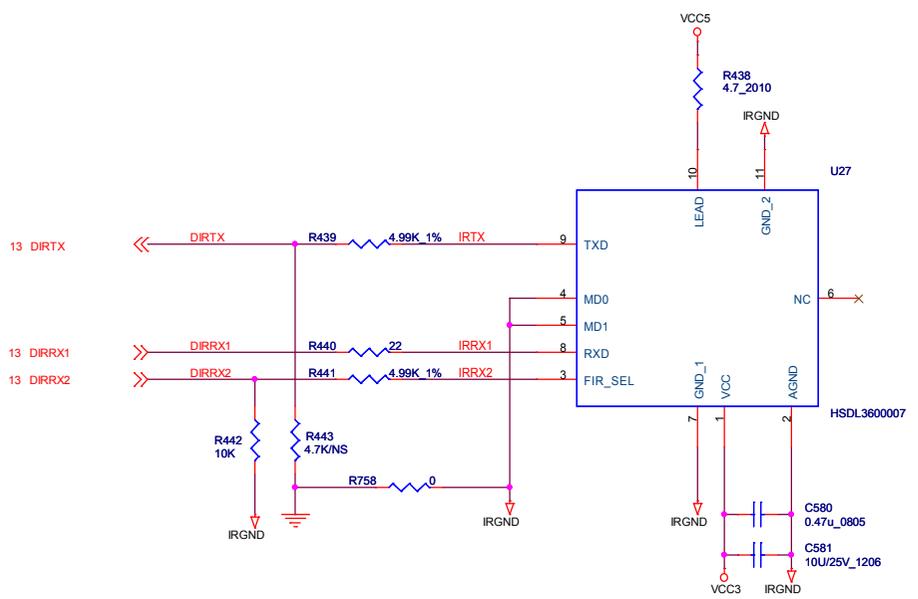


- 9,10,12,14,26,35 AD[0..31] >> AD[0..31]
- 9,10,12,14,26,35 -CBE0 >> -CBE0
- 9,10,12,14,26,35 -CBE1 >> -CBE1
- 9,10,12,14,26,35 -CBE2 >> -CBE2
- 9,10,12,14,26,35 -CBE3 >> -CBE3
- 9,12,14,26,35 -FRAME >> -FRAME
- 9,12,14,26,35 -JRDY >> -JRDY
- 9,12,14,26,35 -TRDY >> -TRDY
- 9,12,14,26,35 -DEVSEL >> -DEVSEL
- 9,12,14,26,35 -STOP >> -STOP
- 9,12,14,26,35 -PAR >> -PAR
- 9,12,14,26,35 -SERR >> -SERR
- 9,12,14,26,35 -PERR >> -PERR
- 9,12,13,26,35 -CLKRUN >> -CLKRUN
- 9 -REQ1 >> -REQ1
- 9 -GNT1 >> -GNT1
- 9 -REQ4 >> -REQ4
- 9 -GNT4 >> -GNT4
- 12,17,35 -INTB >> -INTB
- 12,35 -INTD >> -INTD
- 9,12,14,15,17,35 -PCIRST >> -PCIRST
- 3 PCLK\_MINI >> PCLK\_MINI
- 3,12 PCLK\_MINI2 >> PCLK\_MINI2
- 12,23,35 -ACRST >> -ACRST
- 12,23,35 SYNC >> SYNC
- 12,23,35 BITCLK >> BITCLK
- 12,35 SDIN1 >> SDIN1
- 12,23,35 SDOUT >> SDOUT
- 32 -MODEM\_RI >> -MODEM\_RI
- 23 MODEMSPK >> MODEMSPK

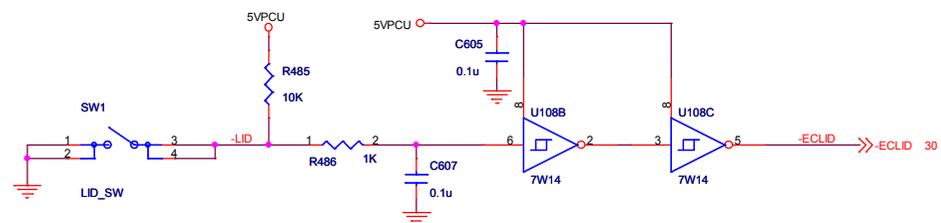
### COM PORT



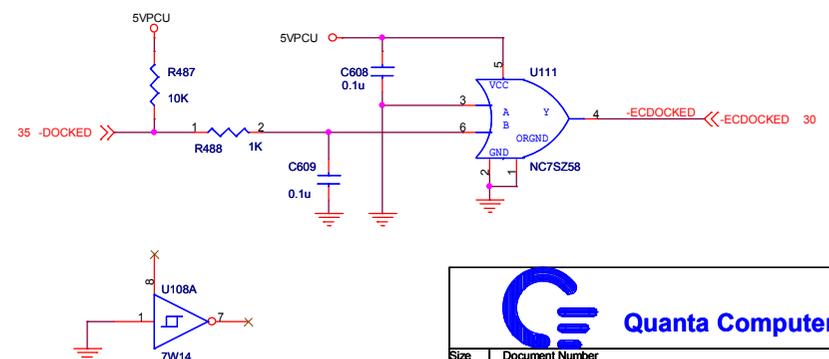
### FIR



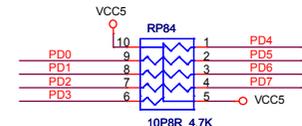
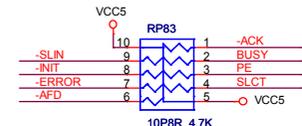
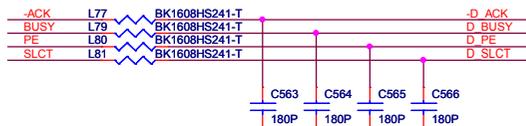
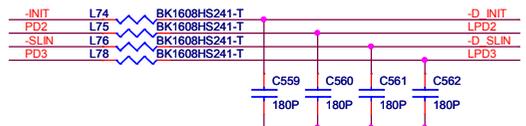
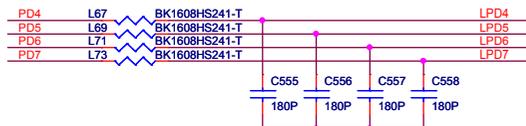
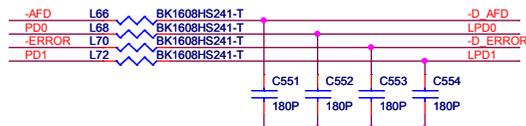
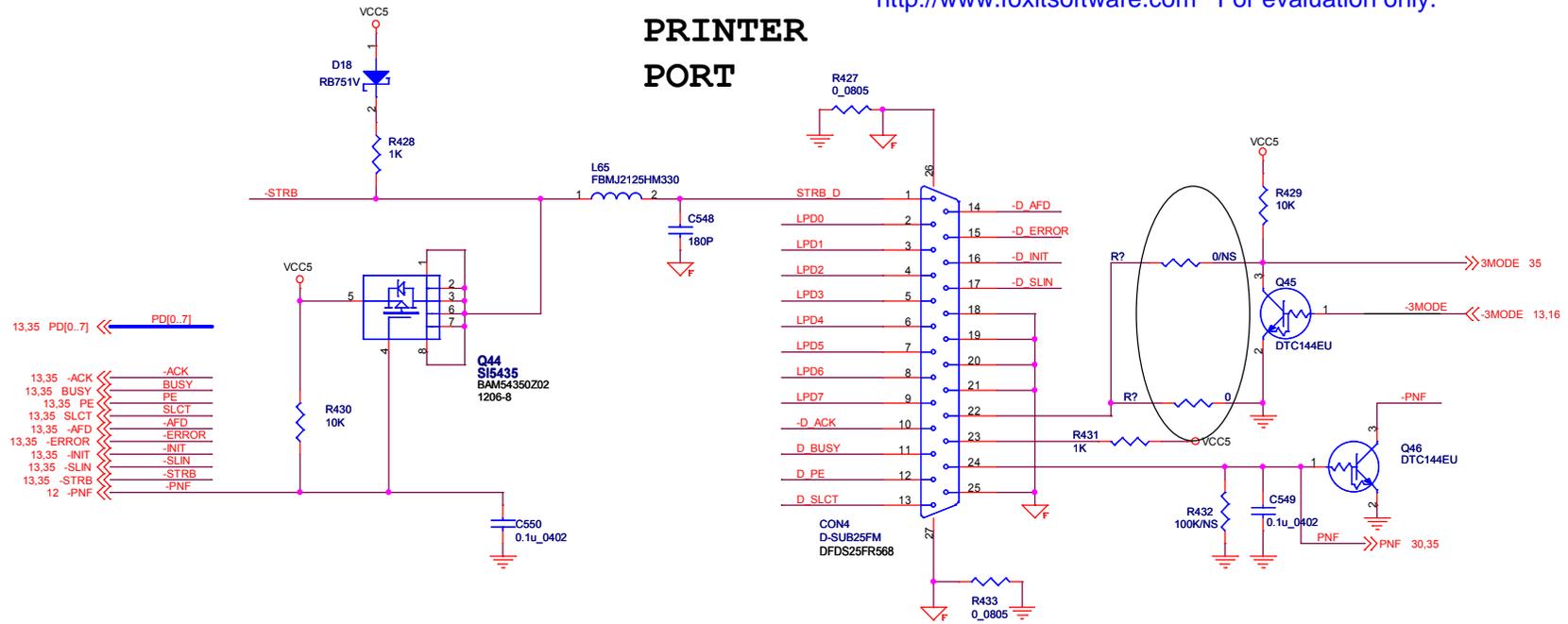
### LID SWITCH

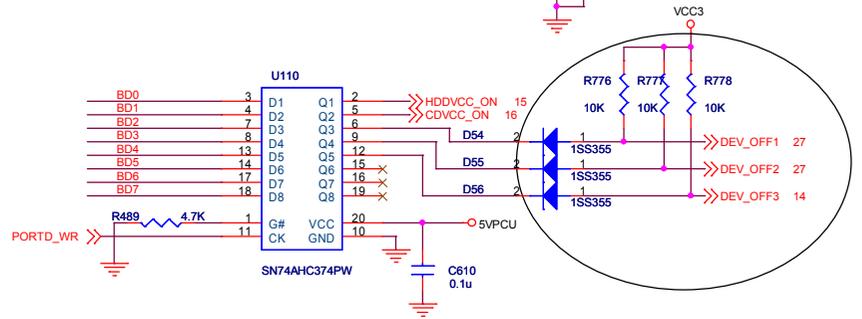
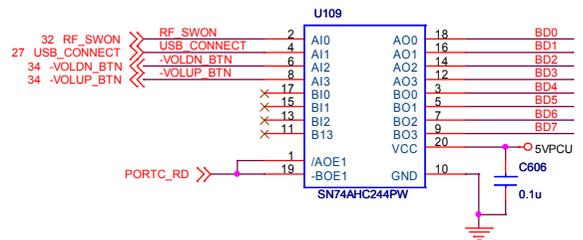
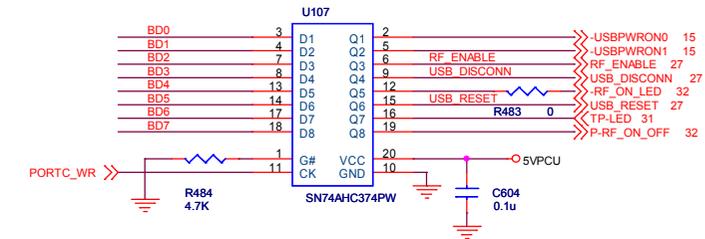
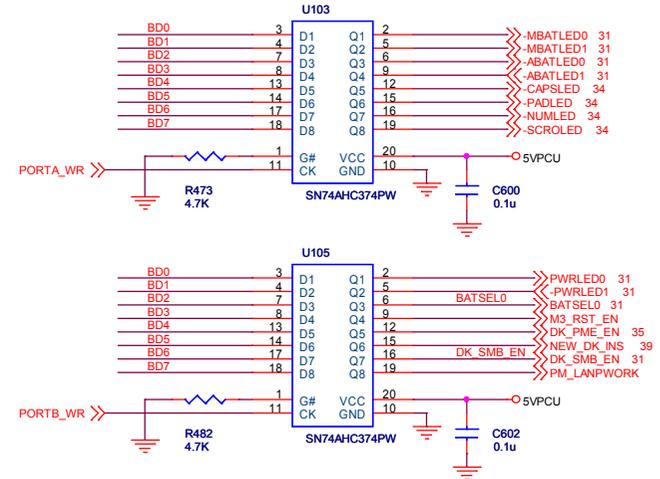
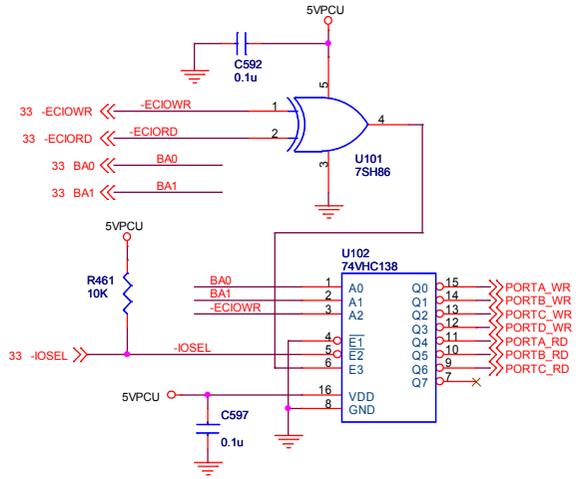
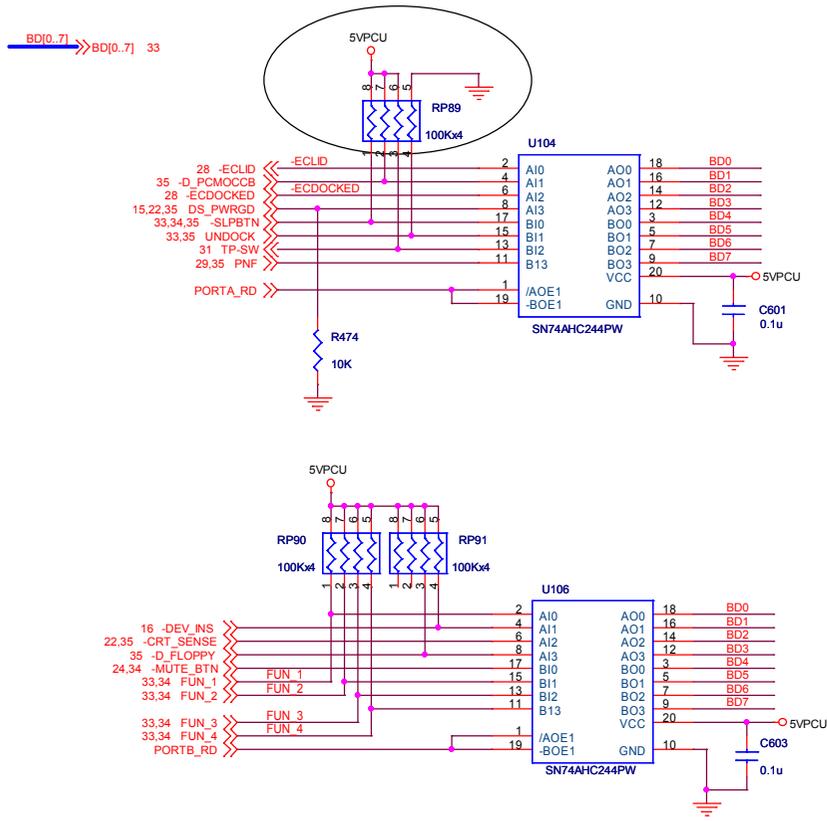


### DOCKED



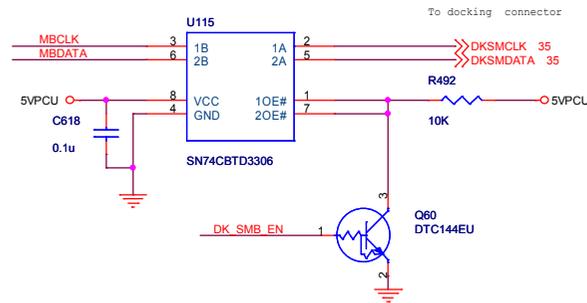
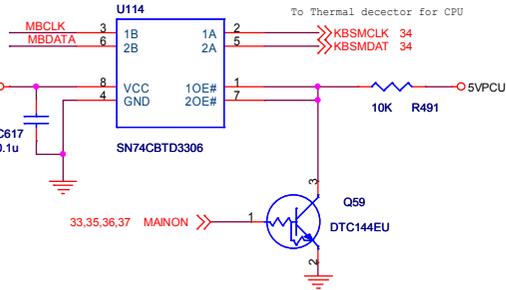
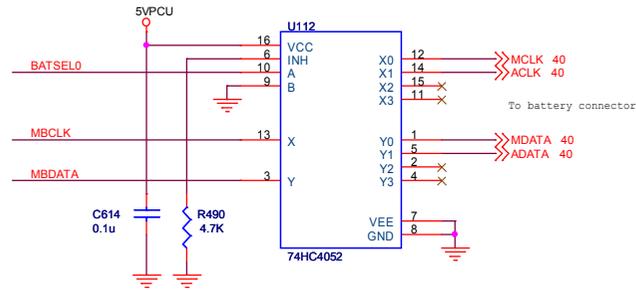
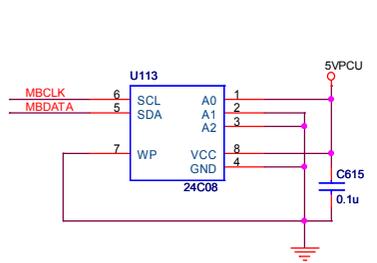
# PRINTER PORT



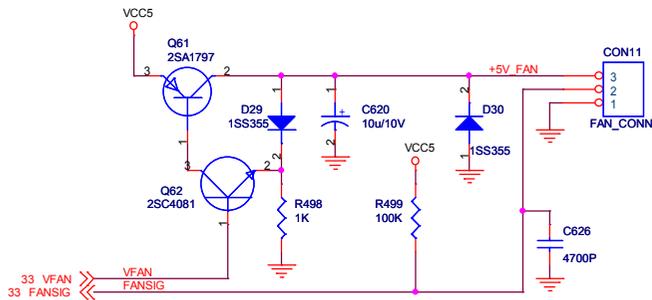


# SMBUS

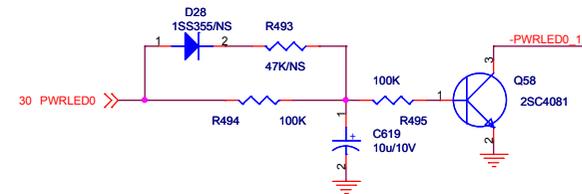
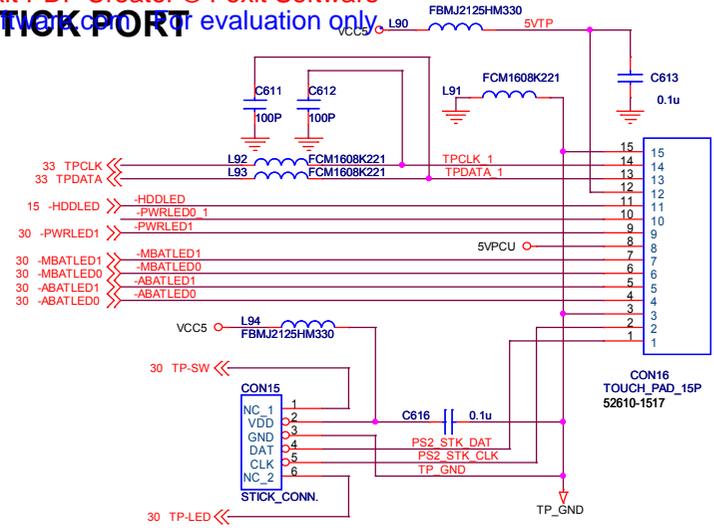
30 BATSELO <<< BATSELO  
 30 DK\_SMB\_EN <<< DK\_SMB\_EN  
 33 MBCLK <<< MBCLK  
 33 MBDATA <<< MBDATA



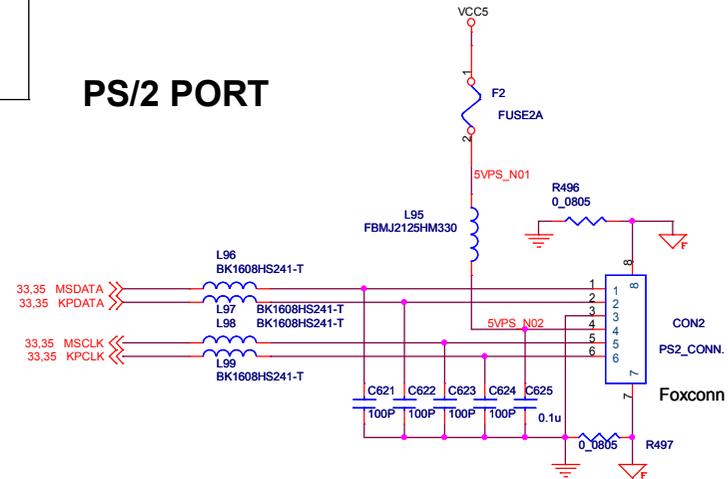
# FAN CONN.



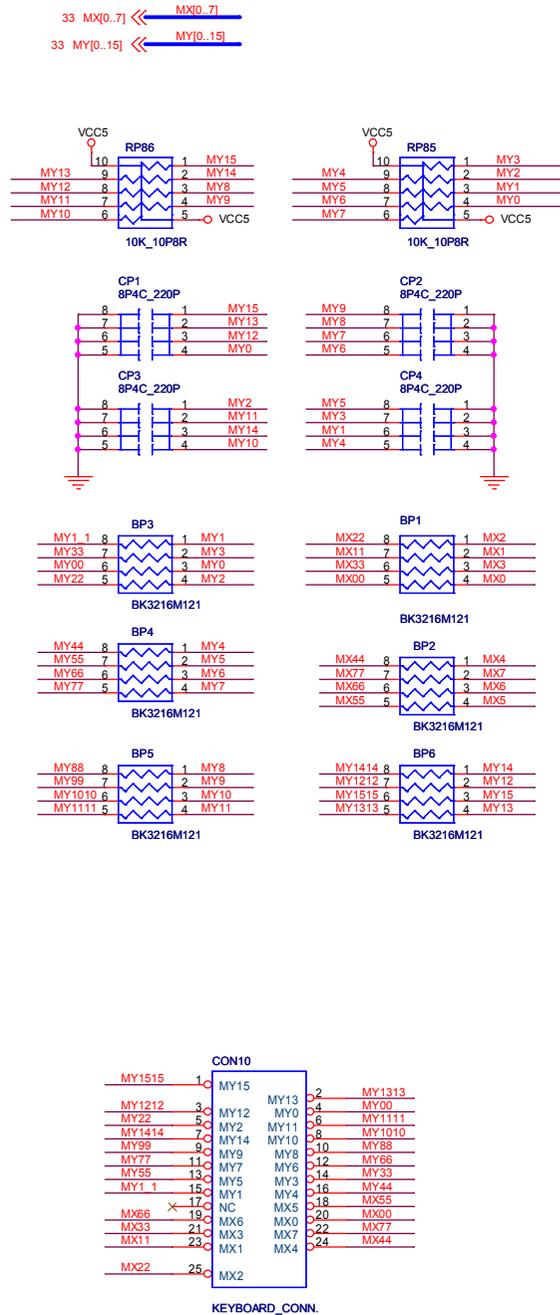
# STICK PORT



# PS/2 PORT

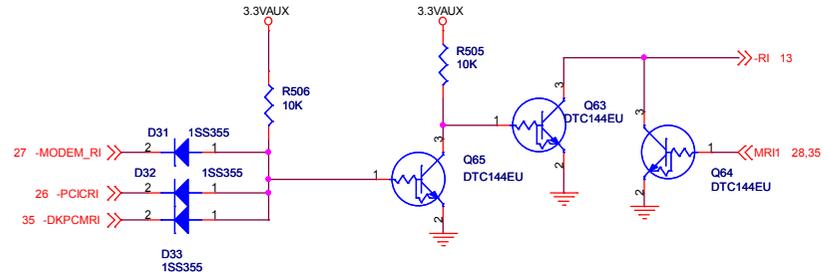


# Keyboard Connector

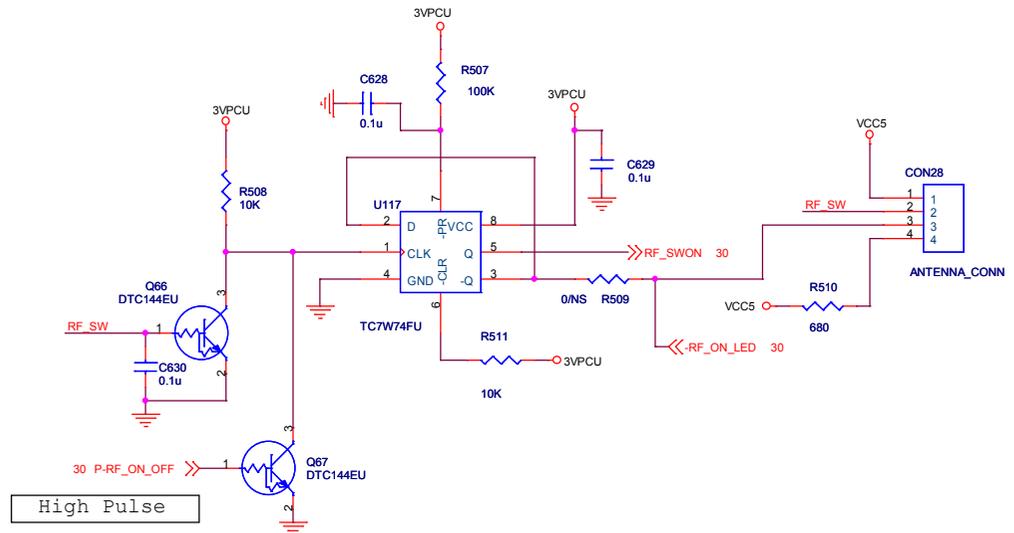


# RING INDICATOR

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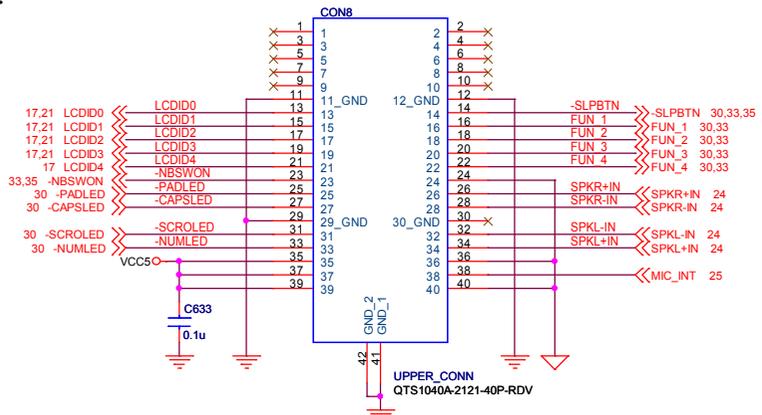


# RF S/W CKT

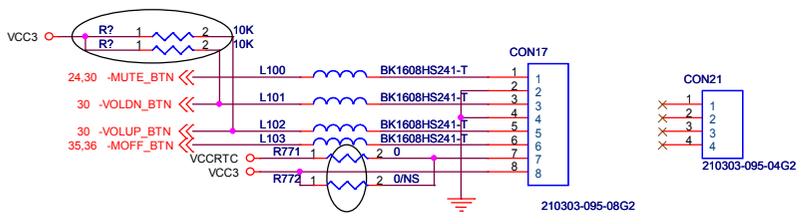




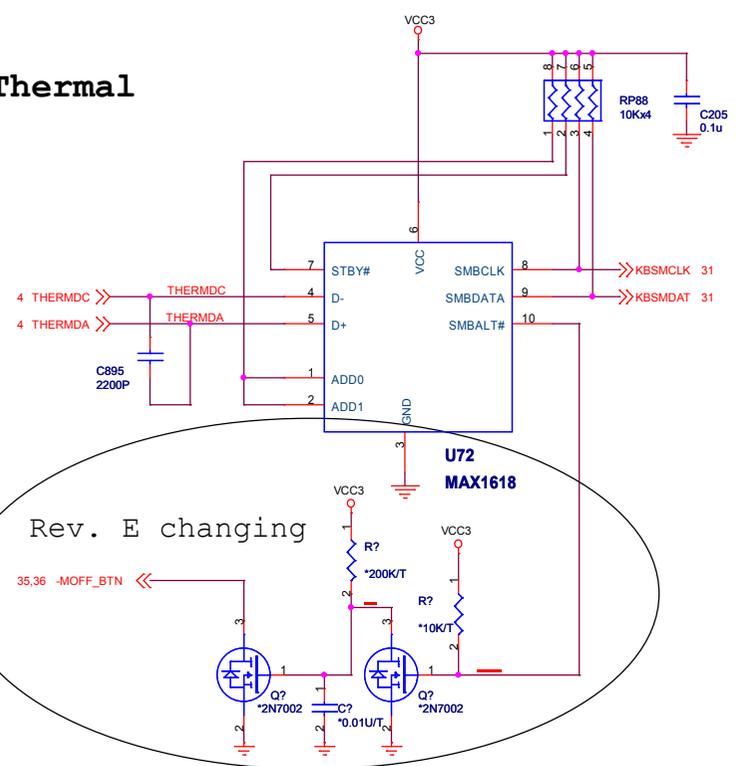
# UP\_BOARD CONNECTOR



# Volume board connector

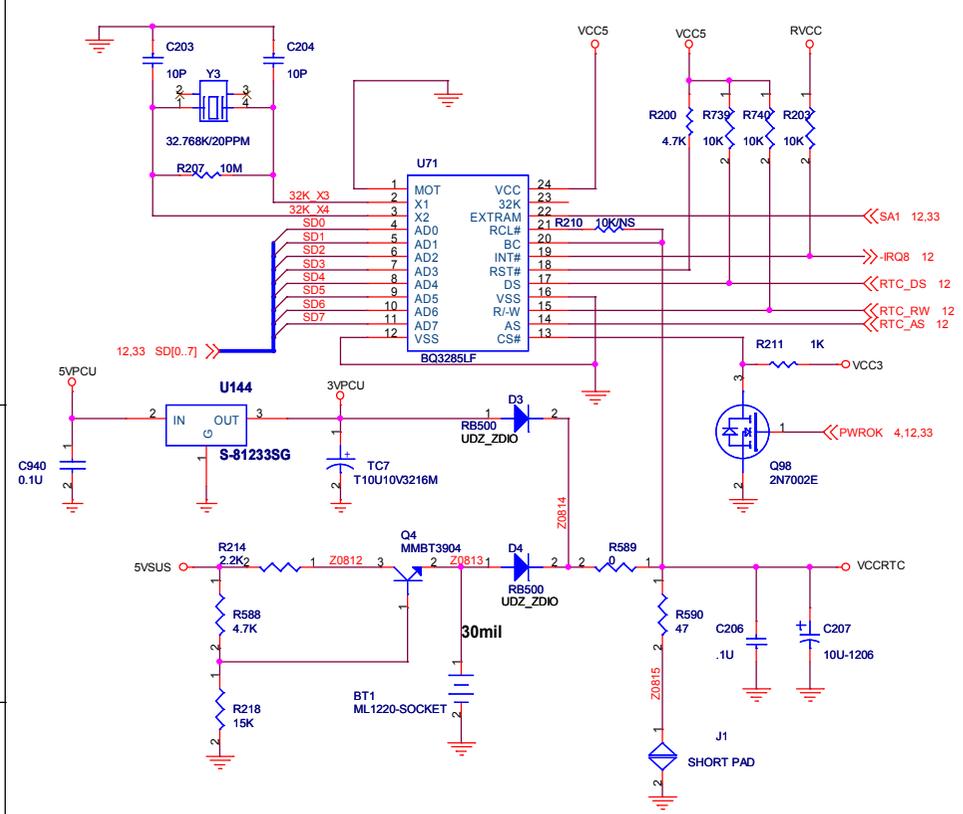


# Thermal

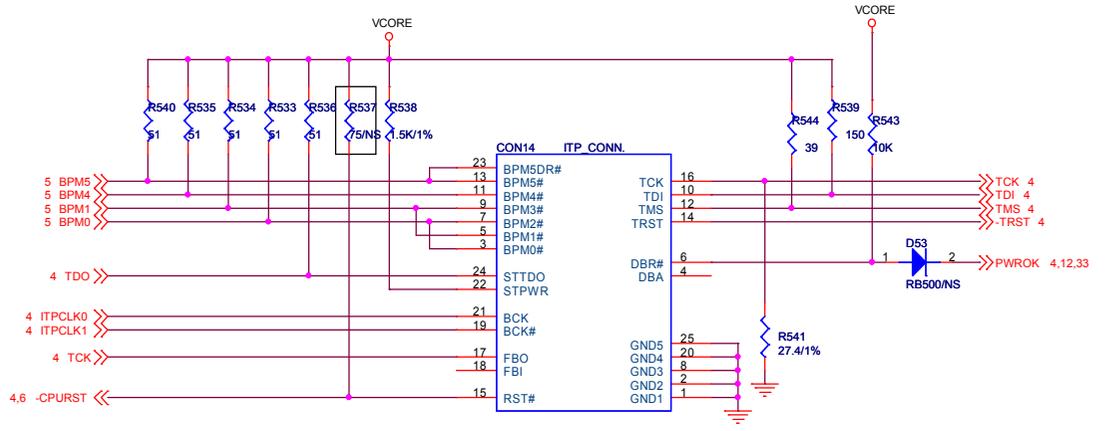


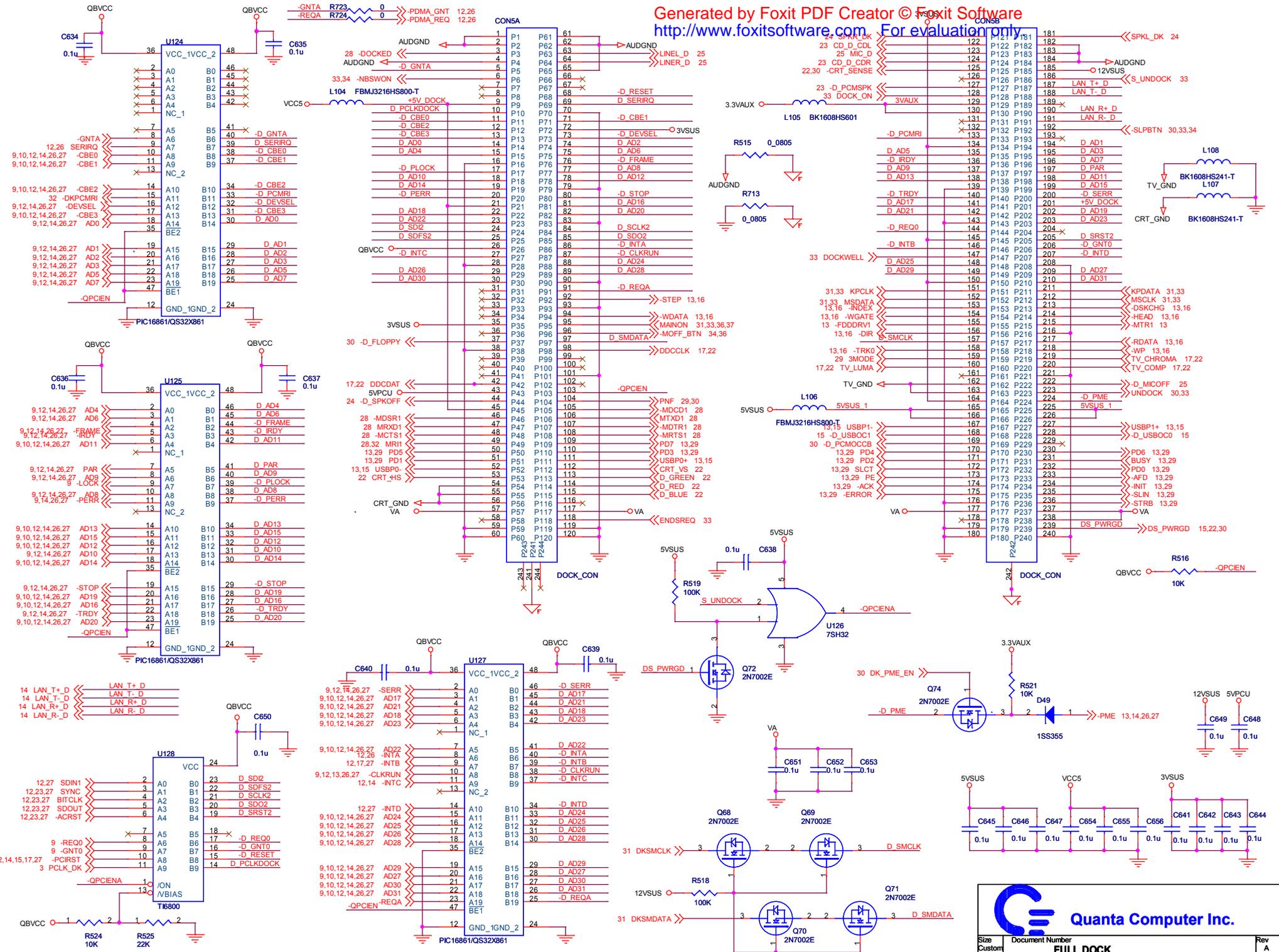
# RTC

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# ITP Connector





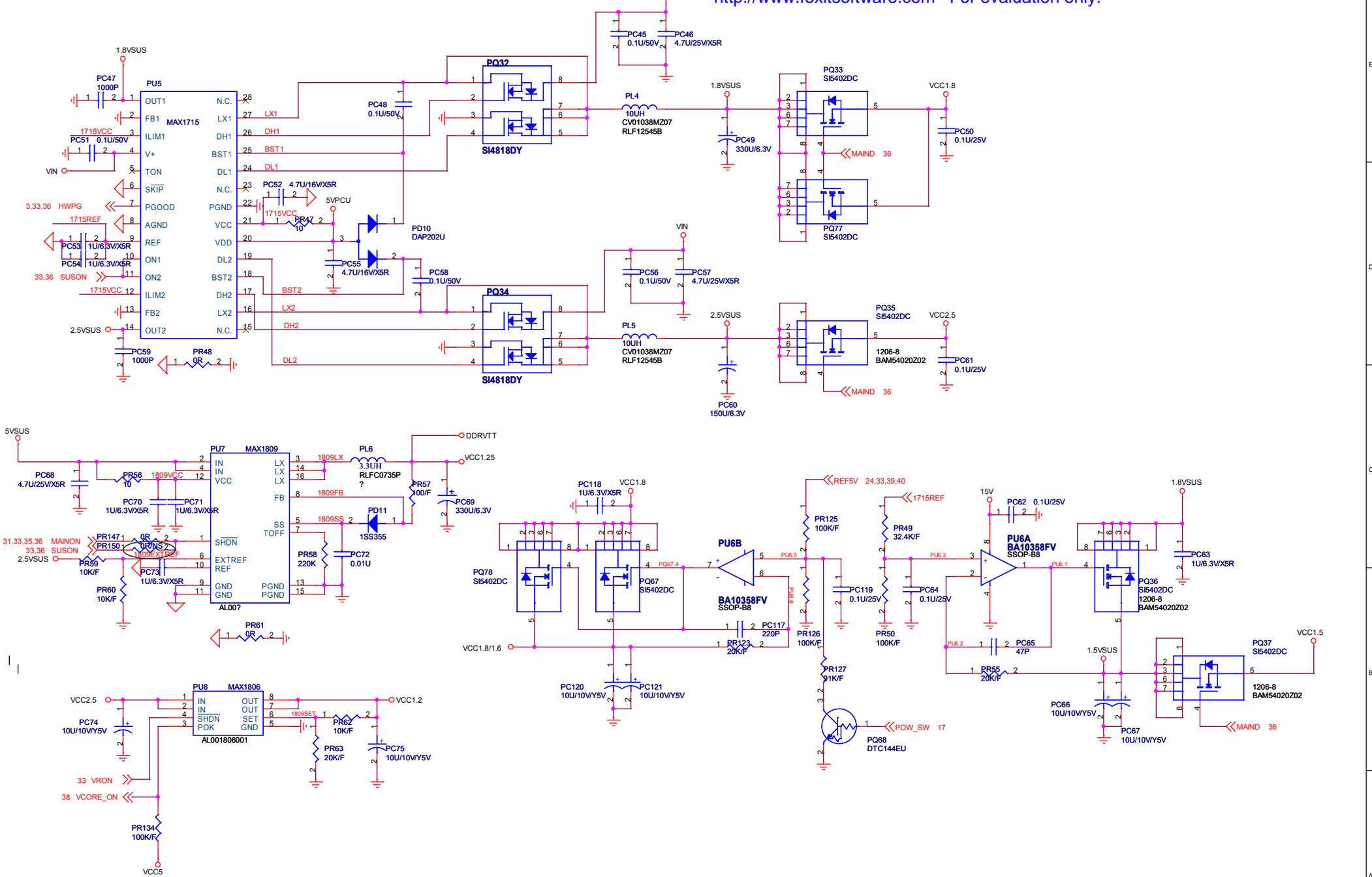
**Quanta Computer Inc.**

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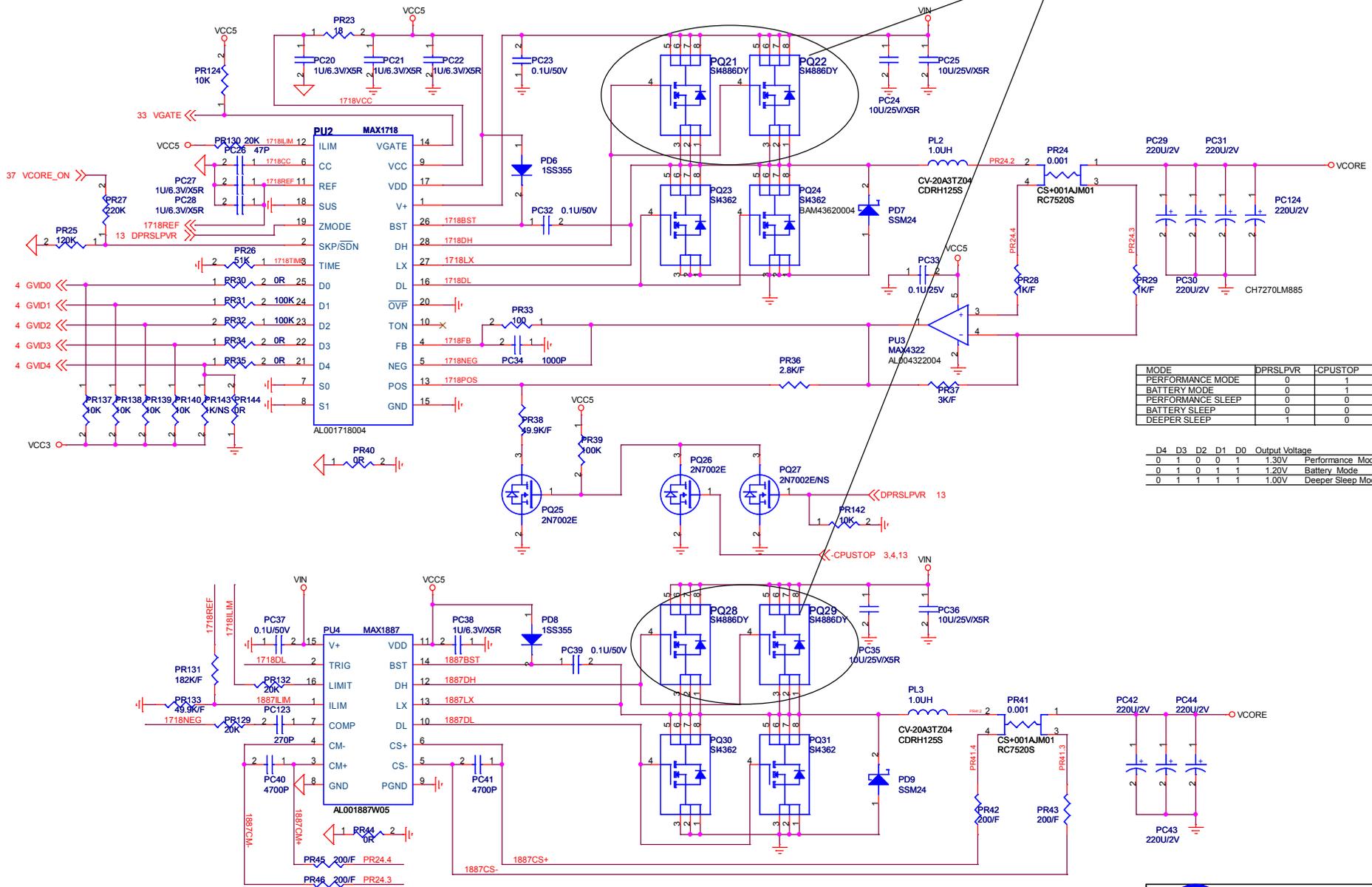
# 2.5/1.8/1.5VSUS & 1.25/1.2V

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# DC/DC CPU POWER

Rev. E changing

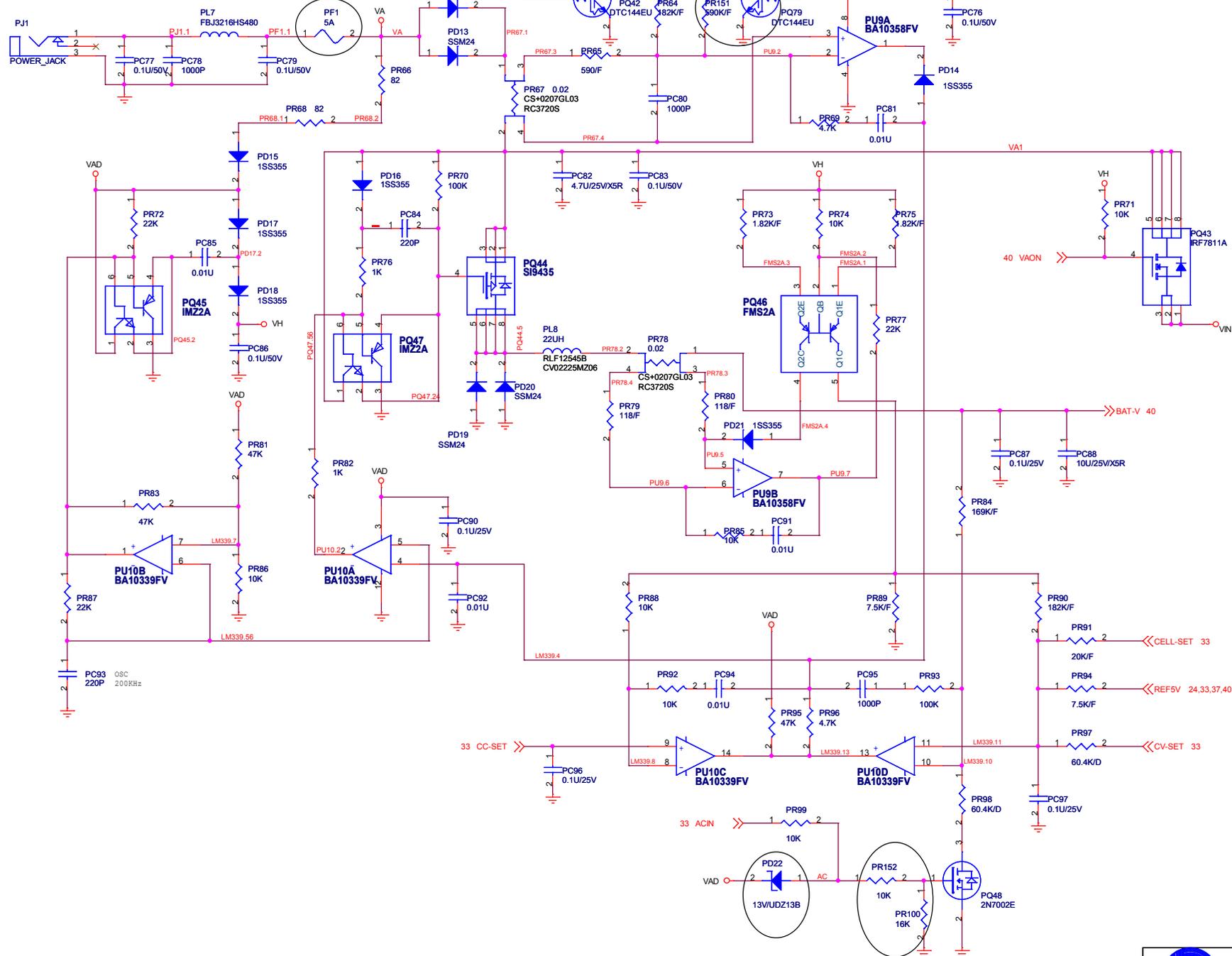


MODE	DPRSLPVR	CPUSTOP	OFFSET	VOUT(0A)
PERFORMANCE MODE	0	1	0mV	1.30
BATTERY MODE	0	1	0mV	1.20
PERFORMANCE SLEEP	0	0	-60mV	1.24
BATTERY SLEEP	0	0	-60mV	1.145
DEEPER SLEEP	1	0	0mV	1.0

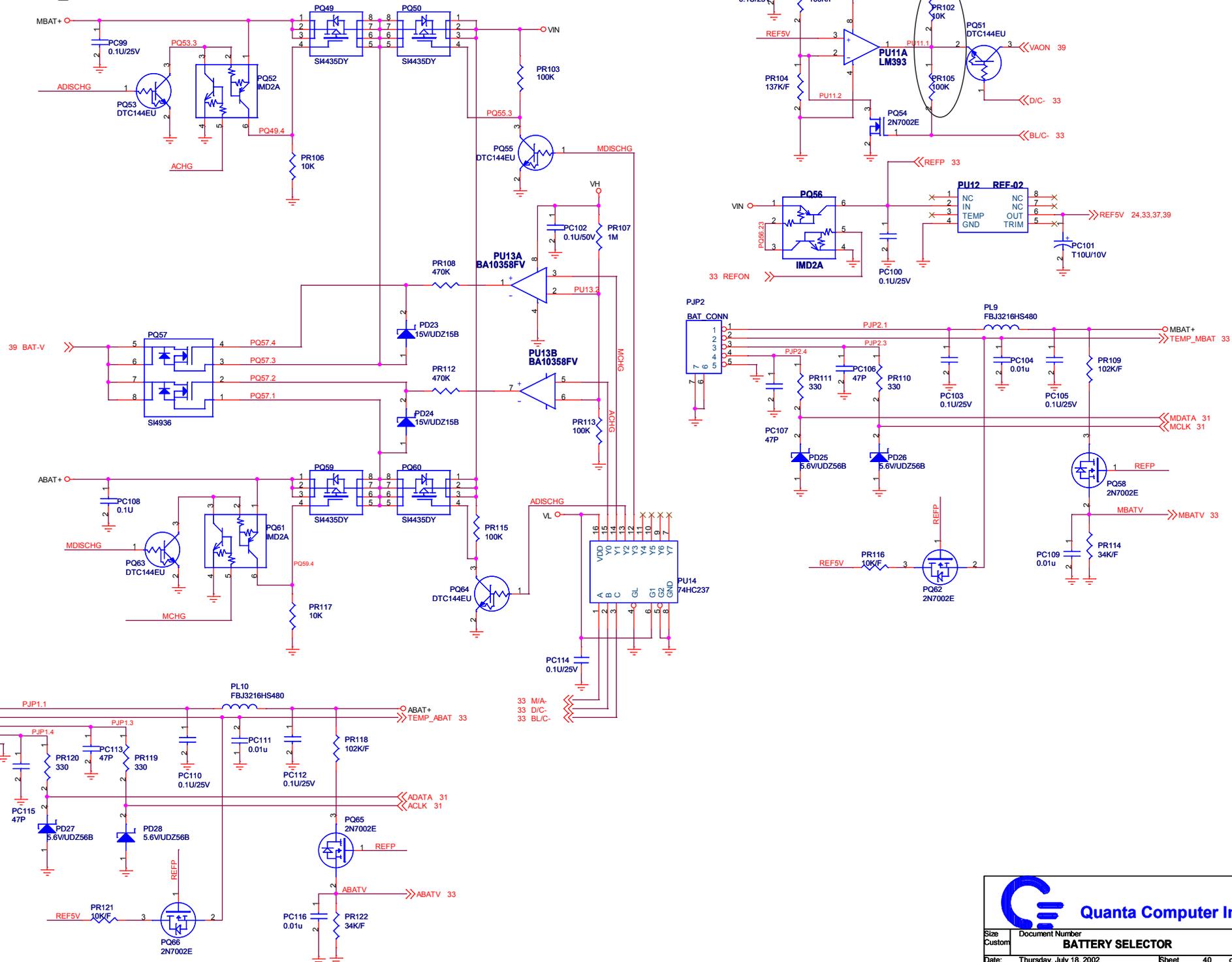
D4	D3	D2	D1	D0	Output Voltage
0	1	0	0	1	1.30V Performance Mode
0	1	0	1	1	1.20V Battery Mode
0	1	1	1	1	1.00V Deeper Sleep Mode

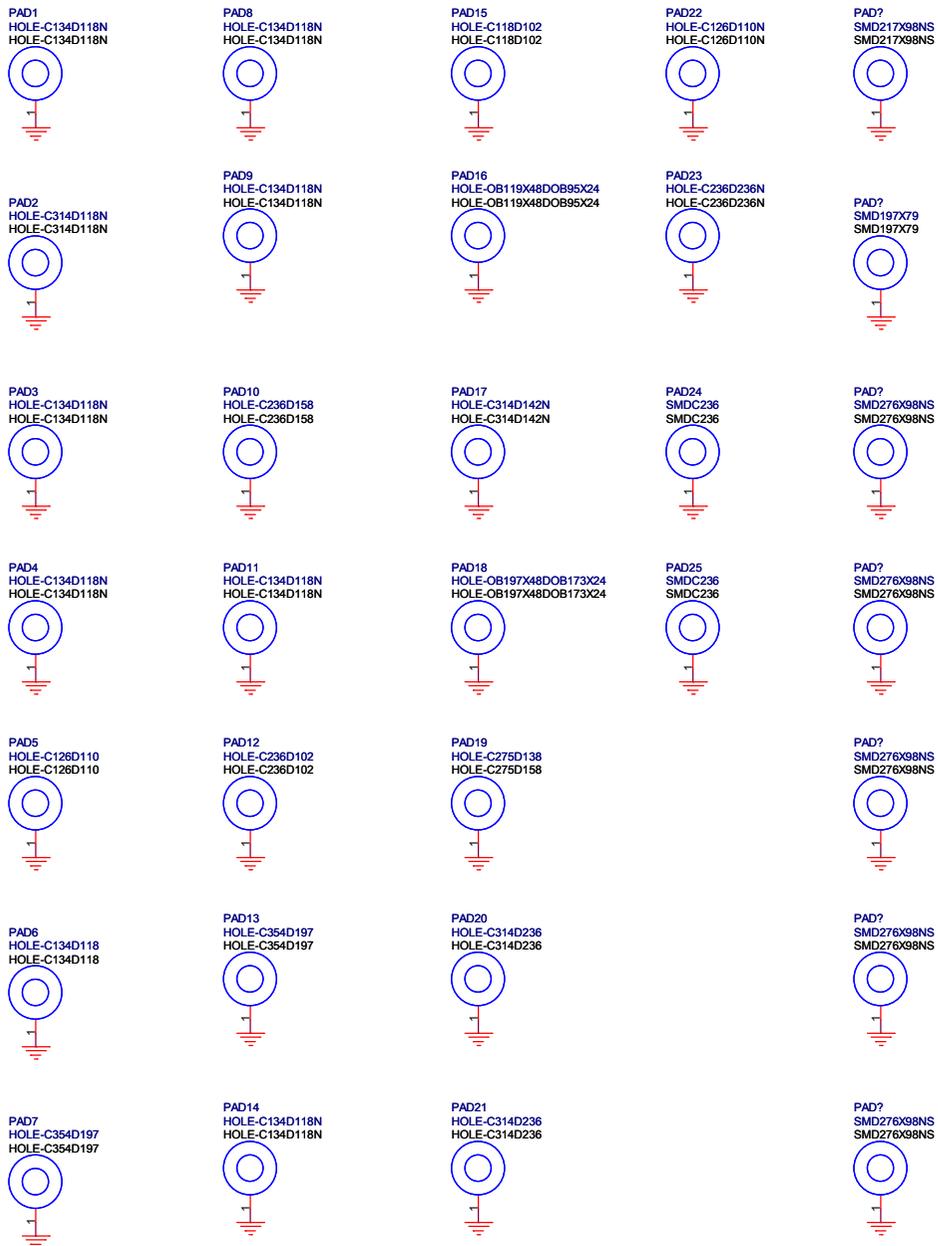
# Battery Charger

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# Battery Selector





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