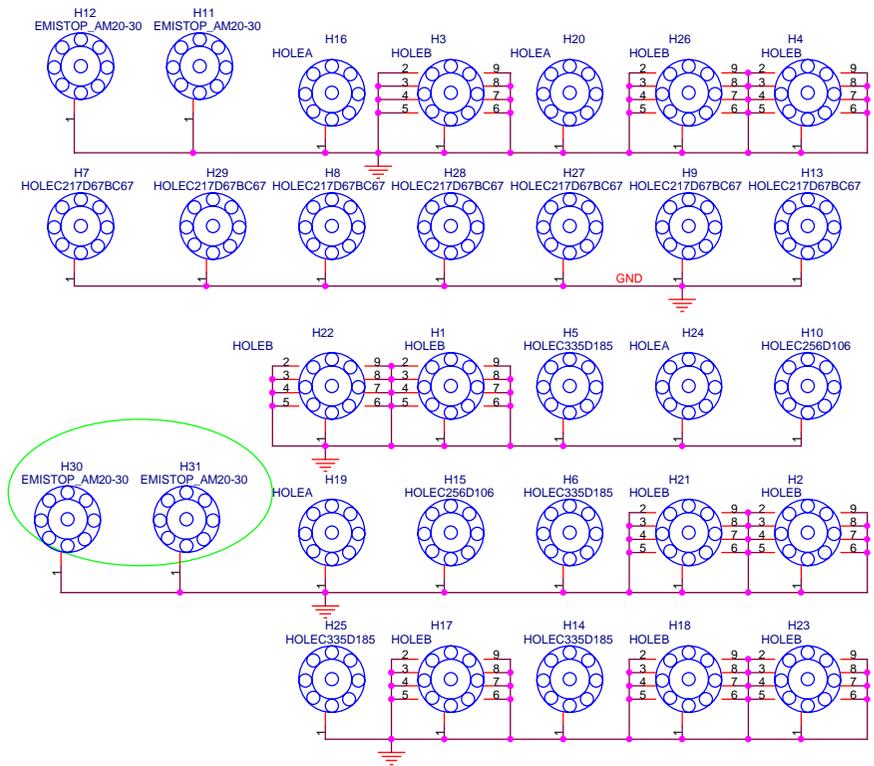


- 01:TITLE
- 02:Pentium 4-1
- 03:Pentium 4-2
- 04:Pentium 4-3
- 05:SIS648(HOST/AGP)
- 06:SIS648(RAM CONTROLLER)
- 07:SIS648(HYPER ZIP/AGP)
- 08:SIS648(POWER)
- 09:DDR_SODIMM
- 10:DDR_TERMINATION RESISTOR
- 11:M10_VGA_IO
- 12:M10_VGA_MEM
- 13:M10_VGA_POWER
- 14:DDR_SG8Mx32_BGA_A
- 15:DDR_SG8Mx32_BGA_B
- 16:963(PCI/IDE)
- 17:963(MISC)
- 18:963(USB CONTROLLER)
- 19:963(POWER)
- 20:KBC PC87591
- 21:W832517D LPC SUPER I/O
- 22:CRT & P.P.
- 23:AUDIO DJ
- 24:HDD & CD-ROM CONN.
- 25:AUDIO & AMP
- 26:LCD CRT &TV CONN
- 27:USB CONN &RJ11
- 28:TI 1410 CONTROLLER
- 29:MINIPCI & PCMCIA SLOT
- 30:RTL8201 LAN PHY
- 31:RTL8801 1394 PHY
- 32:CLOCK GENERATOR
- 33:CLOCK BUFFER
- 34:RTC&POWER GOOD
- 35:POWER SWITCH
- 36:SELECTOR
- 37:MAX1772 CHARGER
- 38:MAX1632 SYSTEM POWER
- 39:HIP6301 CPU POWER
- 40:RT9202_RT9173_LDO
- 41:MAX1858 VGA/RAM POWER
- 42:HISTORY

PAGE	01	02	03	04	05	06	07	08	09	10	11	12	13	14	15	16	17	18	19
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DATE	11/7	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4	9/4

PAGE	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38
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DATE	9/4	9/4	9/4	9/4	9/4	11/7	11/4	9/4	9/4	9/4	9/4	11/4	9/4	9/4	11/3	9/4	9/4	9/4	9/4

PAGE	39	40	41	42
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DATE	9/4	9/4	9/4	11/7

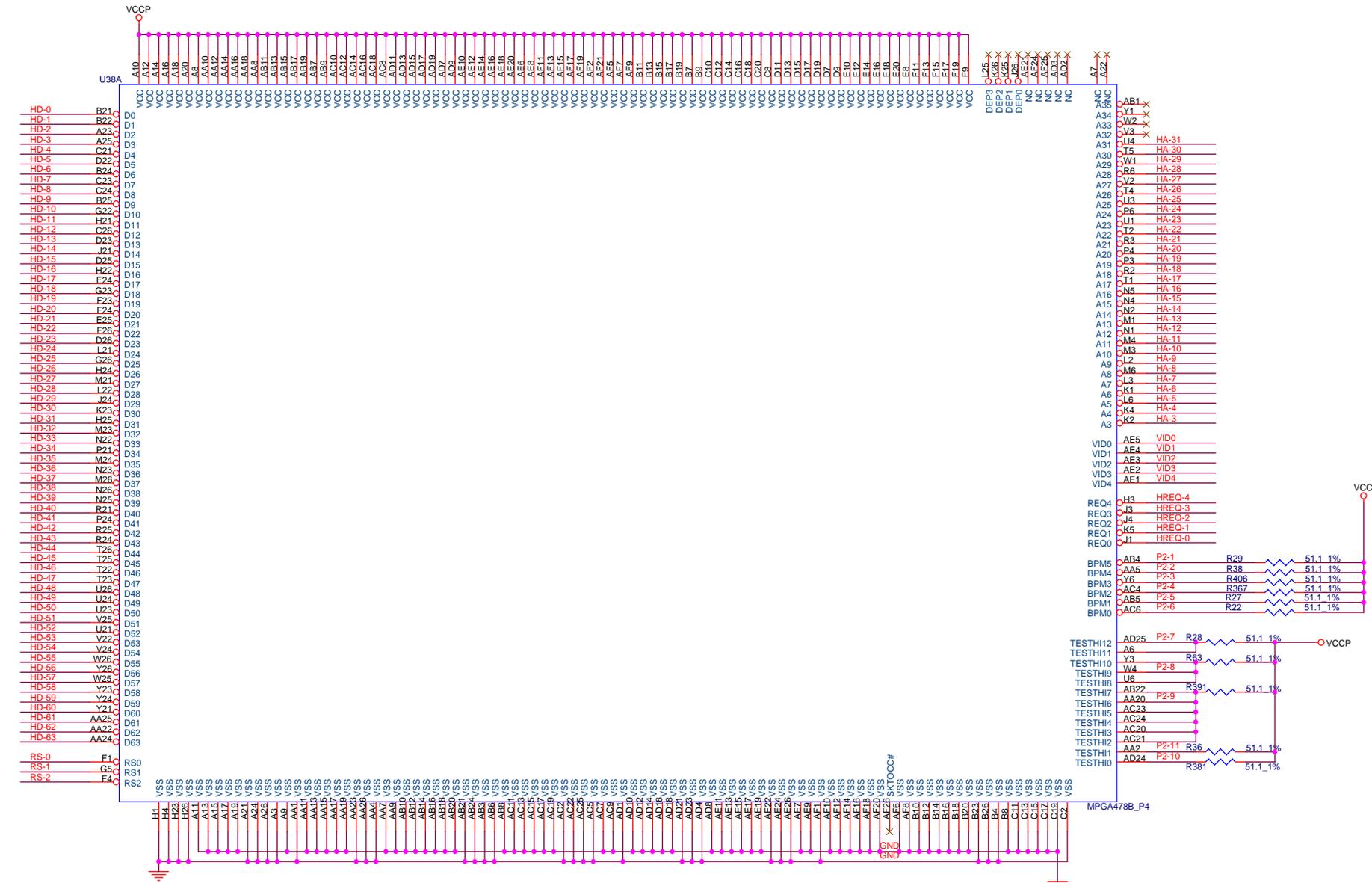


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 C3528 = B TYPE
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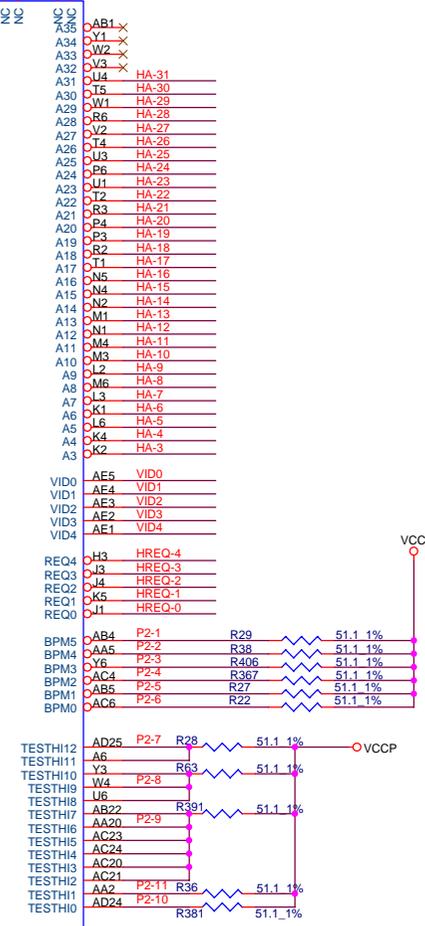
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P.Leader

Approved by	Checked by	Designed by
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Title G736 MB DESCRIPTION PAGE		
Size Custom	Document Number G736-1-4-01	Rev 3.C
Date: Friday, November 07, 2003	Sheet 1	of 42

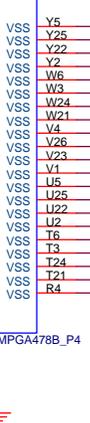
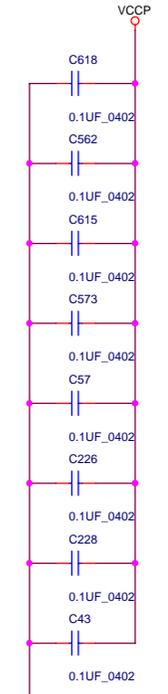
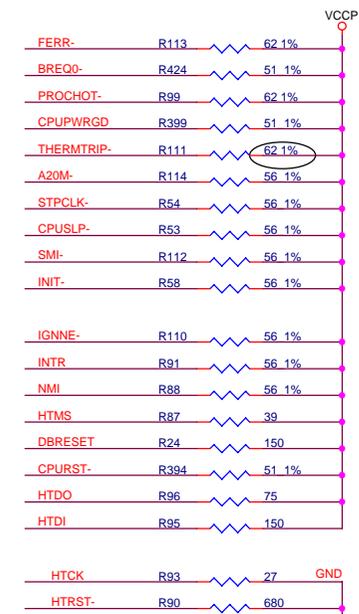
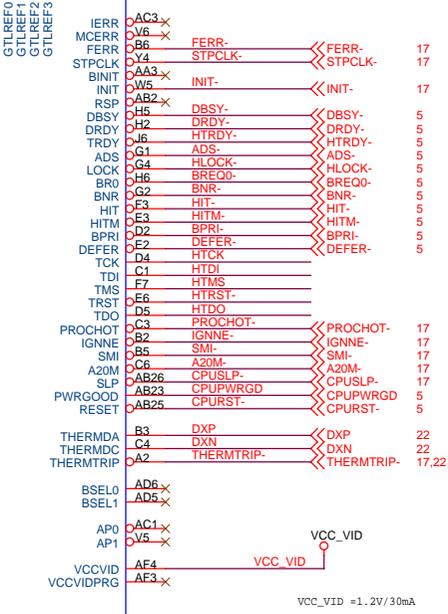
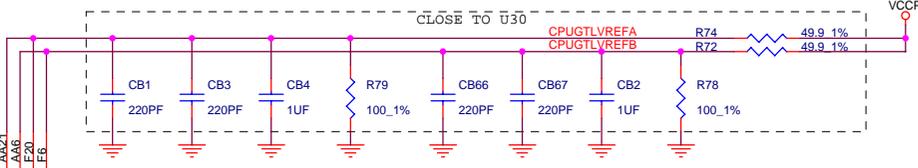
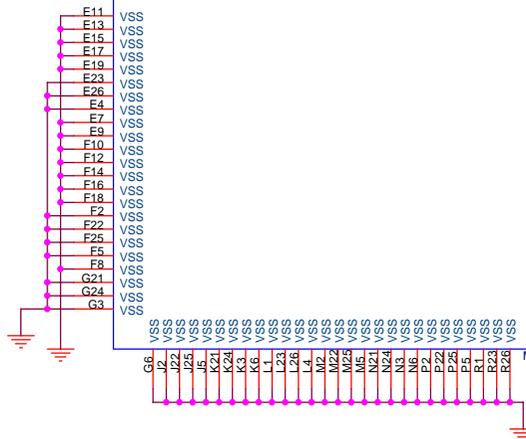
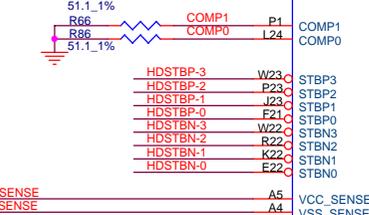
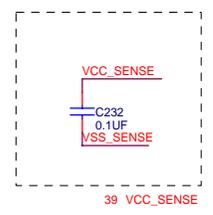
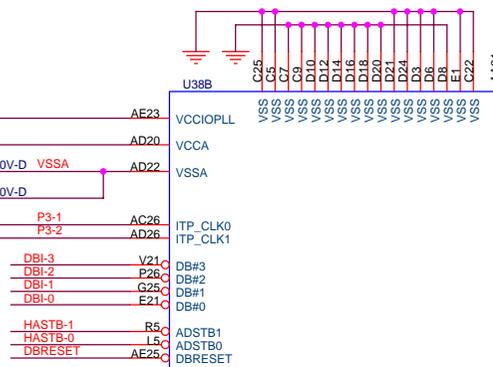
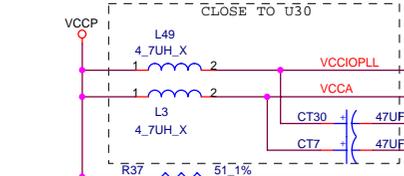


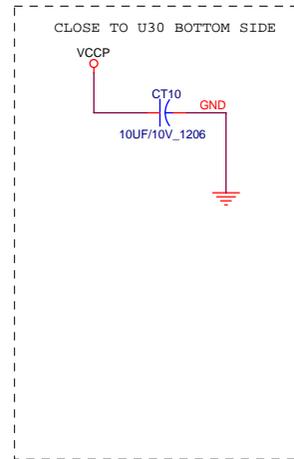
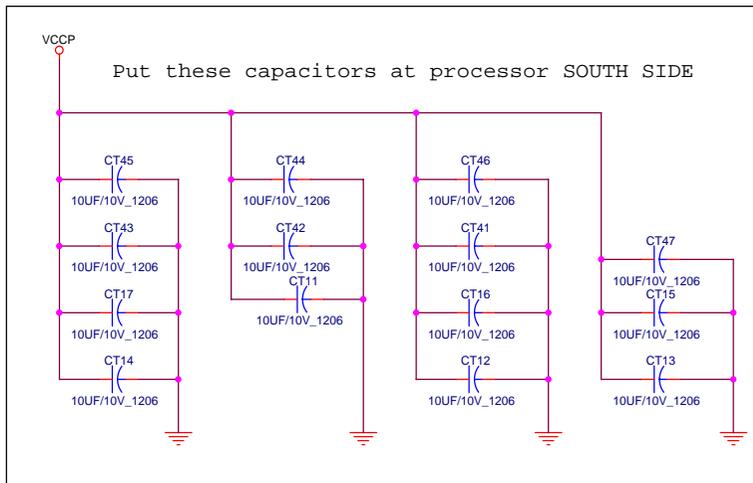
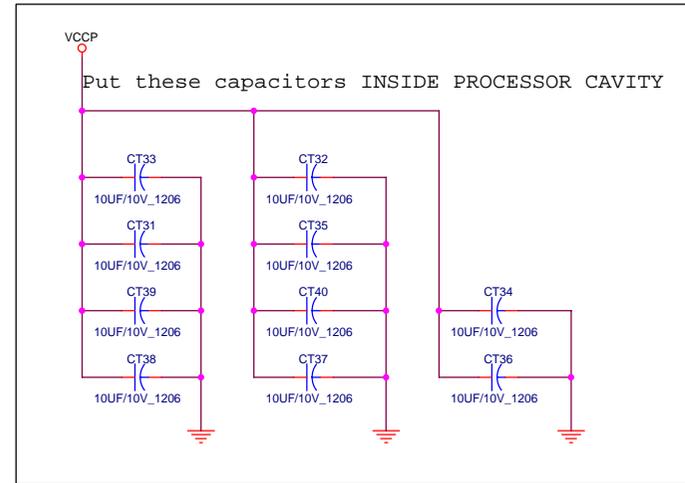
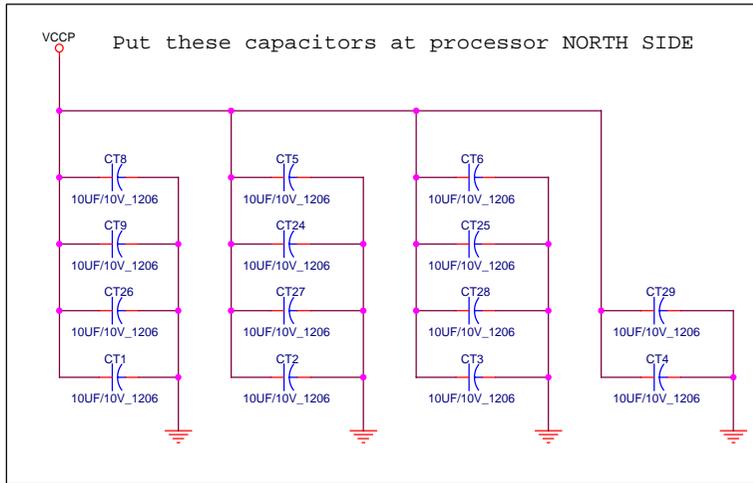
- 5 RS-[0..2] << RS-[0..2]
- 5 HD-[0..63] << HD-[0..63]
- 5 HA-[3..31] << HA-[3..31]
- 5 HREQ-[0..4] << HREQ-[0..4]
- 39 VID-[0..4] << VID-[0..4]



ECS CORP. (G736 MB)		
Title CPU_FSBI-1		
Size Custpm	Document Number G736-1-4-01	Rev 3.0
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- 5 HDSTBP-[0..3] << HDSTBP-[0..3]
- 5 HDSTBN-[0..3] << HDSTBN-[0..3]
- 5 DBI-[0..3] << DBI-[0..3]
- 5 HASTB-[0..1] << HASTB-[0..1]



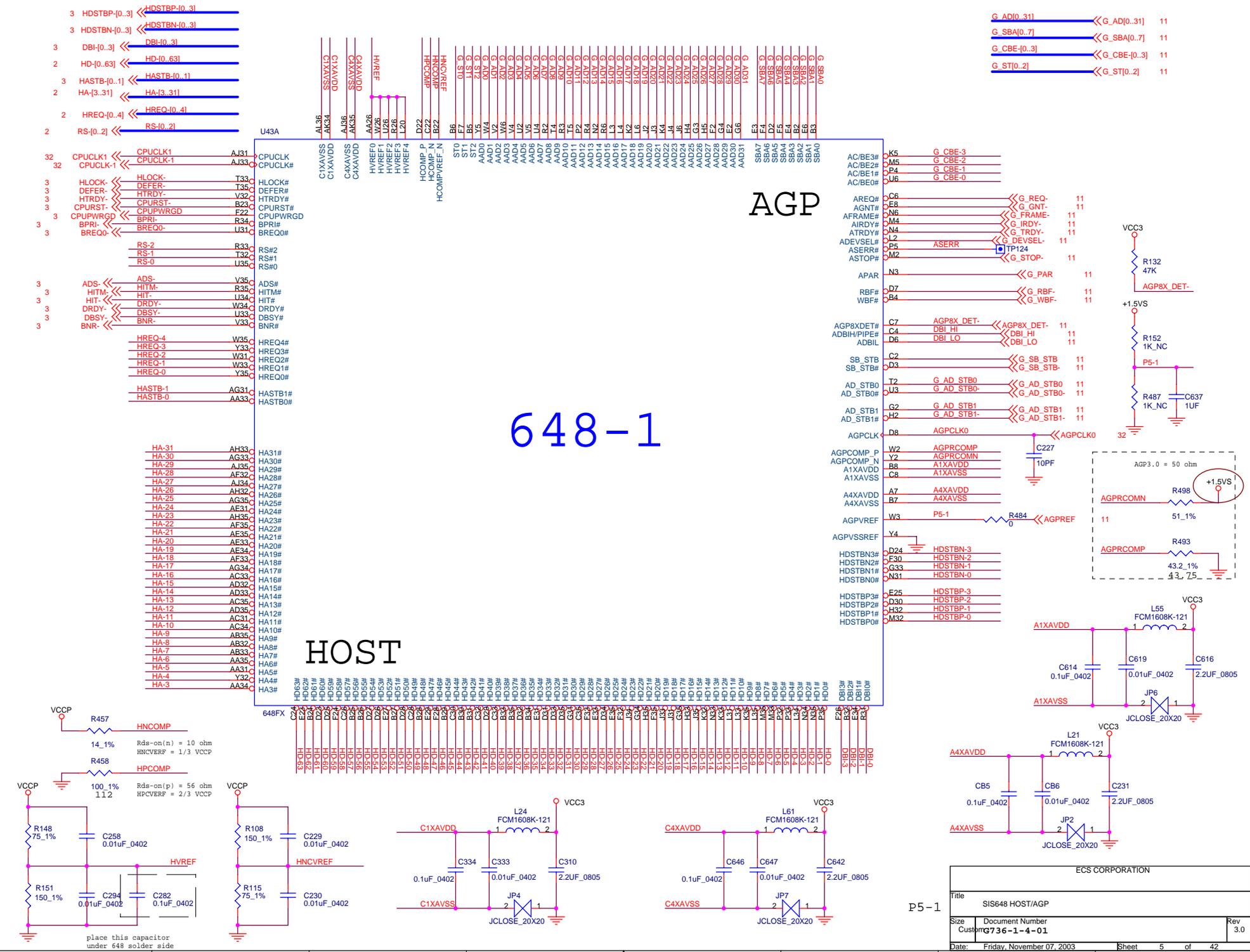


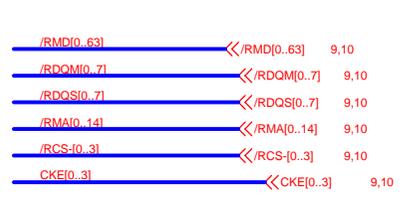
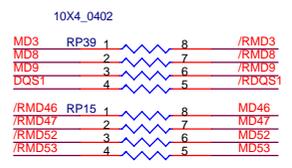
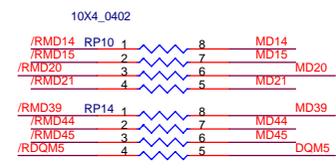
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Size	Document Number	Rev
Custom	G736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 4 of 42

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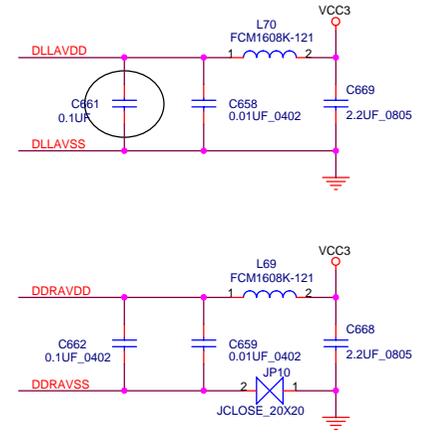
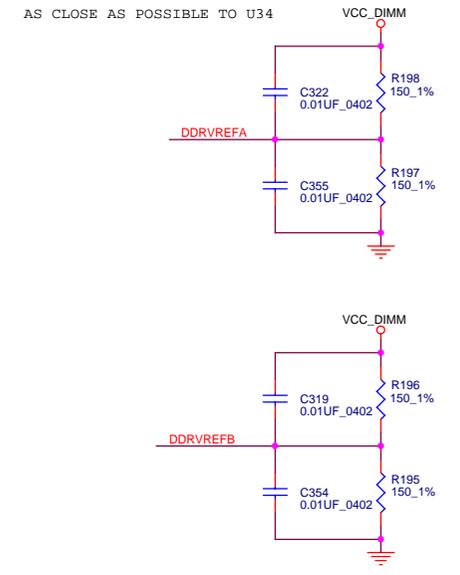
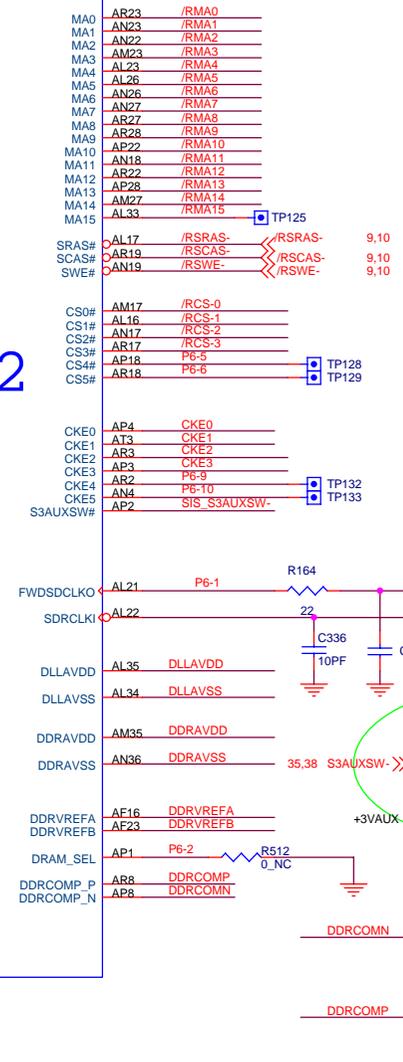
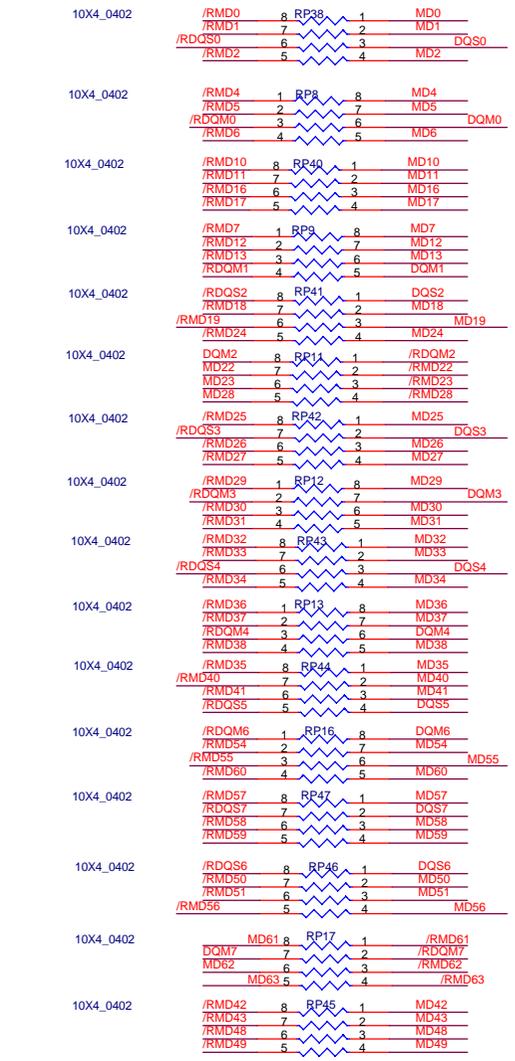
HOST

AGP





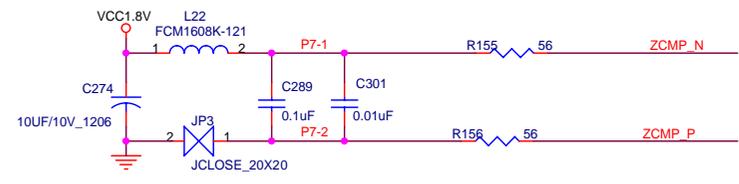
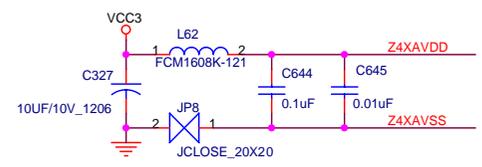
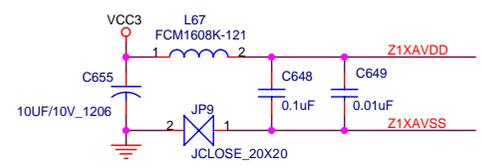
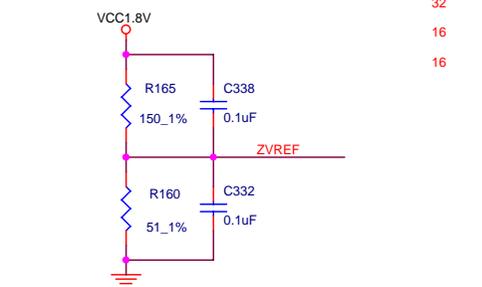
Resistors place close to DIMM1
Rs place close to DIMM1



ECS CORPORATION		
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Date:	Friday, November 07, 2003	Sheet 6 of 42

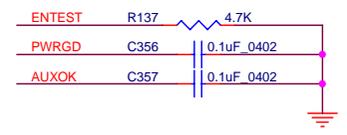
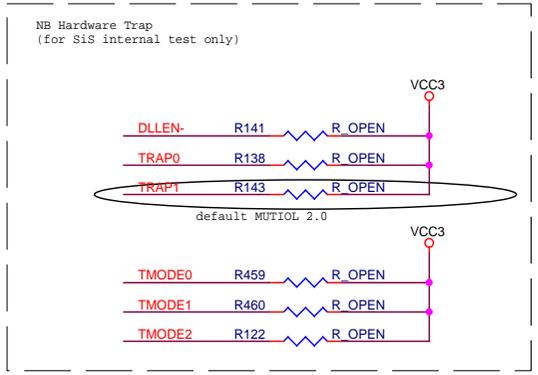
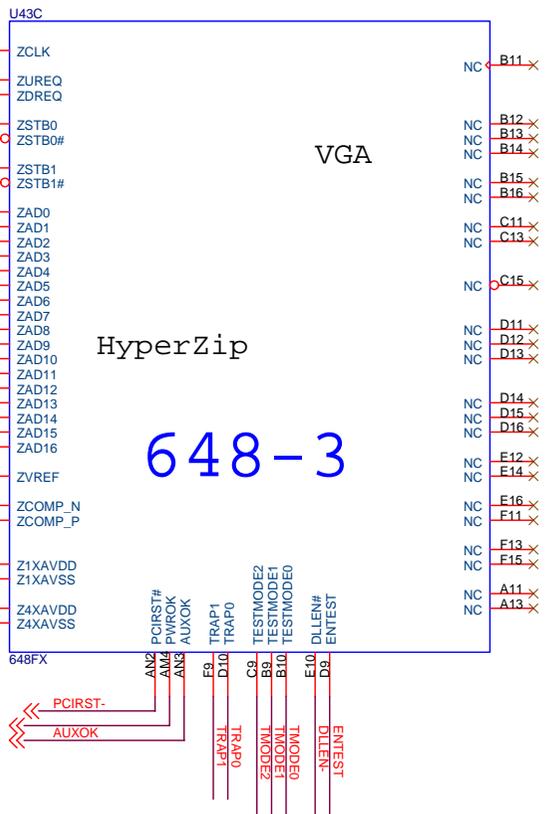
P6-10

16 ZAD[0..16] << ZADI0..16I
 16 ZSTB[0..1] << ZSTBI0..1I
 16 ZSTB-[0..1] << ZSTB-[0..1]I



32 ZCLK0 << ZCLK AL6
 16 ZUREQ << ZUREQ AL4
 16 ZDREQ << ZDREQ AK5
 ZSTB0 AJ2
 ZSTB-0 AJ3
 ZSTB0# ZSTB0#
 ZSTB1 AE3
 ZSTB-1 AE2
 ZSTB1# ZSTB1#
 ZAD0 AH5
 ZAD1 AK2
 ZAD2 AJ4
 ZAD3 AJ6
 ZAD4 AH2
 ZAD5 AH4
 ZAD6 AG3
 ZAD7 AG6
 ZAD8 AF4
 ZAD9 AG2
 ZAD10 AF5
 ZAD11 AG4
 ZAD12 AD2
 ZAD13 AE6
 ZAD14 AE2
 ZAD15 AE4
 ZAD16 AL3
 ZVREF AK4
 ZCAMP_N AD5
 ZCAMP_P AD4
 Z1XAVDD AN1
 Z1XAVSS AM2
 Z4XAVDD AL2
 Z4XAVSS AL1

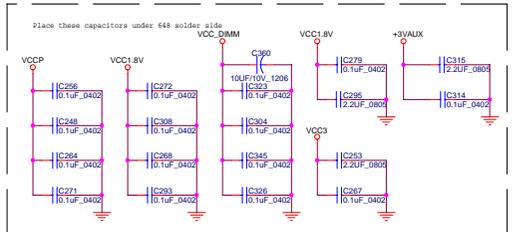
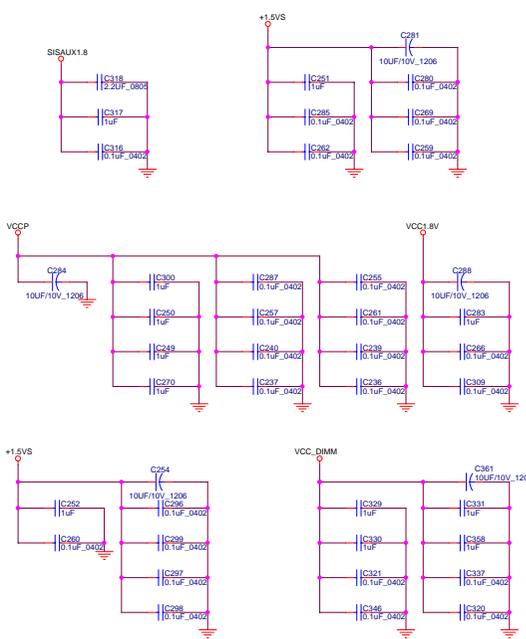
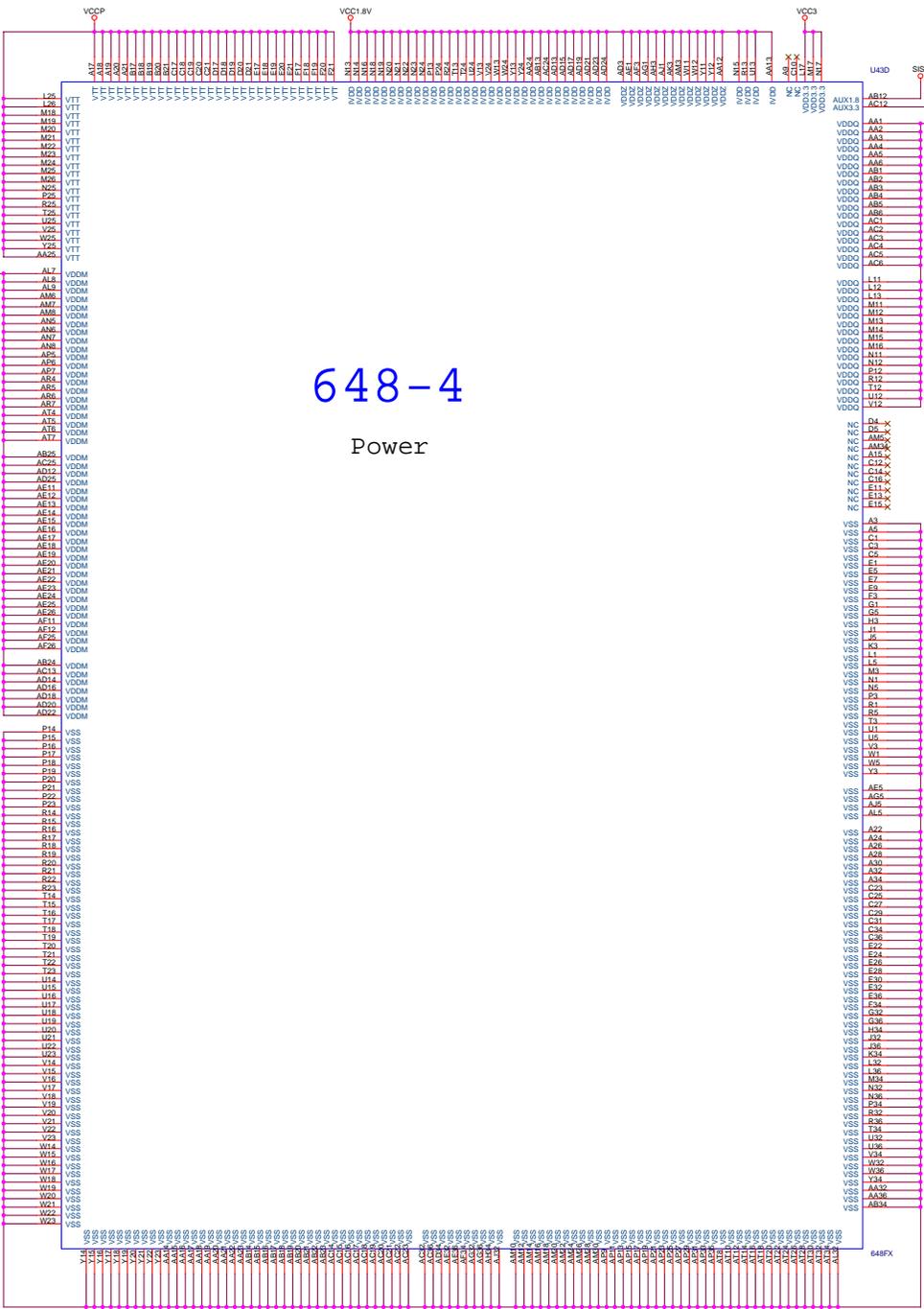
11,20,21,24,28,29,35 PCIRST-
 17,34 PWRGD
 17,30,34 AUXOK

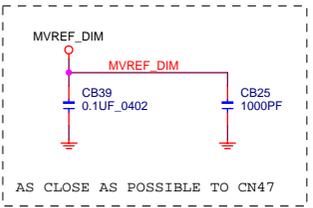
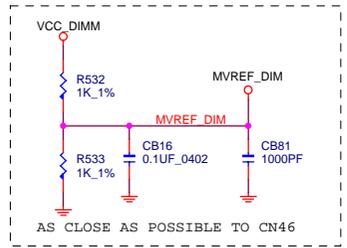
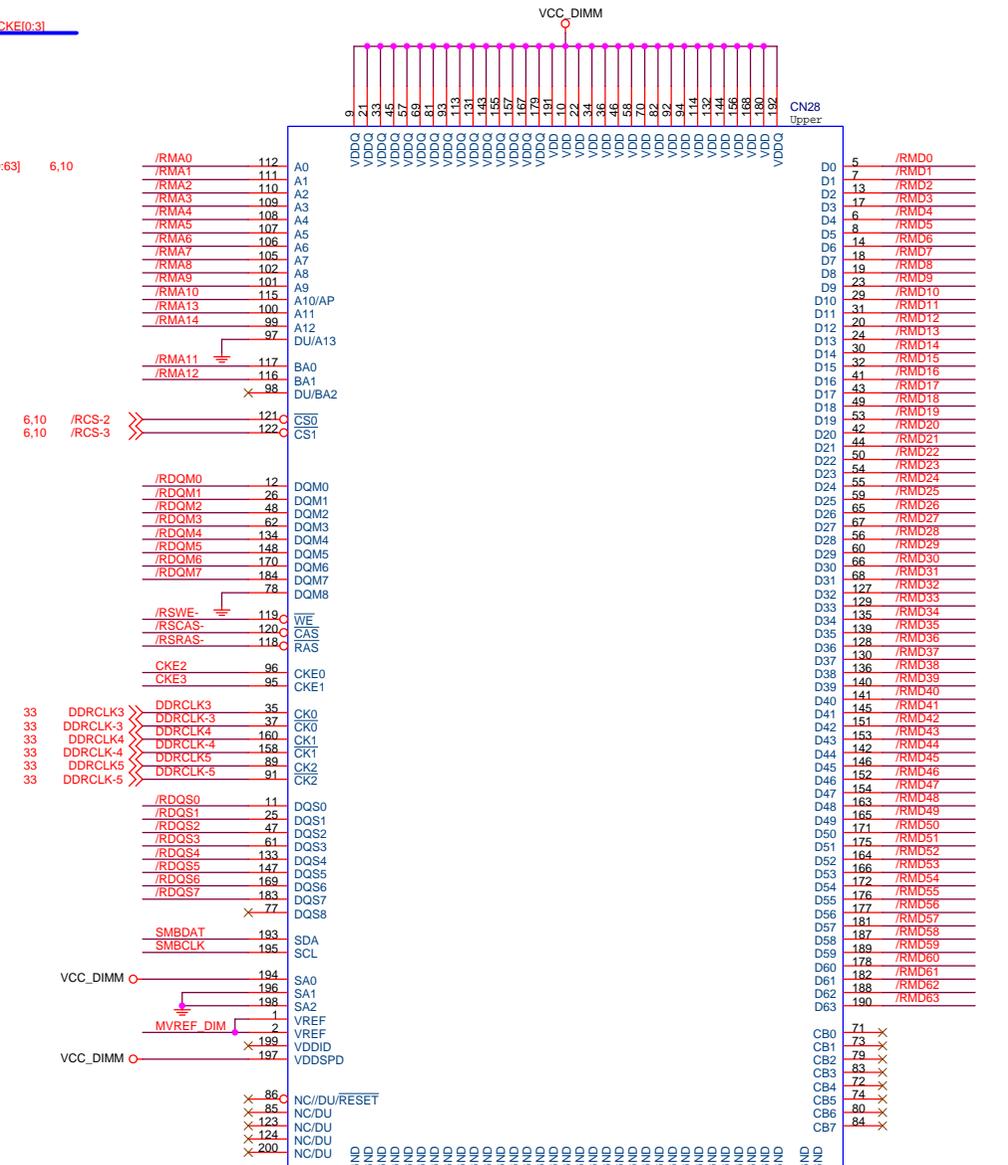
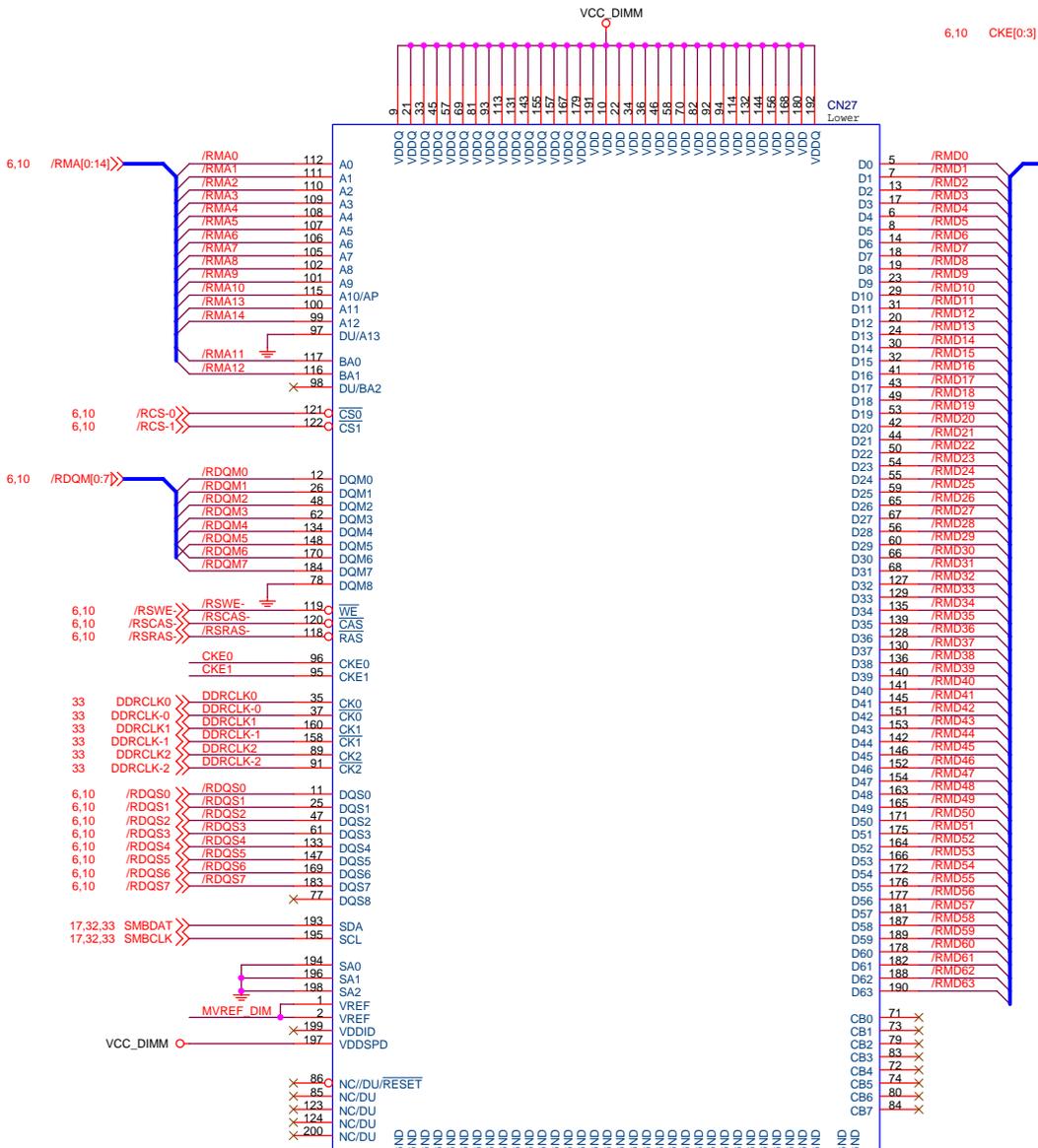


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Size B	Document Number G736-1-4-01	Rev 3.0
Date:	Friday, November 07, 2003	Sheet 7 of 42

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Power





KEYLINK_5746-2-111_DDR92_S
Outsight

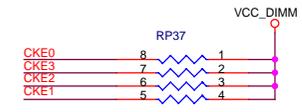
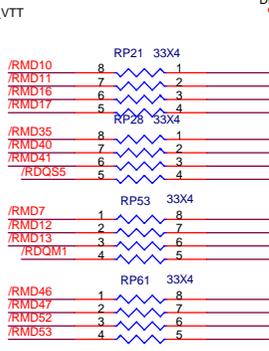
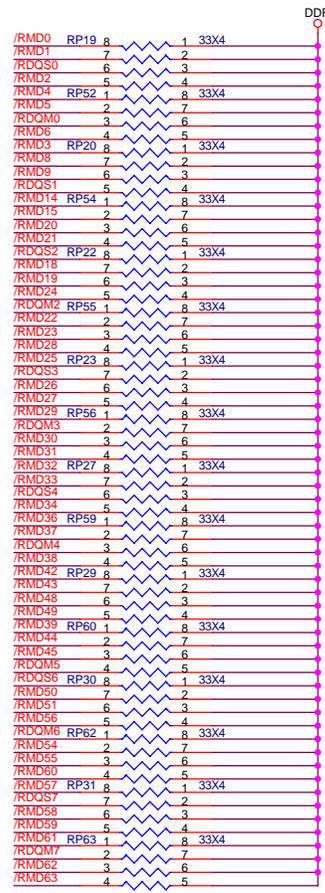
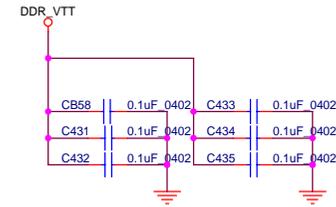
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R - 0101 CL2.5 = 18.75 to Data
DDR266 256MB 4Bx Pmax = 8W
W - 0100 Ptyp = 7W
64MB/128MB/256MB = 500MB/s - 1.0W
- 1000MB/s - 1.65W
- 1500MB/s - 2.5W
- 2000MB/s - 3.2W

AMP_C-137608-1_DDR52_S
Insight

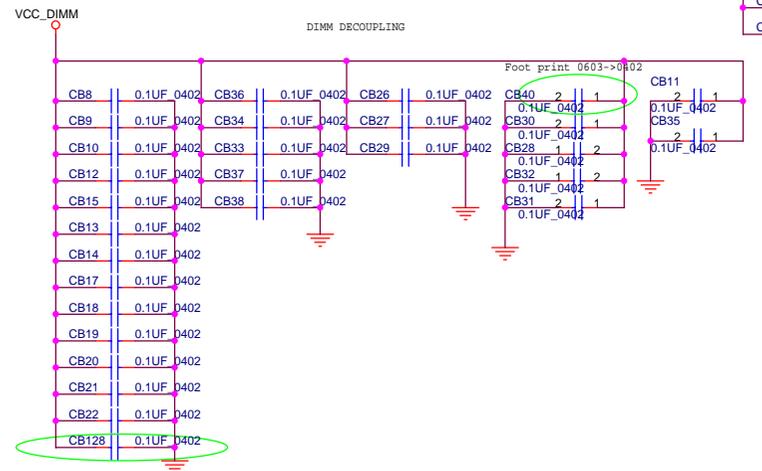
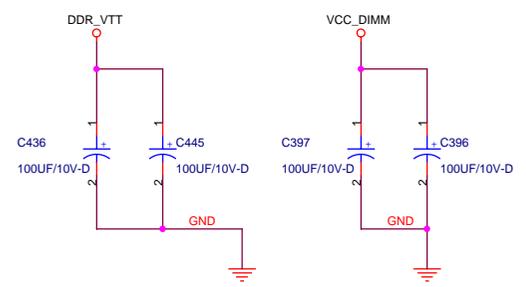
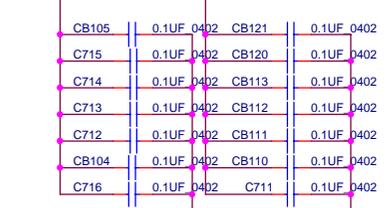
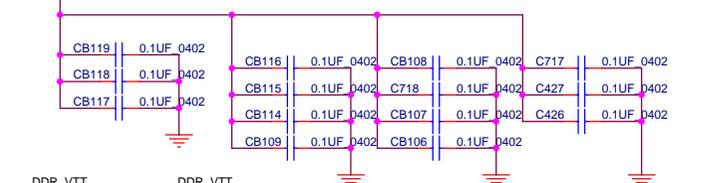
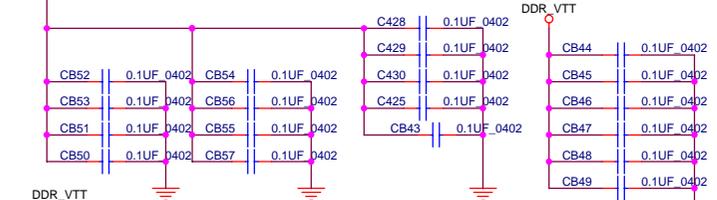
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Size Customer: G736-1-4-01	Document Number	
Date: Friday, November 07, 2003	Sheet 9	of 42
		Rev 3.0

SSTL-2 Termination Resistors

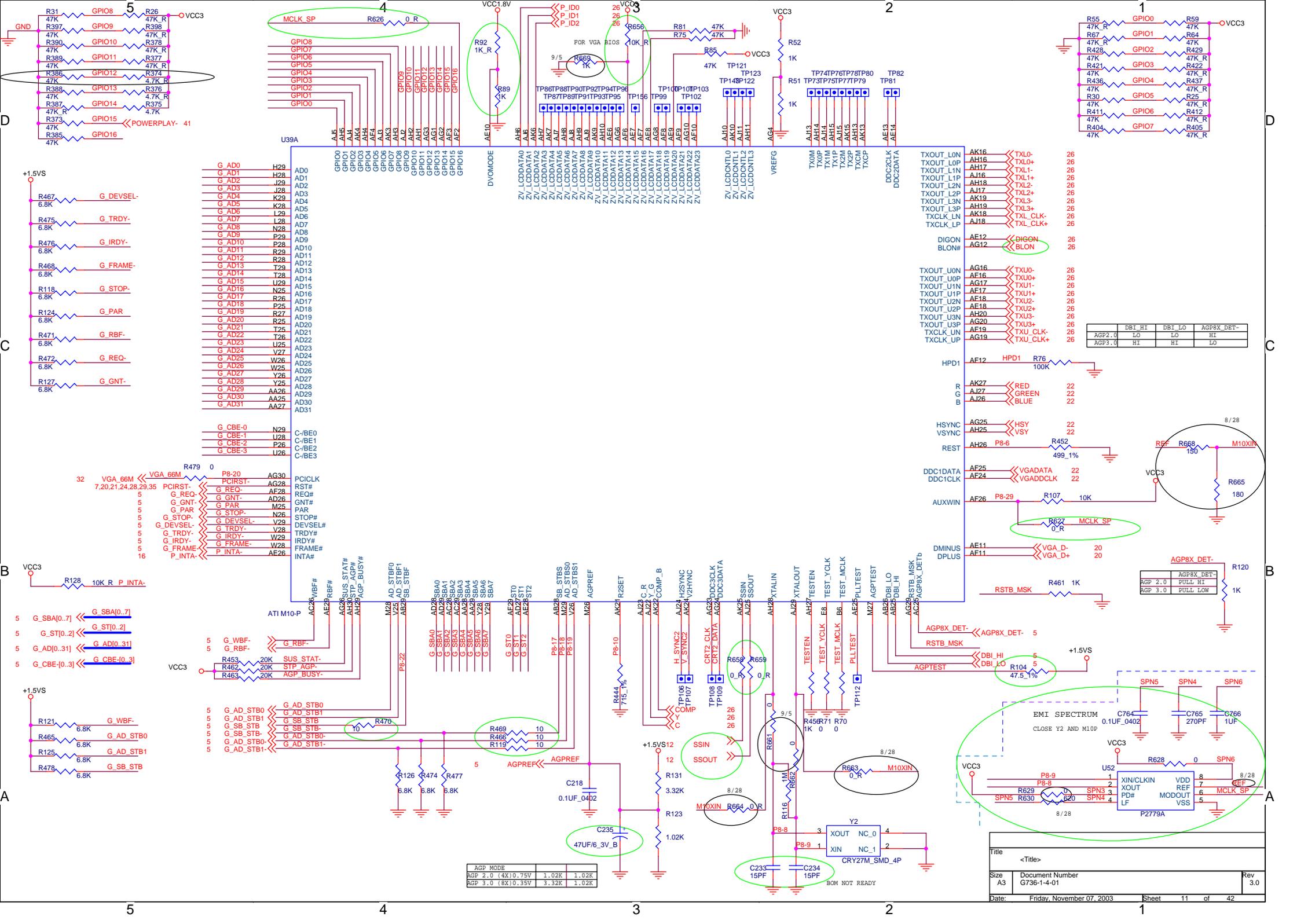
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MD/DQM (/DQS)	I/O-CMOS	SSTL-2	47
MA/Control	I/O-CMOS	SSTL-2	47
CS	I/O-CMOS	SSTL-2	47
CKE	DD 3.3V	DD 2.5V	47



470_8P4R
DECOUPLING CAPACITOR FOR SSTL-2 END TERMINATION VTT ISLAND
0603 Package placed within 200mils of VTT Termination R-packs



ECS CORP. (G736 M/B)		
Title	DDR_TERMINATION RESISTORS	
Size	Document Number	Rev
A3	G736-1-4-01	3.0
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U39A

G AD0	H29	AD0
G AD1	H28	AD1
G AD2	J29	AD2
G AD3	J28	AD3
G AD4	K29	AD4
G AD5	K28	AD5
G AD6	L29	AD6
G AD7	L28	AD7
G AD8	N28	AD8
G AD9	P29	AD9
G AD10	P28	AD10
G AD11	R29	AD11
G AD12	R28	AD12
G AD13	T29	AD13
G AD14	T28	AD14
G AD15	U29	AD15
G AD16	N25	AD16
G AD17	R26	AD17
G AD18	P25	AD18
G AD19	R27	AD19
G AD20	R25	AD20
G AD21	T25	AD21
G AD22	T26	AD22
G AD23	U25	AD23
G AD24	V27	AD24
G AD25	W26	AD25
G AD26	W25	AD26
G AD27	Y26	AD27
G AD28	Y25	AD28
G AD29	AA26	AD29
G AD30	AA25	AD30
G AD31	AA27	AD31

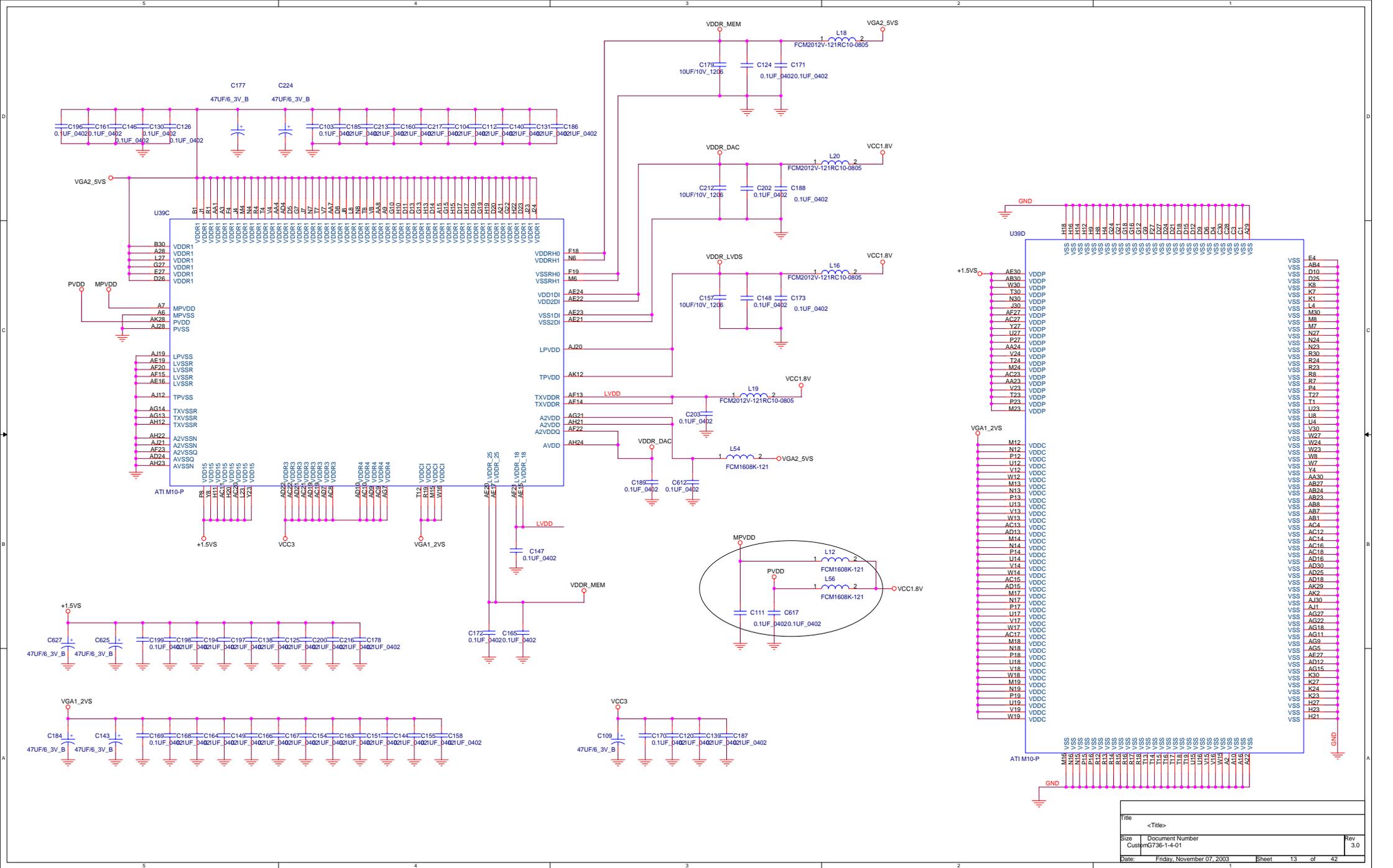
G CBE-0	N29	C-/BE0
G CBE-1	U28	C-/BE1
G CBE-2	P26	C-/BE2
G CBE-3	U26	C-/BE3

G SBA0[.7]	G SBA[0..7]
G ST[0..2]	G ST[0..2]
G AD[0..31]	G AD[0..31]
G CBE[0..3]	G CBE[0..3]

G AD_STB0	G AD_STB0
G AD_STB1	G AD_STB1
G SB_STB	G SB_STB
G SB_STB0	G SB_STB0
G AD_STB0	G AD_STB0
G AD_STB1	G AD_STB1

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AGP 3.0 (8X) 0.35V	3.32K	1.02K

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Size	Document Number
A3	G736-1-4-01
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Title	<Title>	
Size	Document Number	Rev
	Custom6736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 13 of 42

All of signal trace 5mil spacing 10mil .

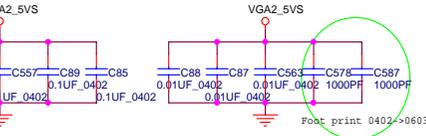
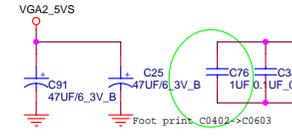
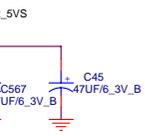
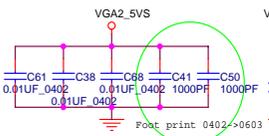
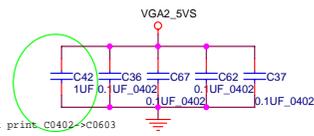
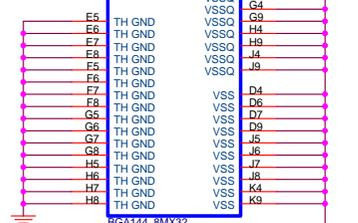
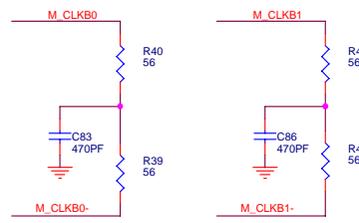
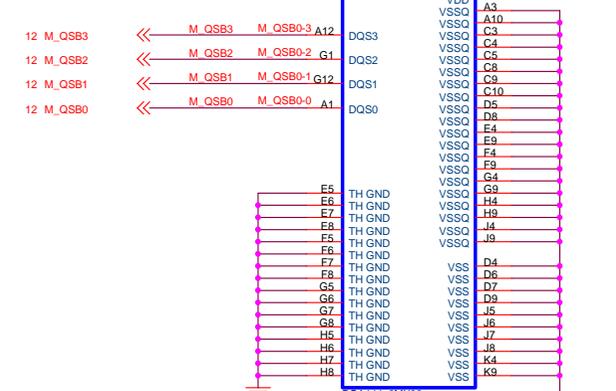
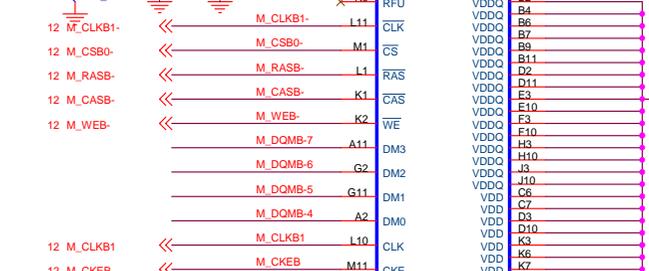
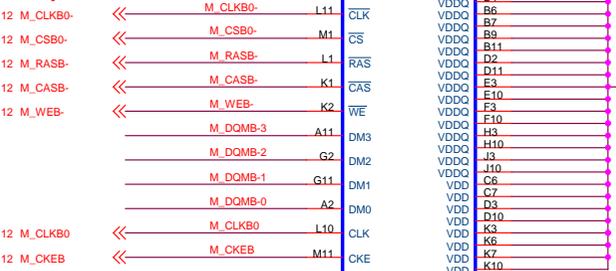
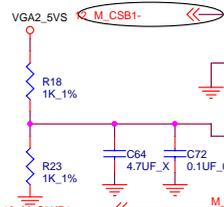
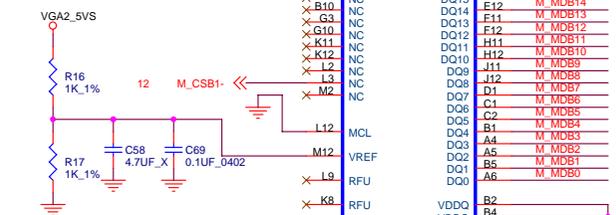
U33

M_MAB13	M3	BA0	DO31	A7	M_MDB31
M_MAB12	L4	BA1	DO30	B8	M_MDB30
			DO29	A8	M_MDB29
M_MAB11	L6	A11	DO28	B9	M_MDB28
M_MAB10	K6	A10	DO27	C11	M_MDB26
M_MAB9	L7	A9	DO26	C12	M_MDB25
M_MAB8	M10	A8/AP	DO25	J2	M_MDB23
M_MAB7	M9	A7	DO24	D12	M_MDB23
M_MAB6	M8	A6	DO23	J1	M_MDB22
M_MAB5	L8	A5	DO22	H1	M_MDB21
M_MAB4	M7	A4	DO21	H2	M_MDB20
M_MAB3	M6	A3	DO20	F1	M_MDB19
M_MAB2	L5	A2	DO19	F2	M_MDB18
M_MAB1	M5	A1	DO18	E1	M_MDB17
M_MAB0	M4	A0	DO17	E2	M_MDB16
			DO16	E11	M_MDB15
			DO15	E12	M_MDB14
			DO14	F11	M_MDB13
			DO13	F12	M_MDB12
			DO12	H11	M_MDB11
			DO11	H12	M_MDB10
			DO10	J11	M_MDB9
			DO9	J12	M_MDB8
			DO8	D1	M_MDB7
			DO7	C1	M_MDB6
			DO6	C2	M_MDB5
			DO5	B1	M_MDB4
			DO4	A4	M_MDB3
			DO3	A5	M_MDB2
			DO2	B5	M_MDB1
			DO1	B6	M_MDB0
			DQ0	A6	M_MDB0

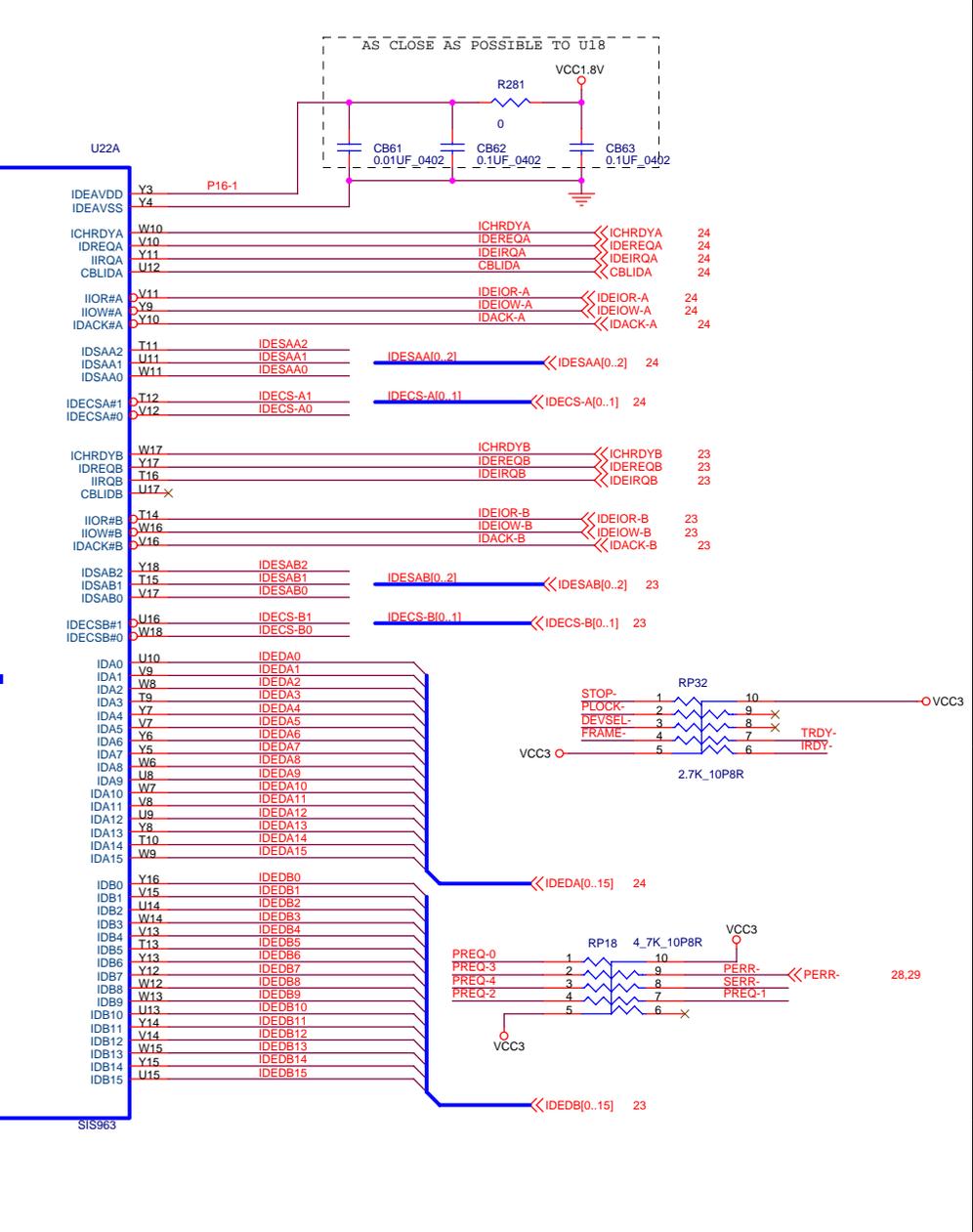
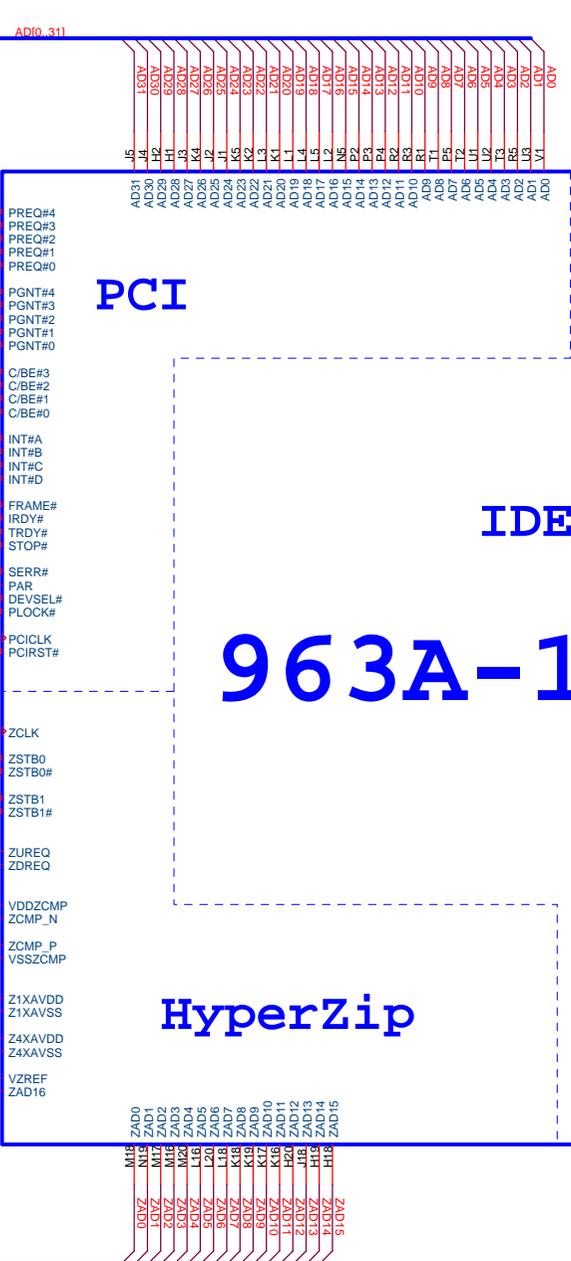
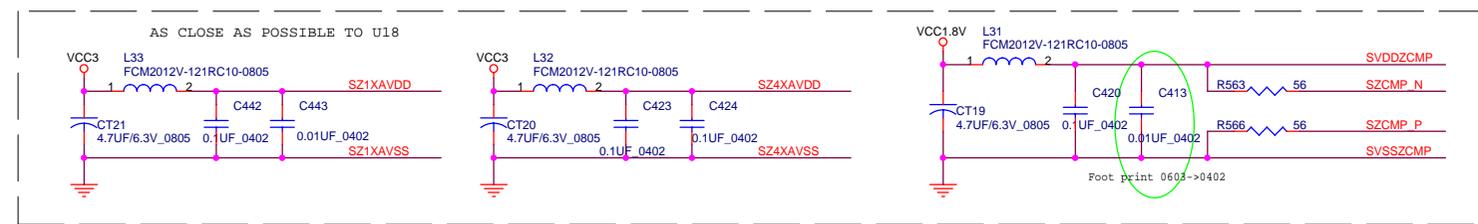
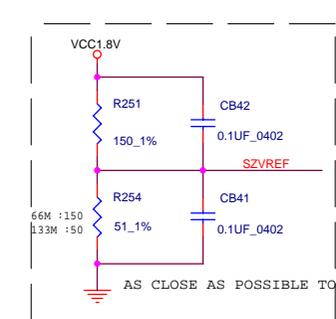
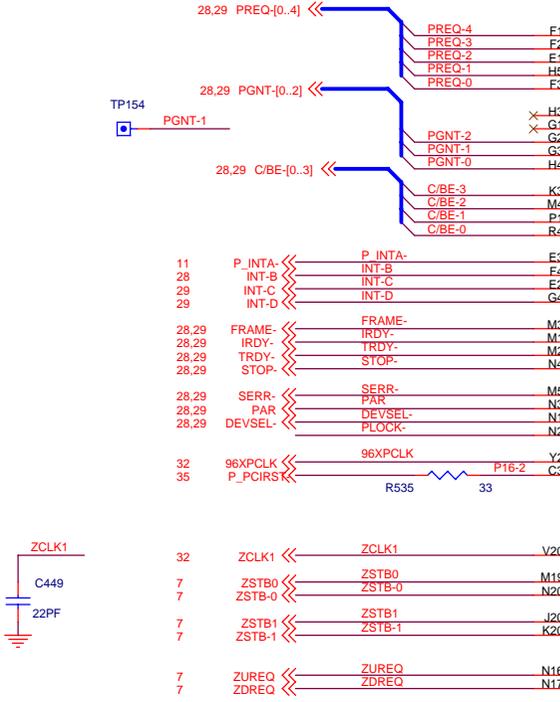
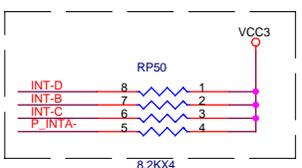
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 12 M_MAB[0..13] << M_MAB[0..13]
 12 M_MDB[0..63] << M_MDB[0..63]

U34

M_MAB13	M3	BA0	DO31	A7	M_MDB63
M_MAB12	L4	BA1	DO30	B8	M_MDB62
			DO29	A8	M_MDB61
M_MAB11	L6	A11	DO28	B9	M_MDB60
M_MAB10	K6	A10	DO27	C11	M_MDB59
M_MAB9	L7	A9	DO26	C12	M_MDB58
M_MAB8	M10	A8/AP	DO25	J2	M_MDB56
M_MAB7	M9	A7	DO24	J1	M_MDB55
M_MAB6	M8	A6	DO23	H1	M_MDB54
M_MAB5	L8	A5	DO22	H2	M_MDB53
M_MAB4	M7	A4	DO21	F1	M_MDB52
M_MAB3	M6	A3	DO20	F2	M_MDB51
M_MAB2	L5	A2	DO19	E1	M_MDB50
M_MAB1	M5	A1	DO18	E2	M_MDB49
M_MAB0	M4	A0	DO17	E11	M_MDB48
			DO16	E12	M_MDB47
			DO15	F11	M_MDB46
			DO14	F12	M_MDB45
			DO13	H11	M_MDB44
			DO12	H12	M_MDB43
			DO11	J11	M_MDB42
			DO10	J12	M_MDB41
			DO9	D1	M_MDB39
			DO8	C1	M_MDB38
			DO7	C2	M_MDB37
			DO6	B1	M_MDB36
			DO5	A4	M_MDB35
			DO4	A5	M_MDB34
			DO3	B5	M_MDB33
			DO2	B6	M_MDB32
			DQ1	A6	M_MDB32
			DQ0	A6	M_MDB32

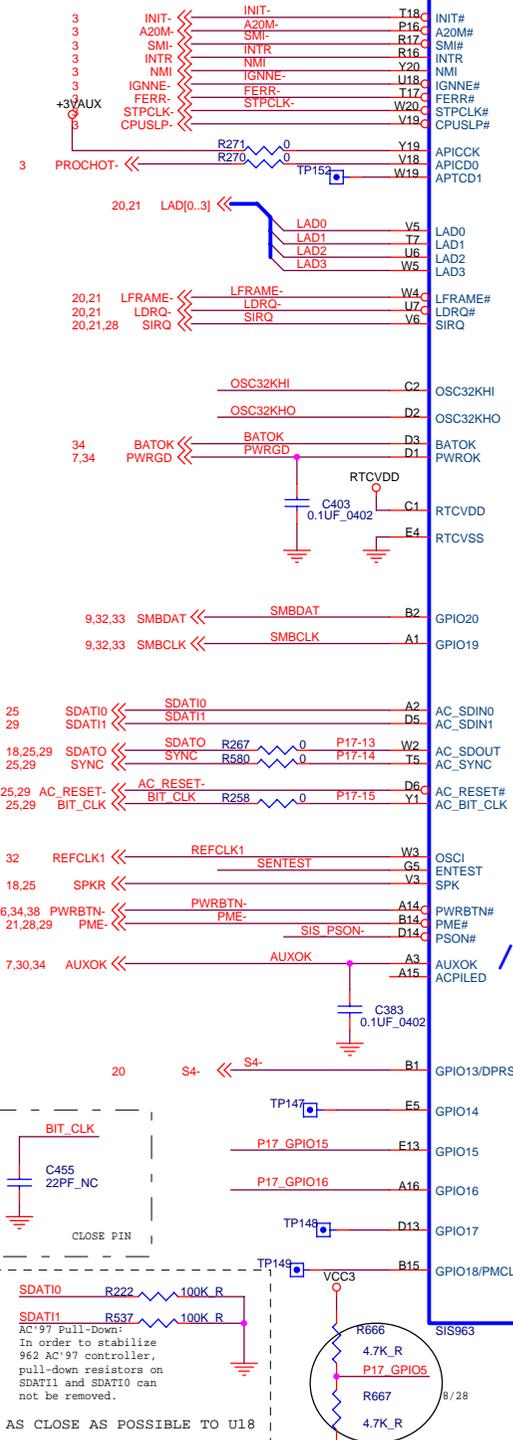


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Size	Document Number	Rev
Custom	G736-1-4-01	3.0
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ECS CORP. (G736 M/B)		
Title	SIS962-1	
Size	Document Number	Rev
Custom	G736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 16 of 42

Programmable on-die pull-high strength for CPU_S:
 (Infinite, 150, 110, 56 Ohm)



CPU_S

APIC

LPC

RTC

GPIO

AC97

ACPI / OTHER

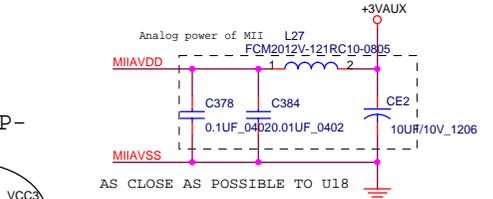
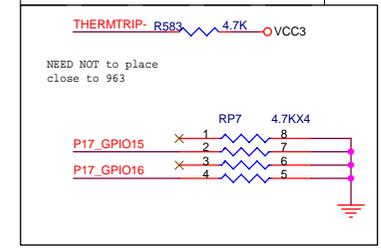
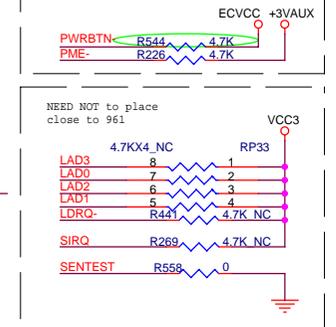
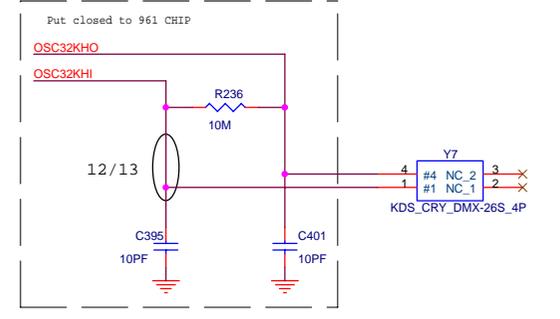
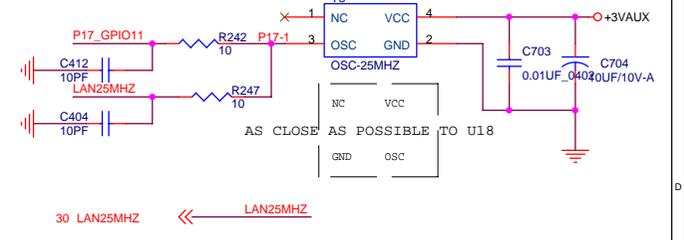
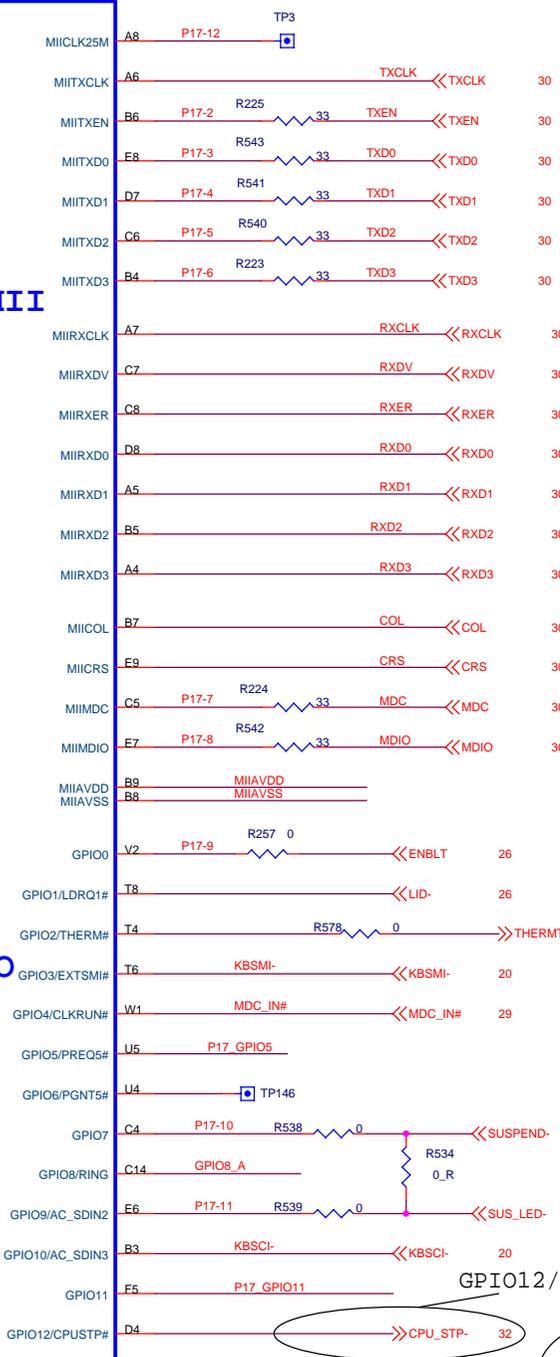
KBC /geyserville

MII

GPIO

GPIO12 / CPU_STP-

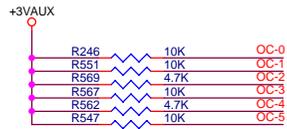
963A-2



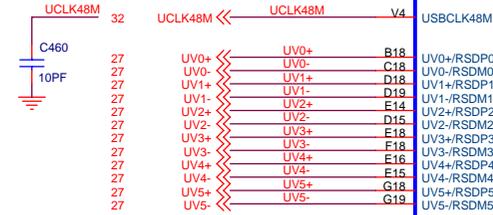
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Size	Document Number	G736-1-4-01
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Date:	Friday, November 07, 2003	Sheet 17 of 42
		Rev 3.0

AS CLOSE AS POSSIBLE TO U18

AS CLOSE AS POSSIBLE TO U18

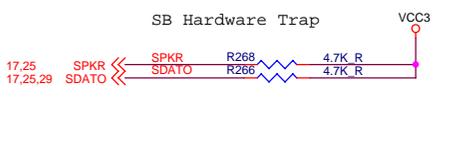
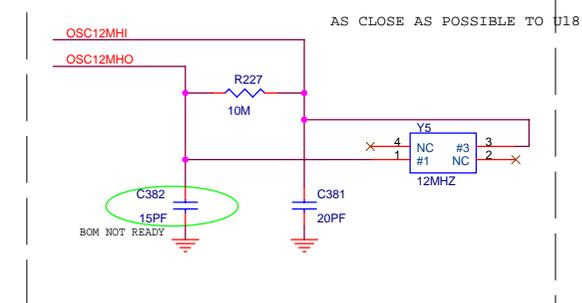
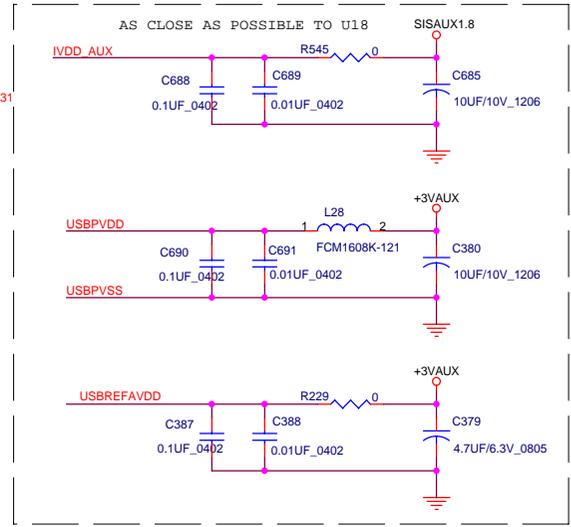
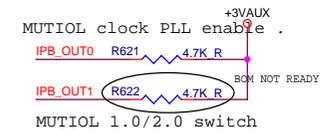
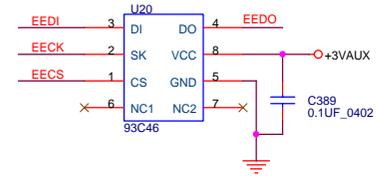
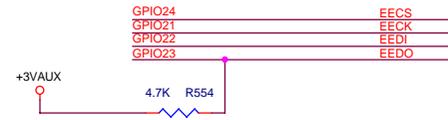
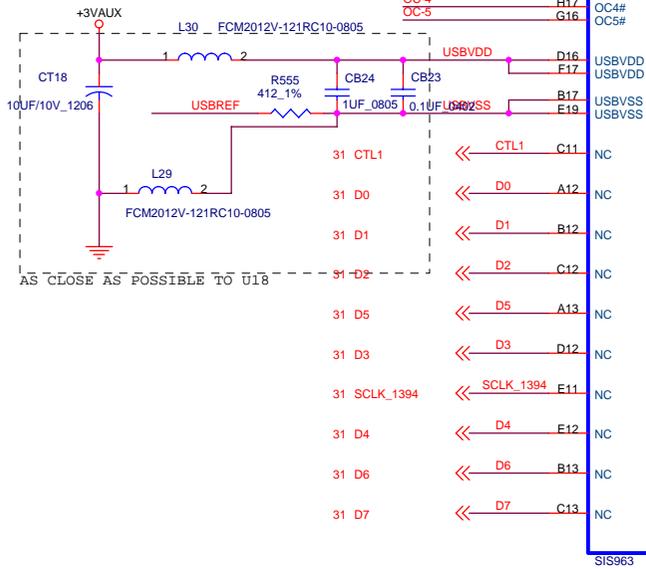


They don't need to be placed near the SiS963

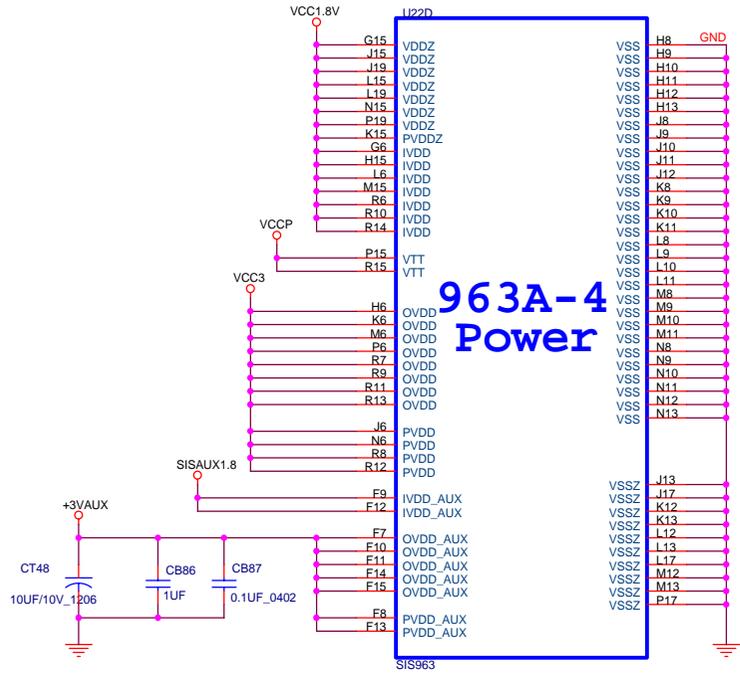
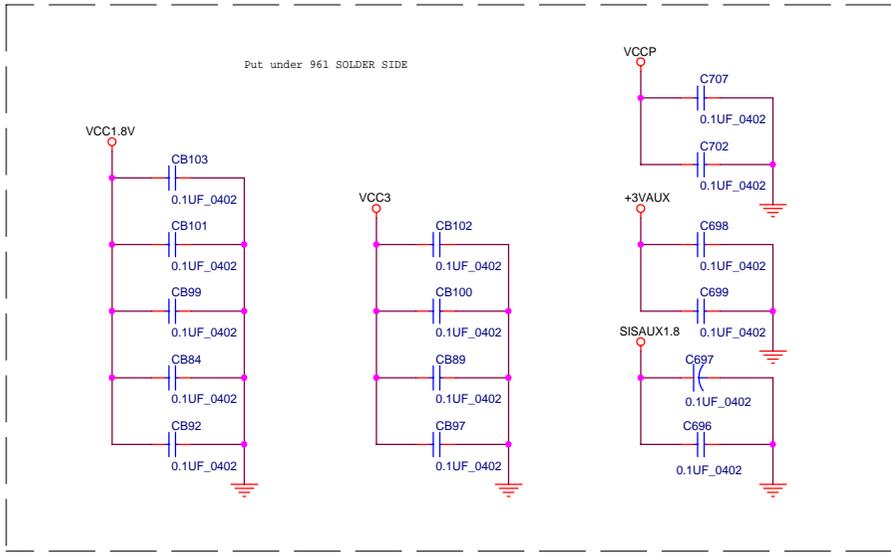
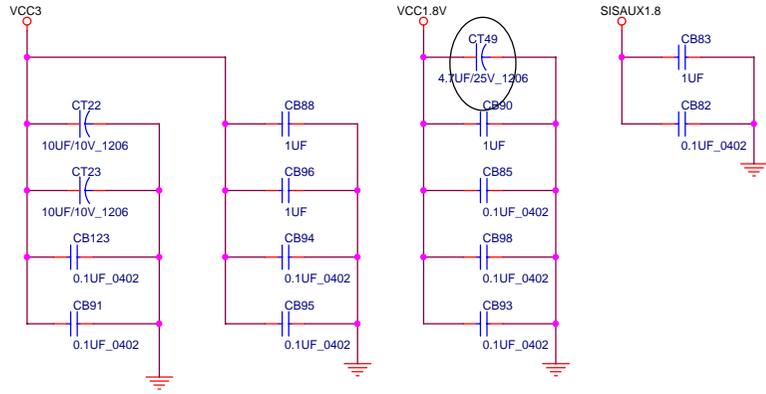


USB

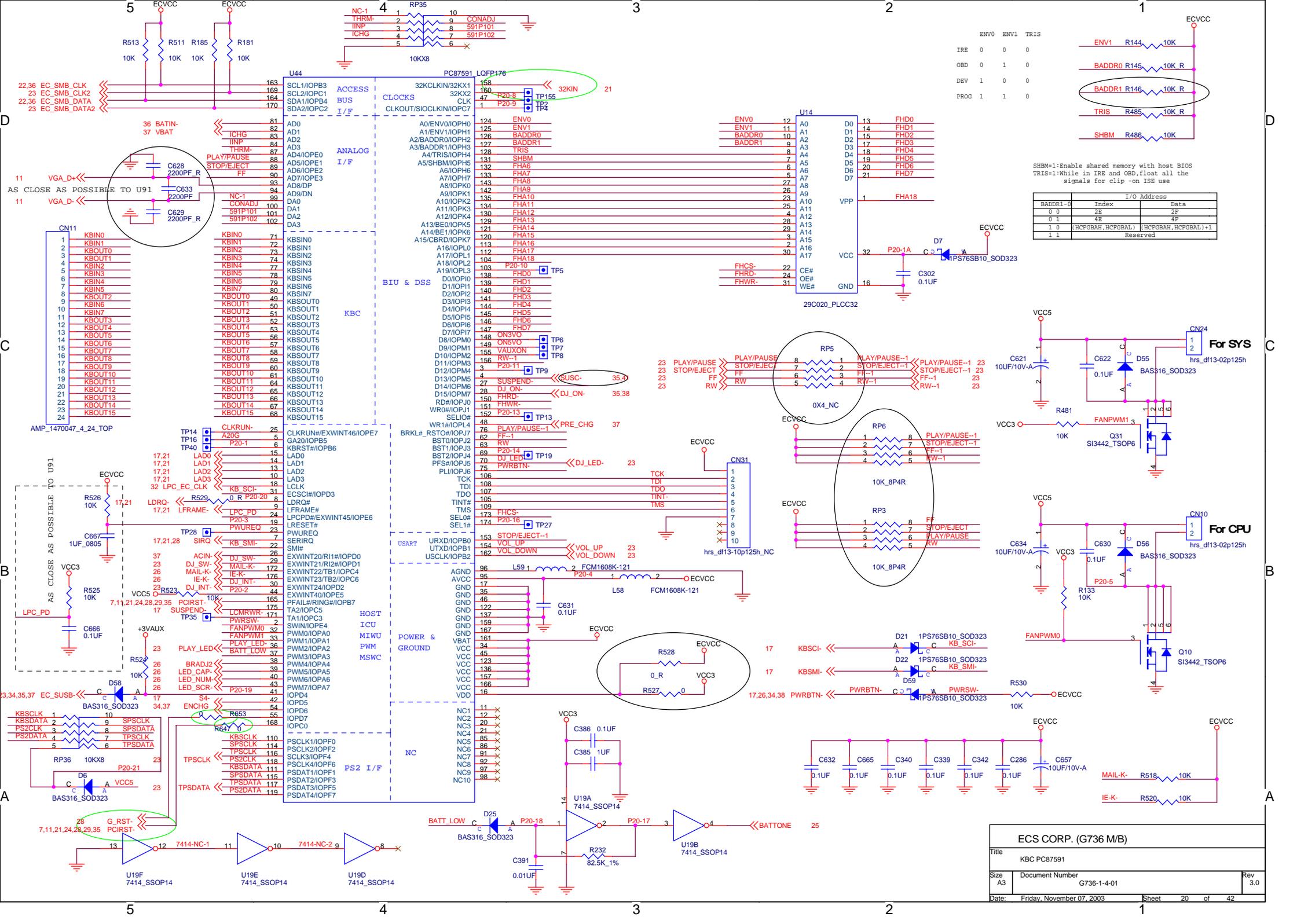
963A-3



	0	1	Default
SPKR(LPC addr mapping)	disable	enable	0
SDATO(PCICLK PLL)	enable	disable	0
OC2-(SB debug mode)	enable	disable	1
OC5-(Trap mode)	PCI AD	ROM	1



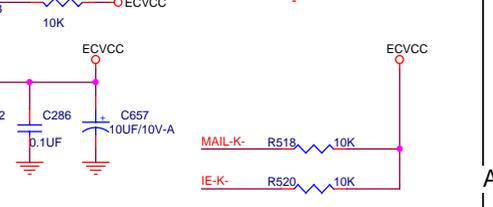
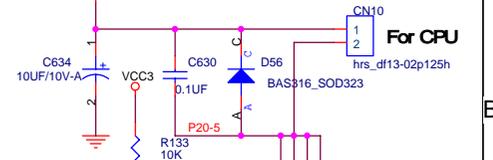
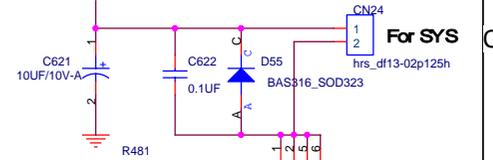
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Title SIS962-4			
Size Custom	Document Number G736-1-4-01		Rev 3.0
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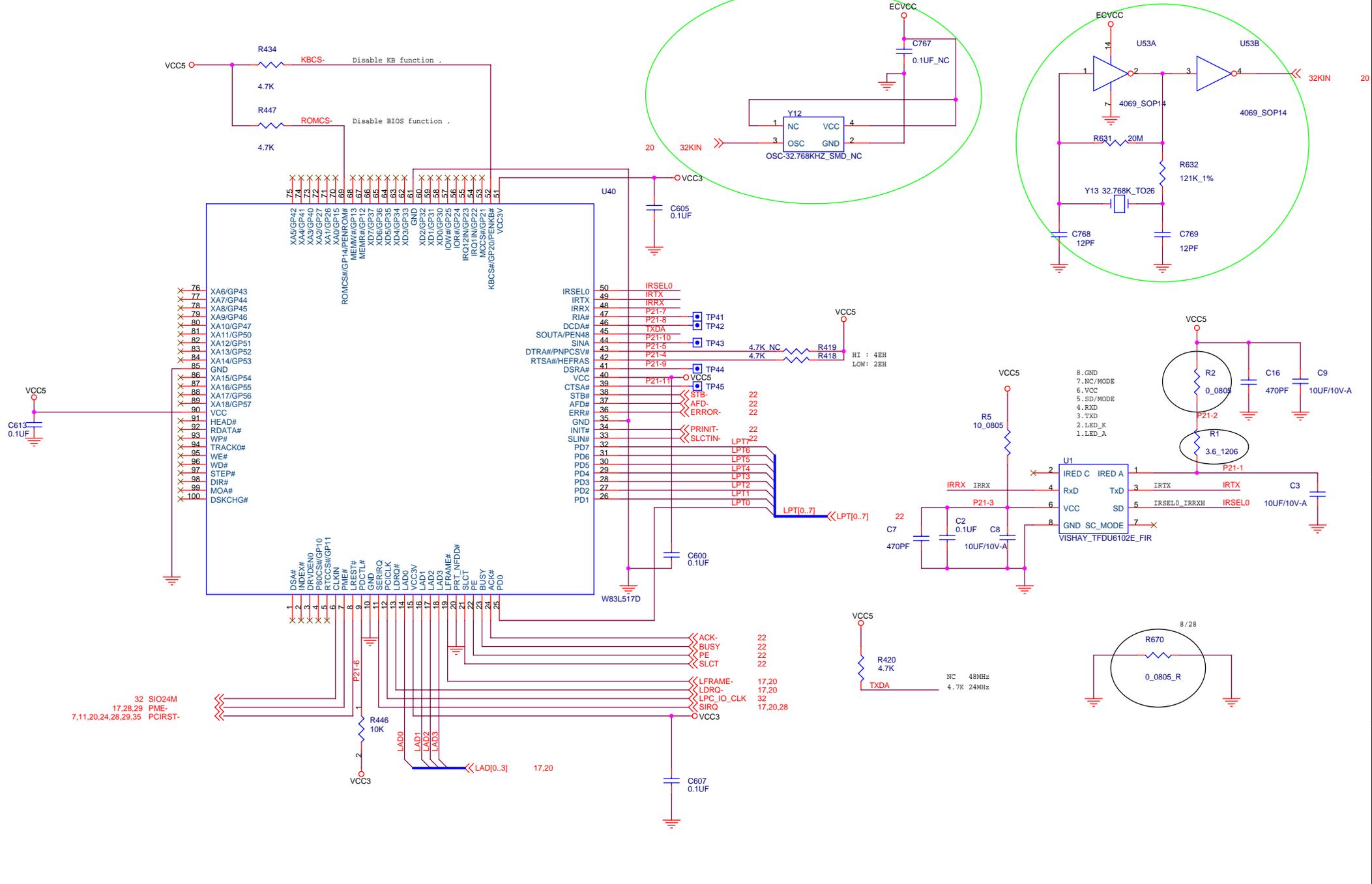
ENVO	ENV1	TRIS
IRE	0	0
OBD	0	1
DEV	1	0
PROG	1	1

SHM=1:Enable shared memory with host BIOS
 TRIS=1:While in IRE and OBD, float all the signals for clip-on ISE use

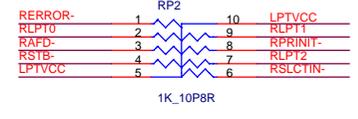
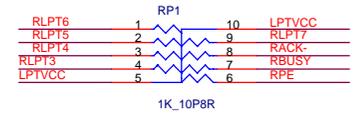
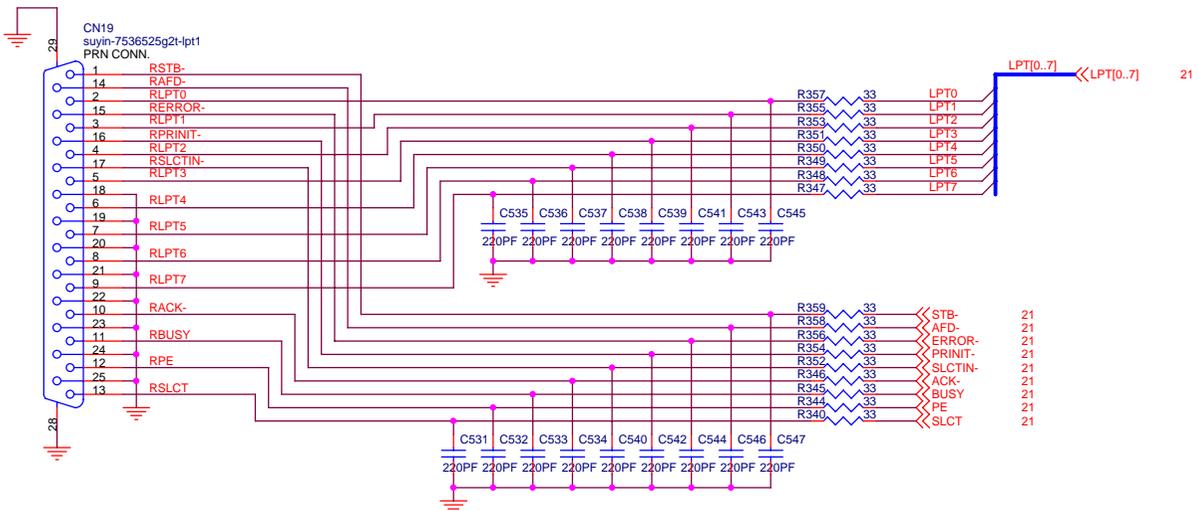
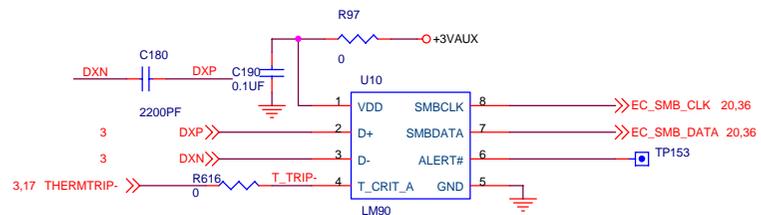
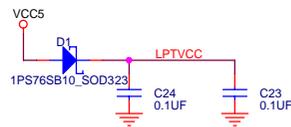
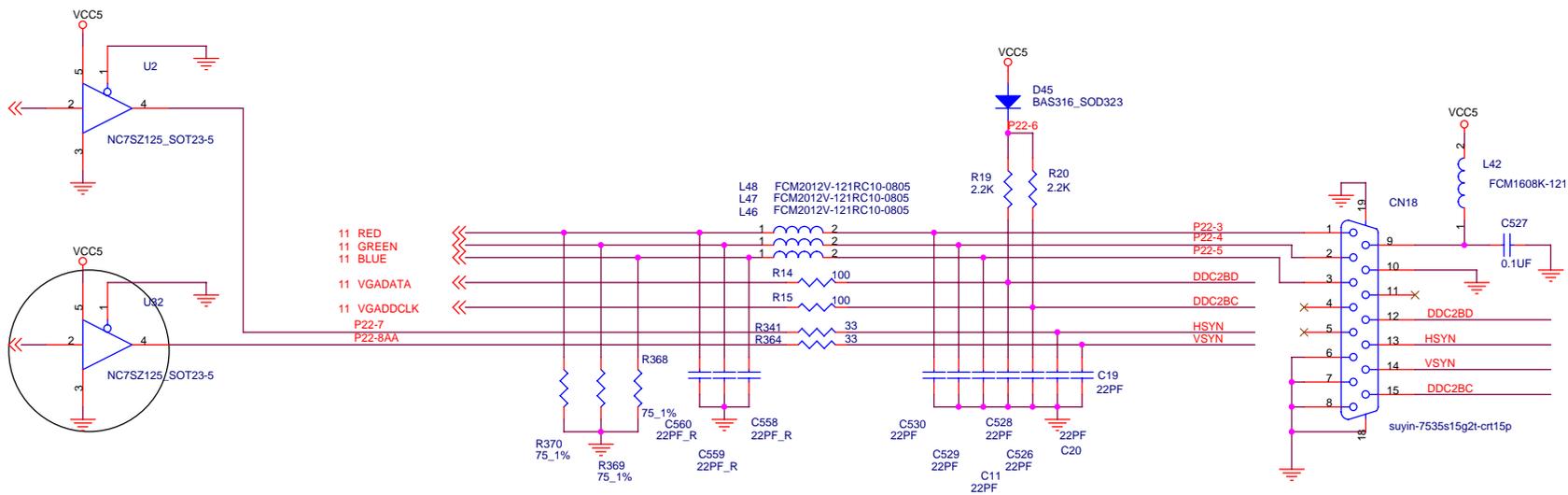
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1		Reserved



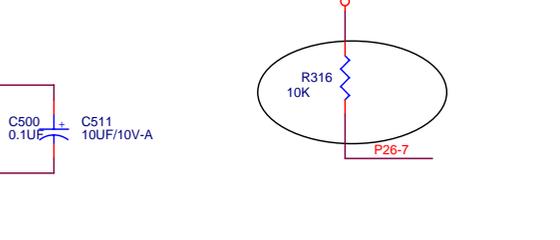
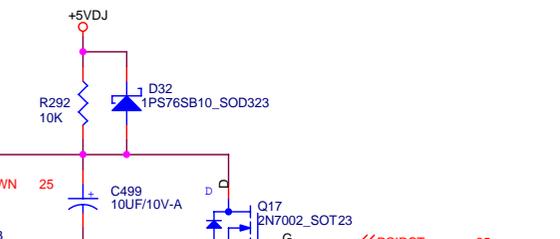
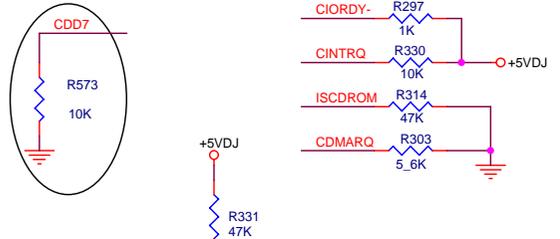
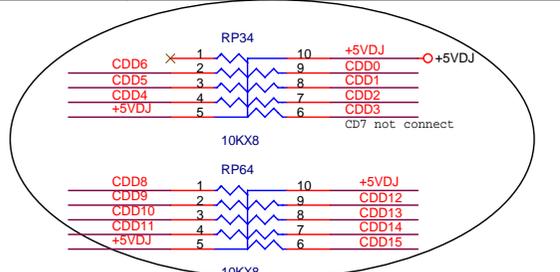
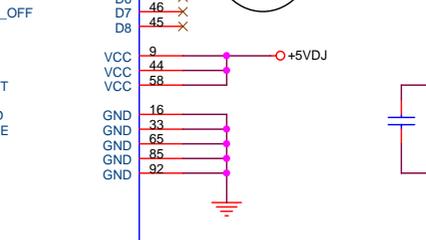
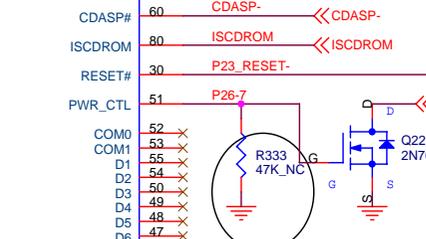
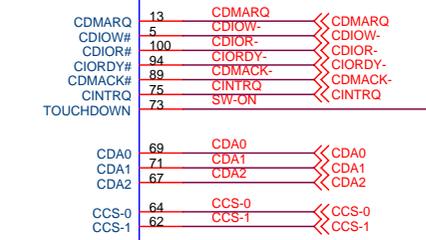
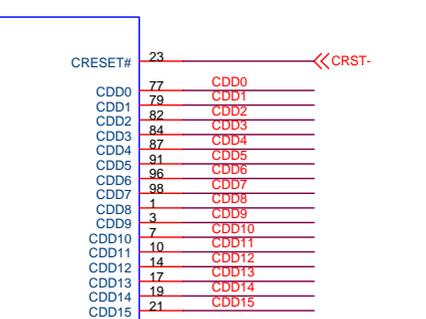
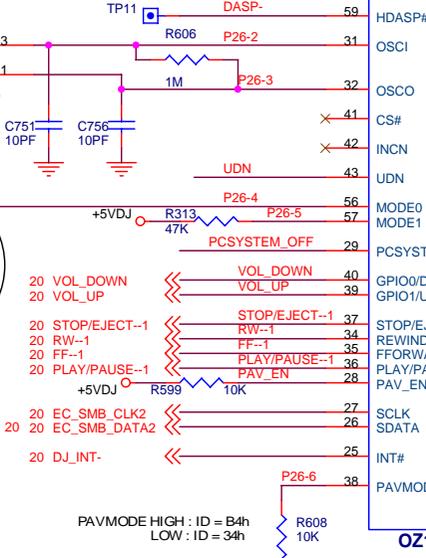
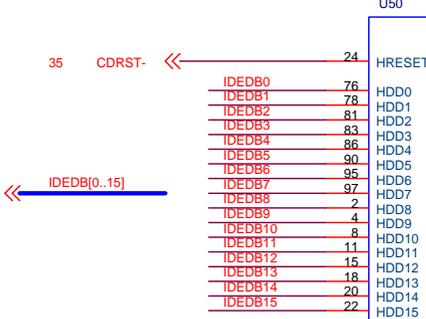
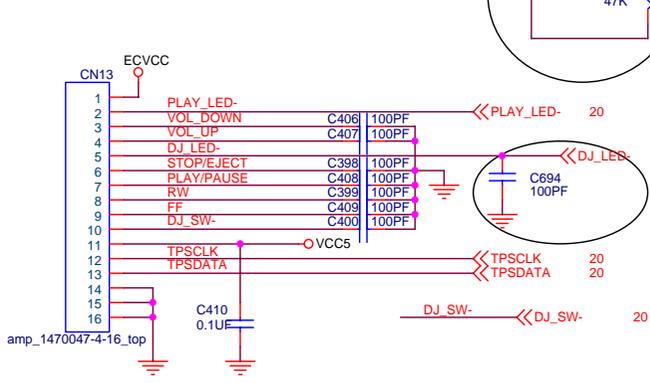
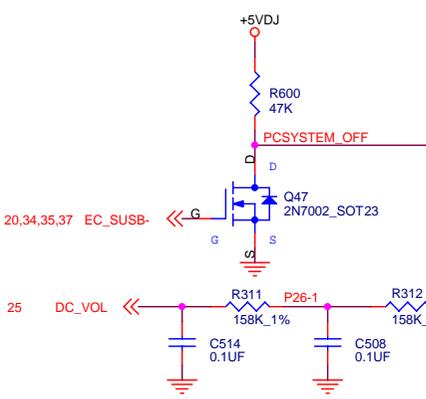
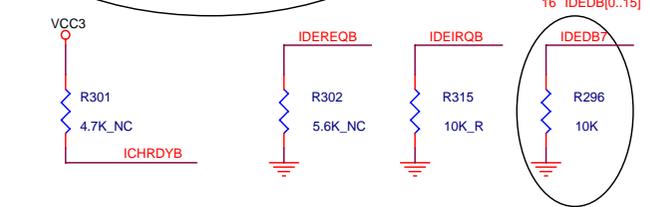
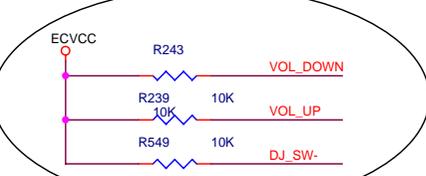
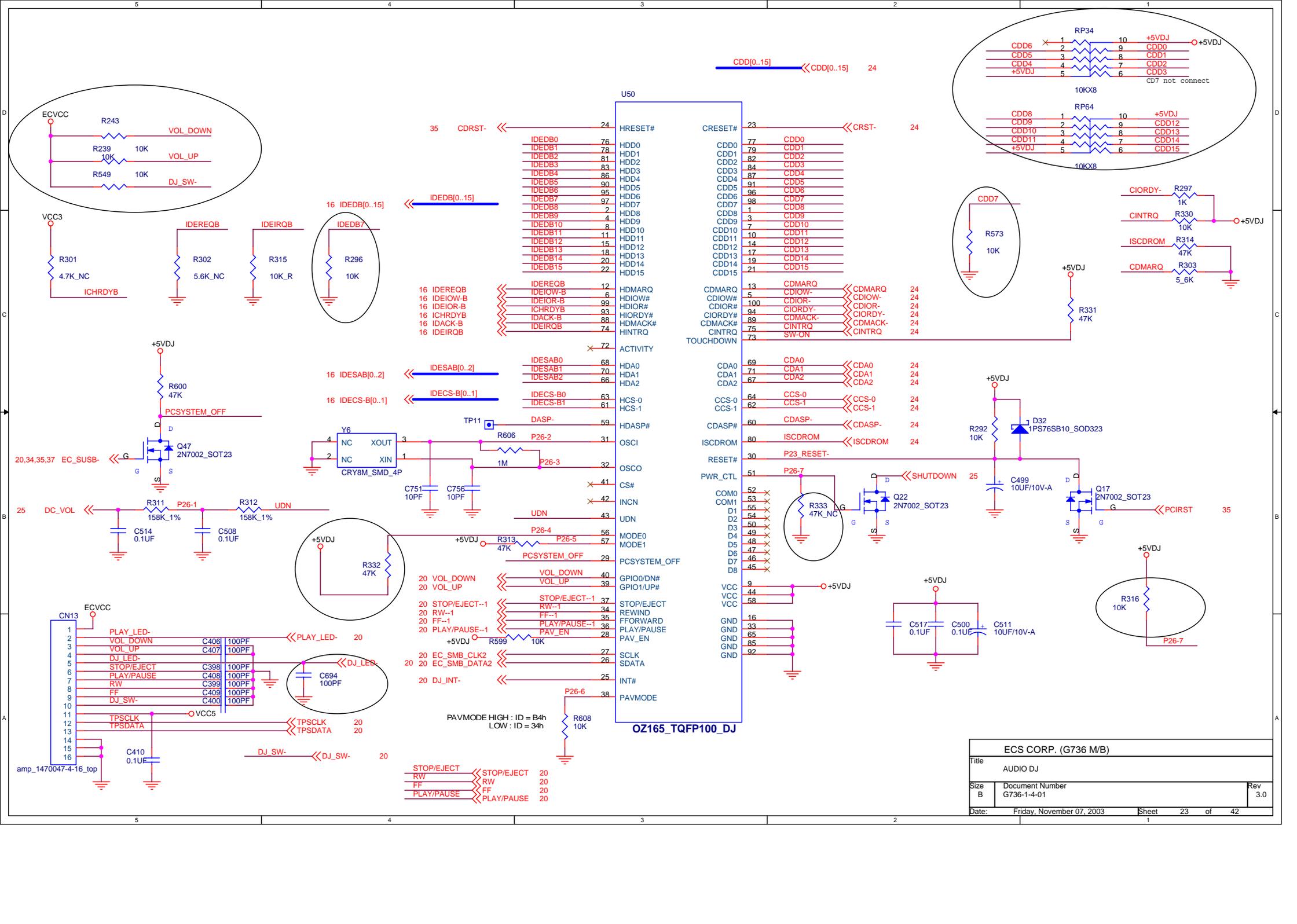
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Size	Document Number	Rev
A3	G736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 20 of 42



ECS CORP. (G736 M/B)			
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Size Custm	Document Number G736-1-4-01		Rev 3.0
Date: Friday, November 07, 2003	Sheet 21	of 42	

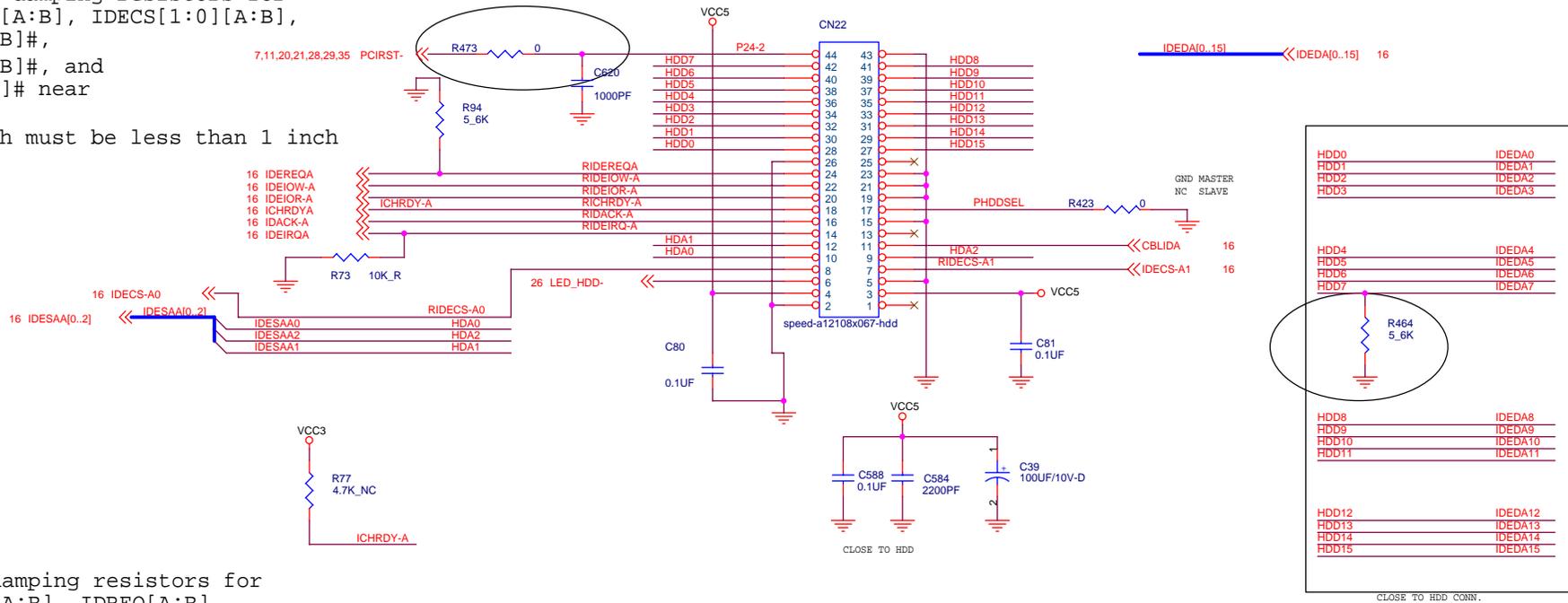


ECS CORP. (G736 M/B)		
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Size Custom	Document Number G736-1-4-01	Rev 3.0
Date: Friday, November 07, 2003	Sheet 22	of 42

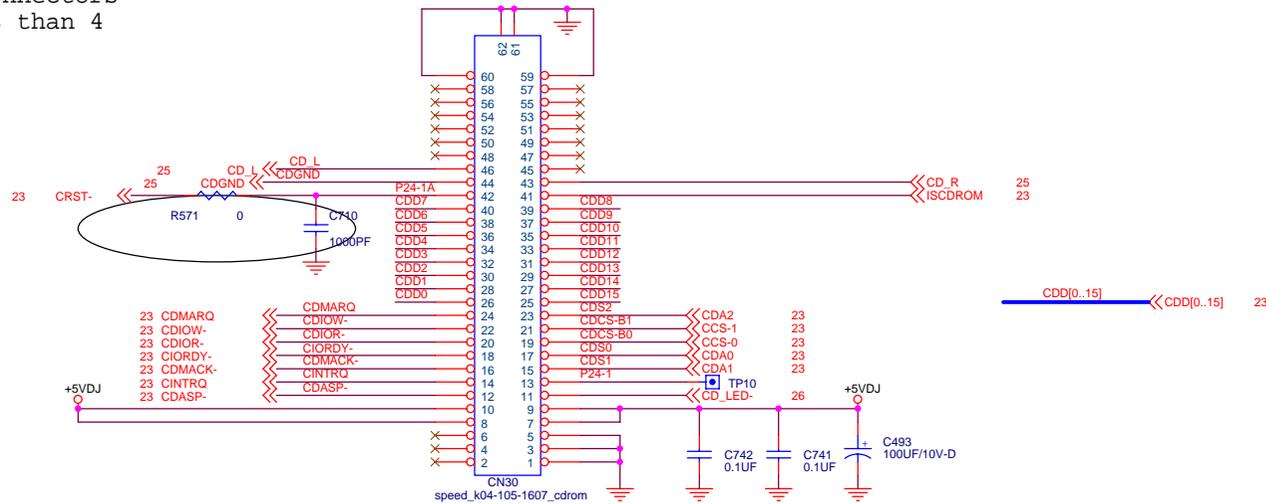


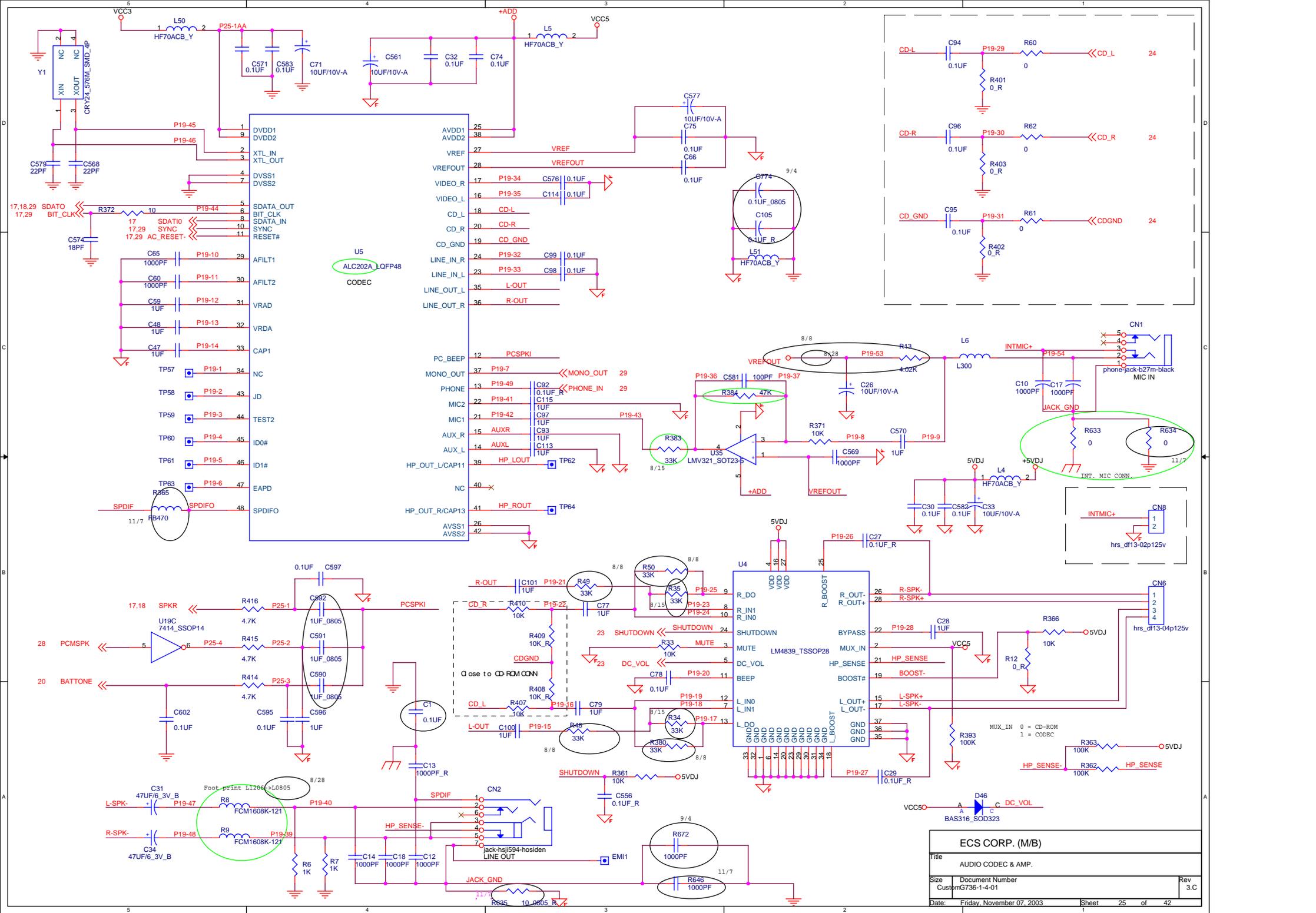
ECS CORP. (G736 M/B)		
Title	AUDIO DJ	
Size	Document Number	Rev
B	G736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 23 of 42

Place the damping resistors for
 IDEA[2:0][A:B], IDECS[1:0][A:B],
 IDEIOR[A:B]#,
 IDEIOW[A:B]#, and
 IDACK[A:B]# near
 SiS650
 The length must be less than 1 inch

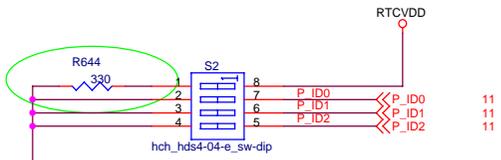


Place the damping resistors for
 IDED[15:0][A:B], IDREQ[A:B],
 ICHRDY[A:B], and
 IDEIRQ[A:B] near IDE connectors
 The length must be less than 4
 inches



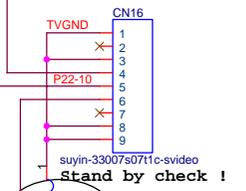
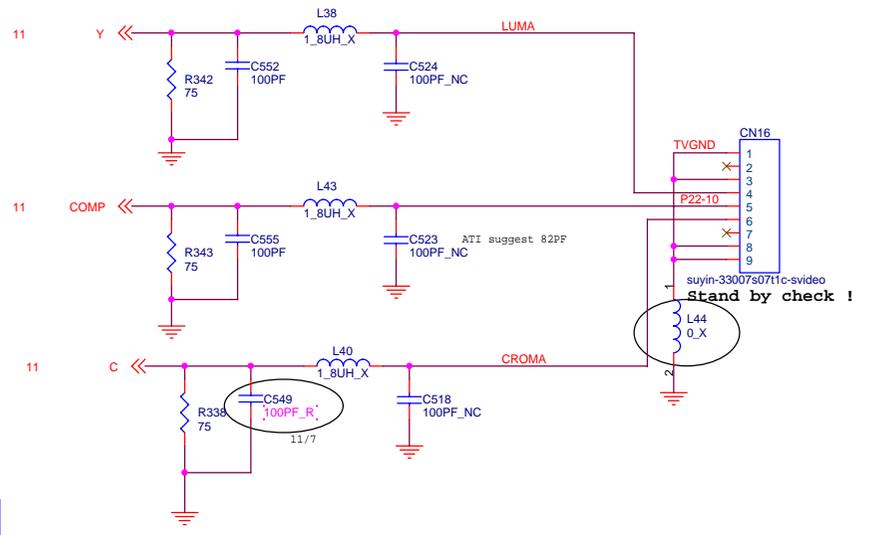
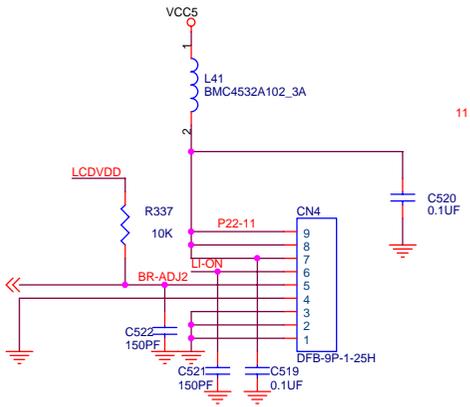
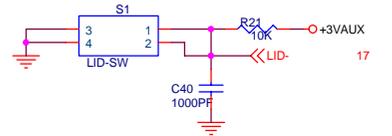
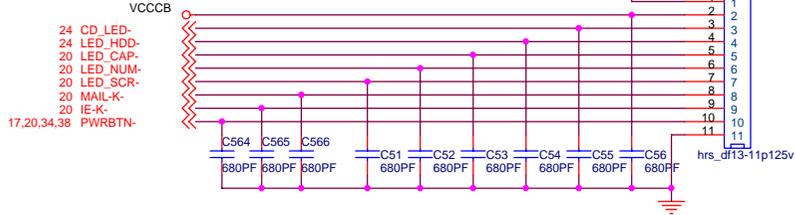
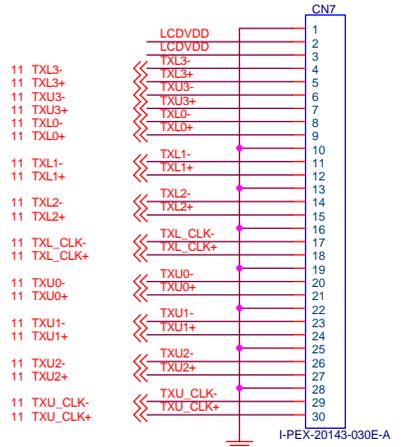
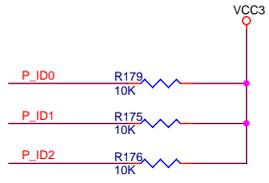


ECS CORP. (M/B)		
Title AUDIO CODEC & AMP.		
Size	Document Number	Rev
Custom	G736-1-4-01	3.C
Date:	Friday, November 07, 2003	Sheet 25 of 42



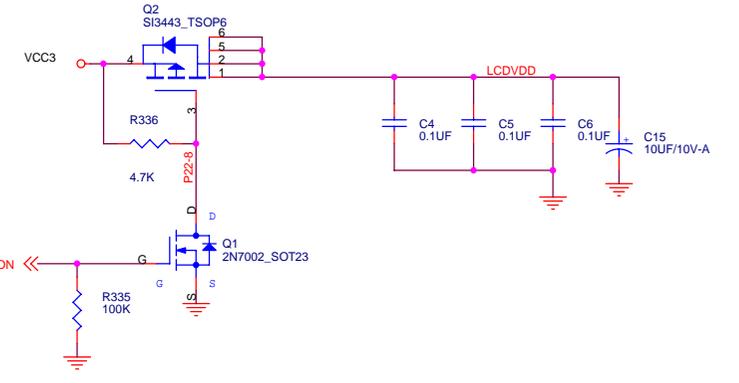
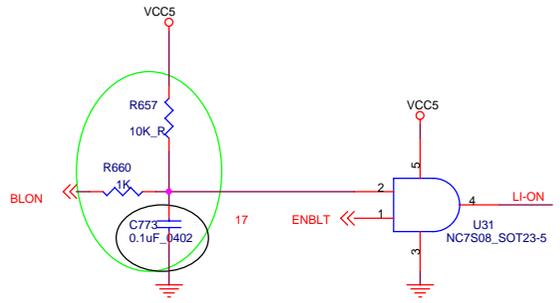
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OFF	OFF	ON	WXGA+
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11/3



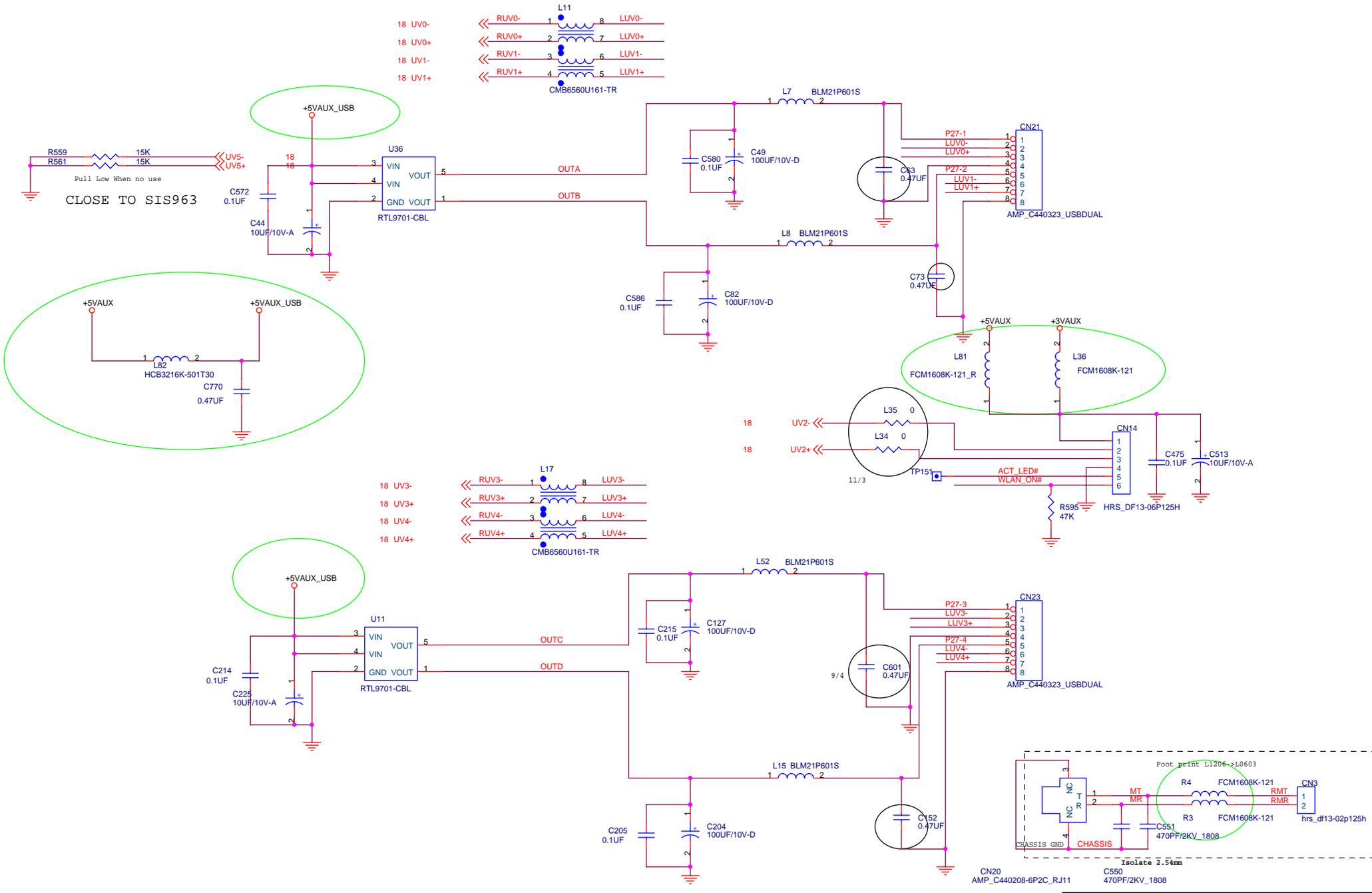
stand by check !

Signal channel : L
Dual channel : L+U



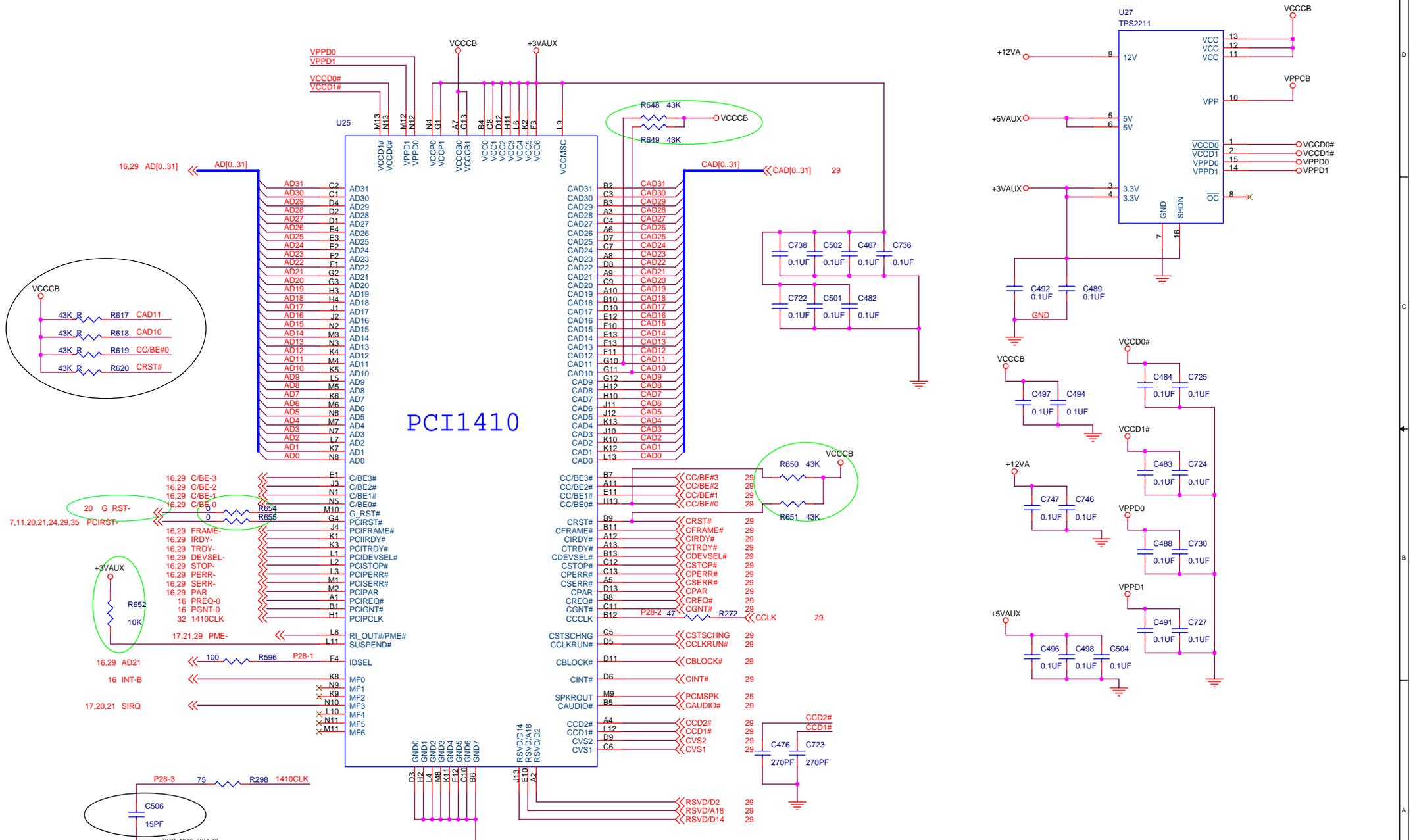
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Title LCD CRT & TV CONNECTOR	
Size CustomG736-1-4-01	Document Number Rev 3.C
Date: Friday, November 07, 2003	Sheet 26 of 42

P22-11



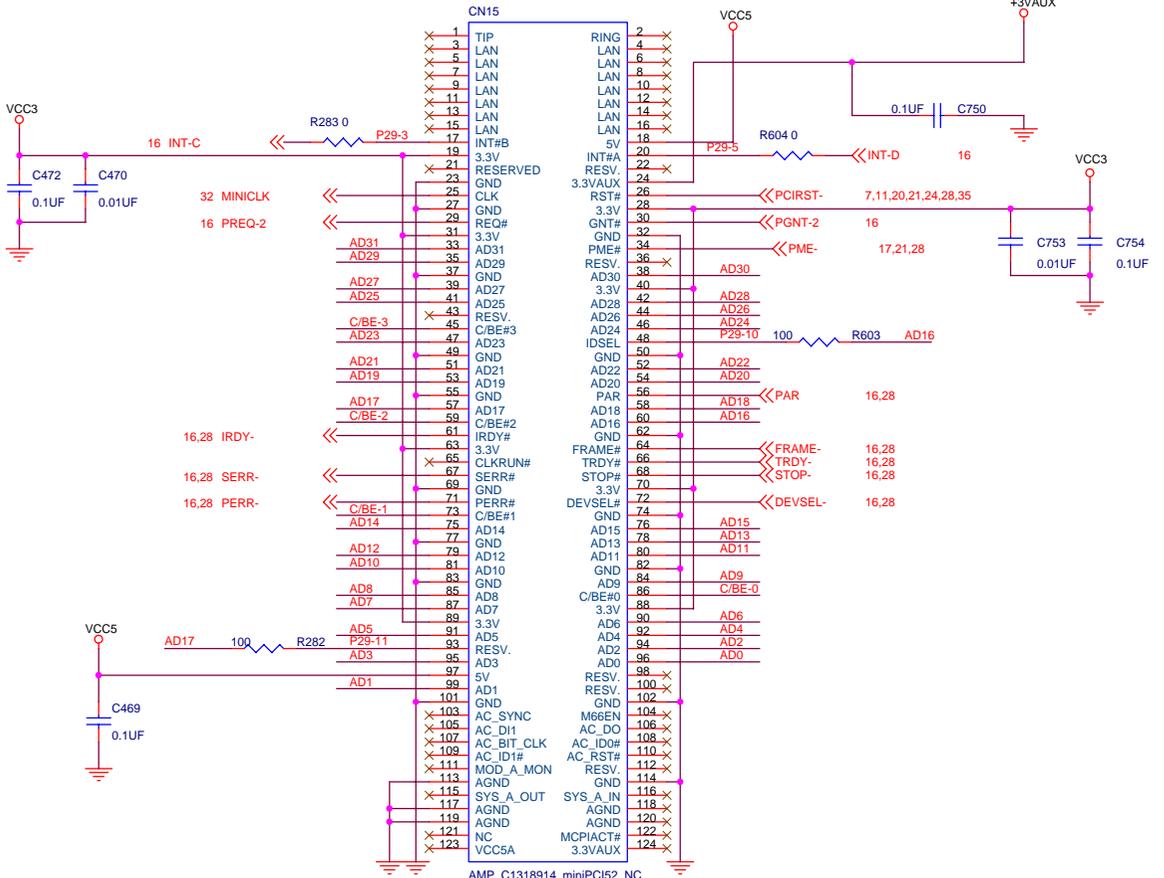
ECS CORP. (G736 M/B)			
Title USB CONN & RJ11			
Size Custom	Document Number G736-1-4-01		Rev 3.B
Date: Friday, November 07, 2003	Sheet 27	of 42	

PCI1410

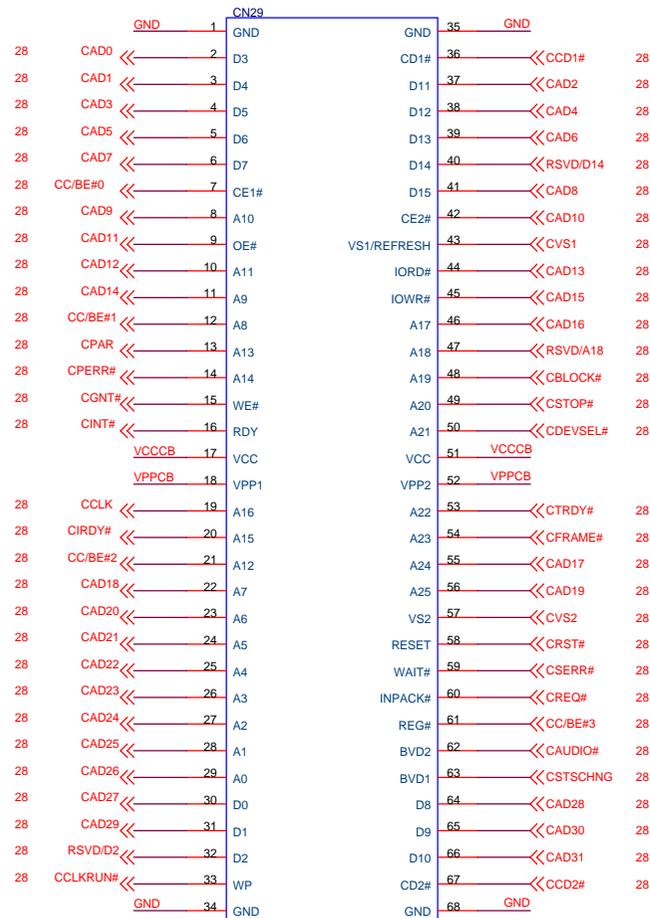


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Size	Document Number	Rev
Custom	G736-1-4-01	3.0
Date:	Friday, November 07, 2003	Sheet 28 of 42

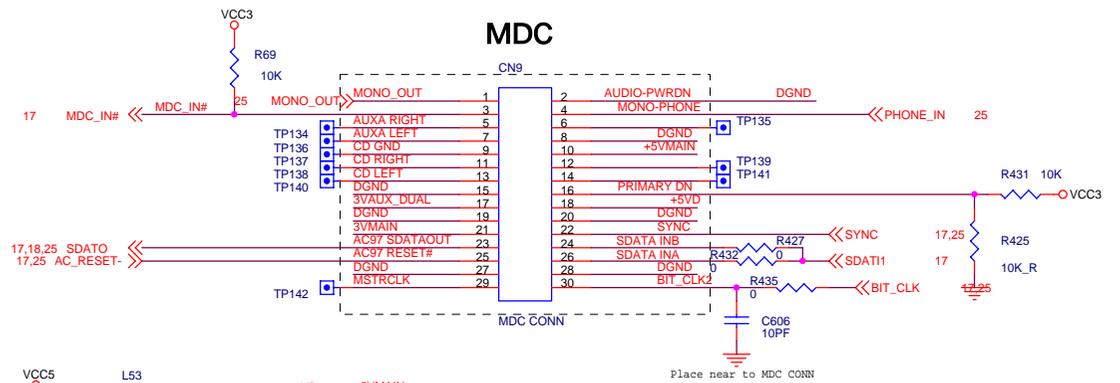
MINI PCI SOCKET



PCMCIA SLOT

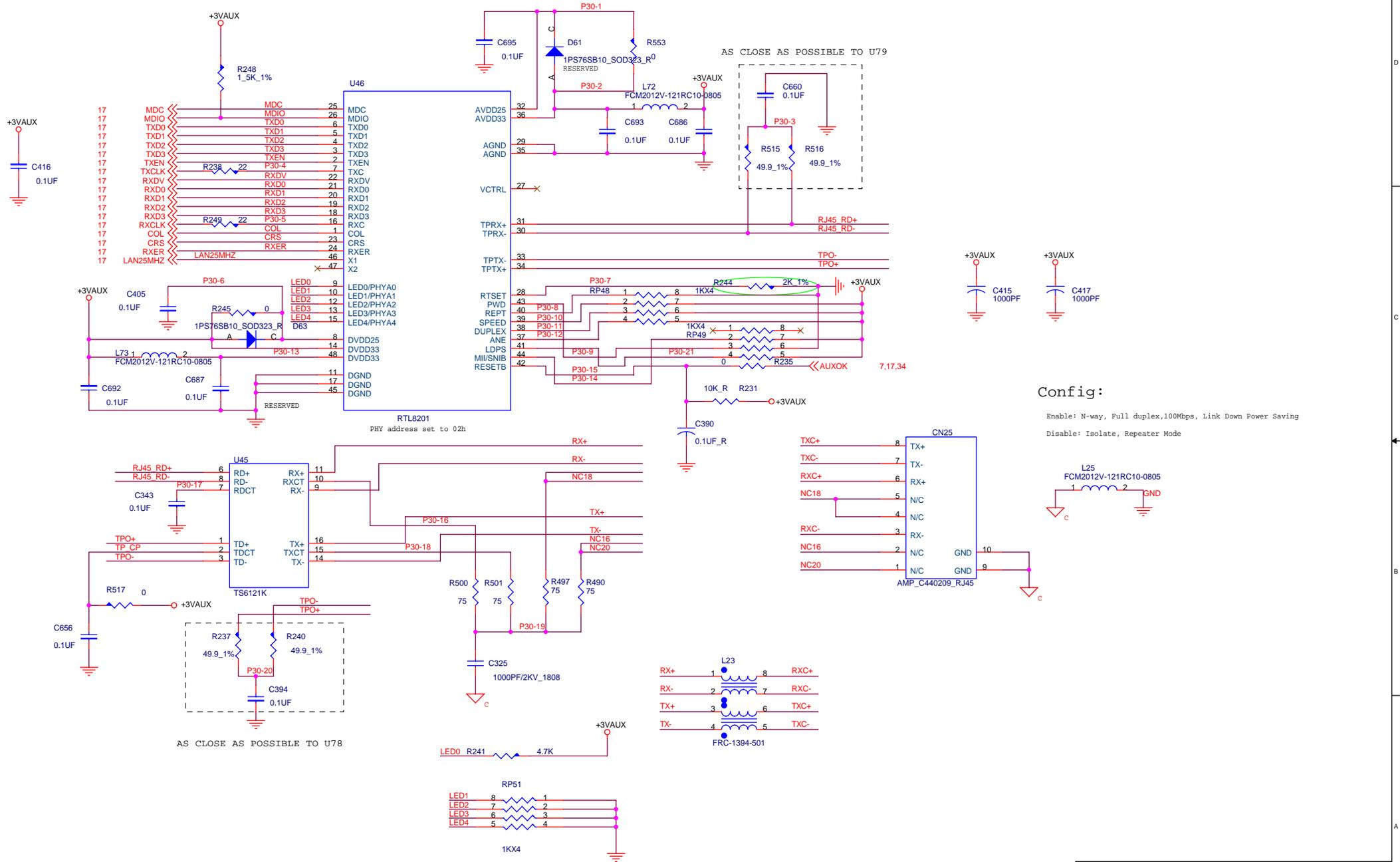


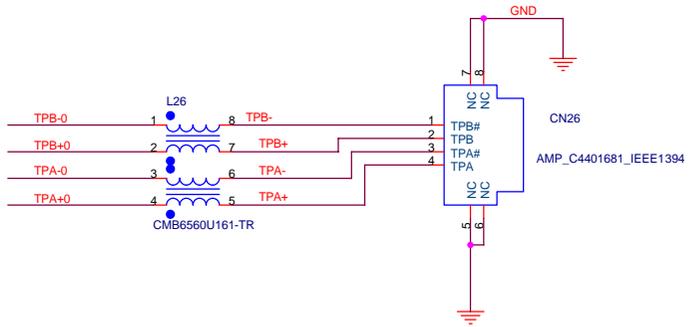
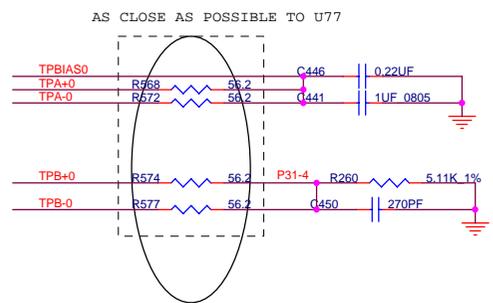
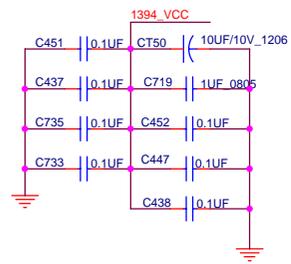
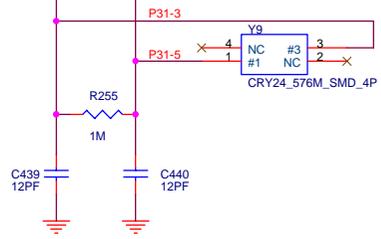
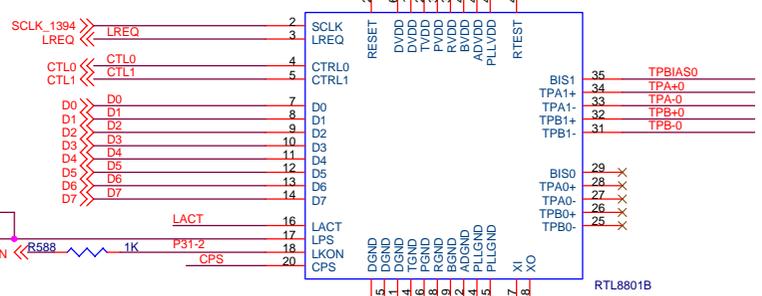
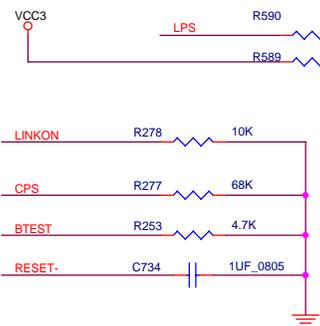
MDC



ECS CORP. (G736 M/B)		
Title MINI PCI & PCMCIA SLOT		
Size Custom	Document Number G736-1-4-01	Rev 3.0
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RTL8201

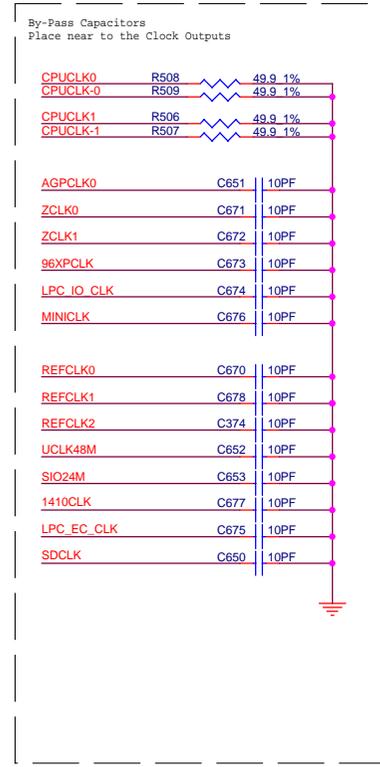
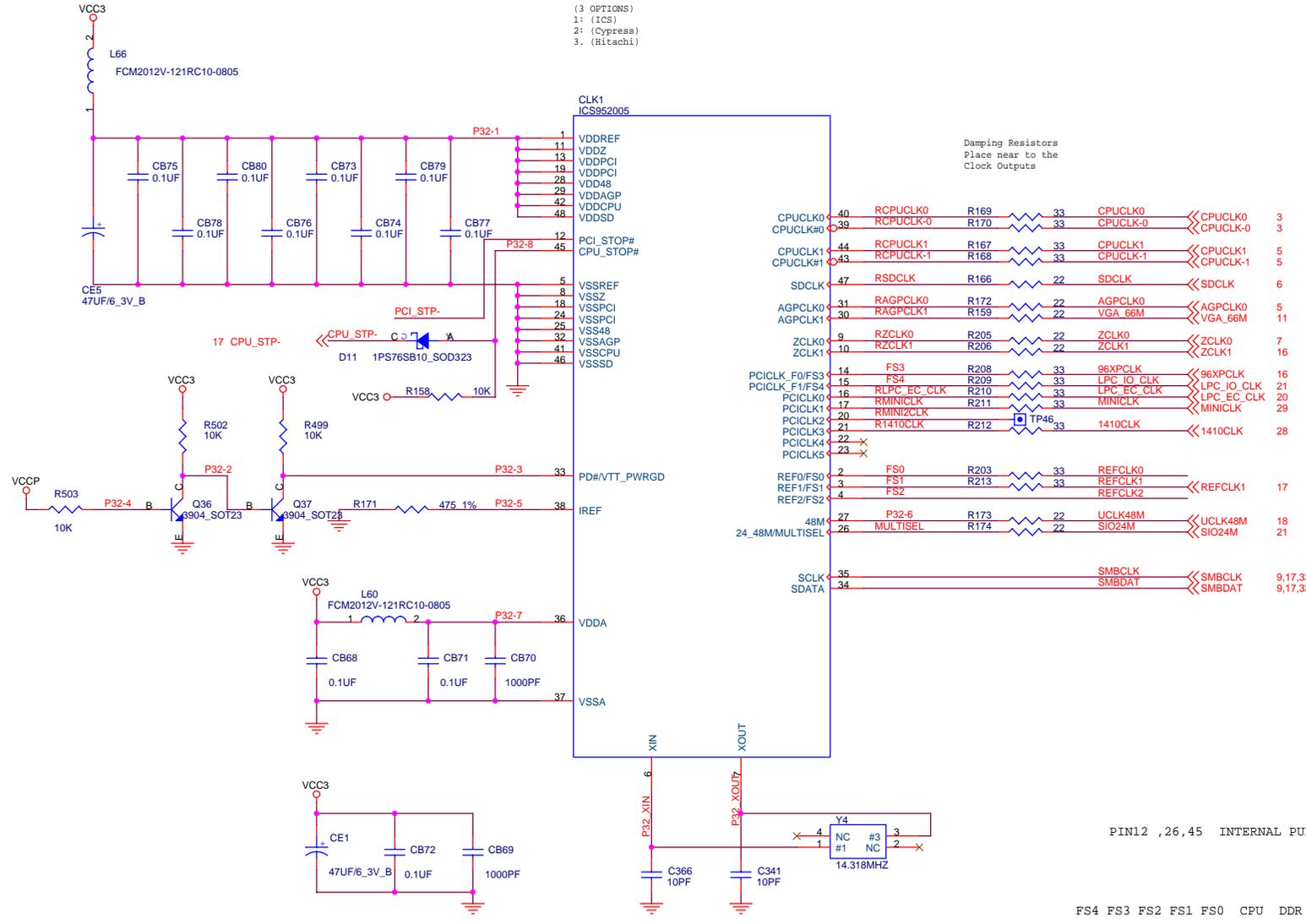




ECS CORP. (G736 M/B)		
Title	IEEE 1394	
Size	Document Number	Rev
Custom	G736-1-4-01	3.B
Date:	Friday, November 07, 2003	Sheet 31 of 42

Main Clock Generator

- (3 OPTIONS)
 1: (ICS)
 2: (Cypress)
 3: (Hitachi)



Damping Resistors
Place near to the
Clock Outputs

PIN12 ,26,45 INTERNAL PULL-UP

	FS4	FS3	FS2	FS1	FS0	CPU	DDR	AGP	PCI	ZCLK
1	0	0	0	0	1	100	133	66	33	133
1	0	1	0	0	1	133	133	66	33	133

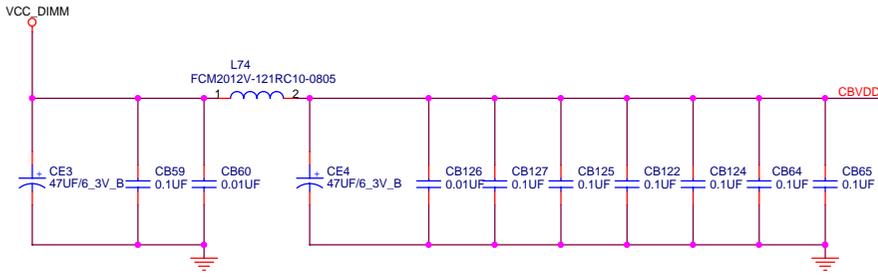


ECS CORP. (G736 M/B)

Title: CLOCK GENERATOR

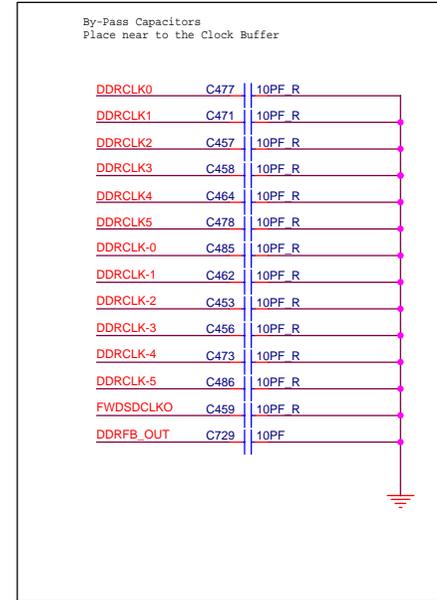
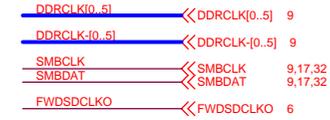
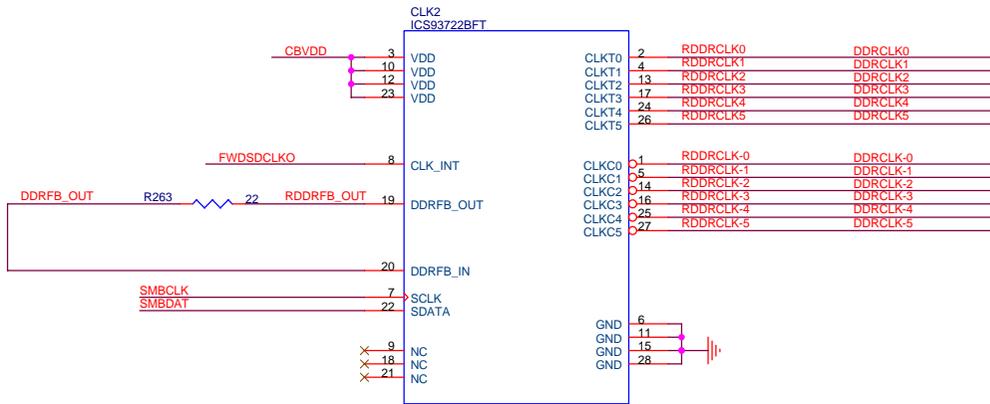
Size	Document Number	Rev
Custom	G736-1-4-01	3.0

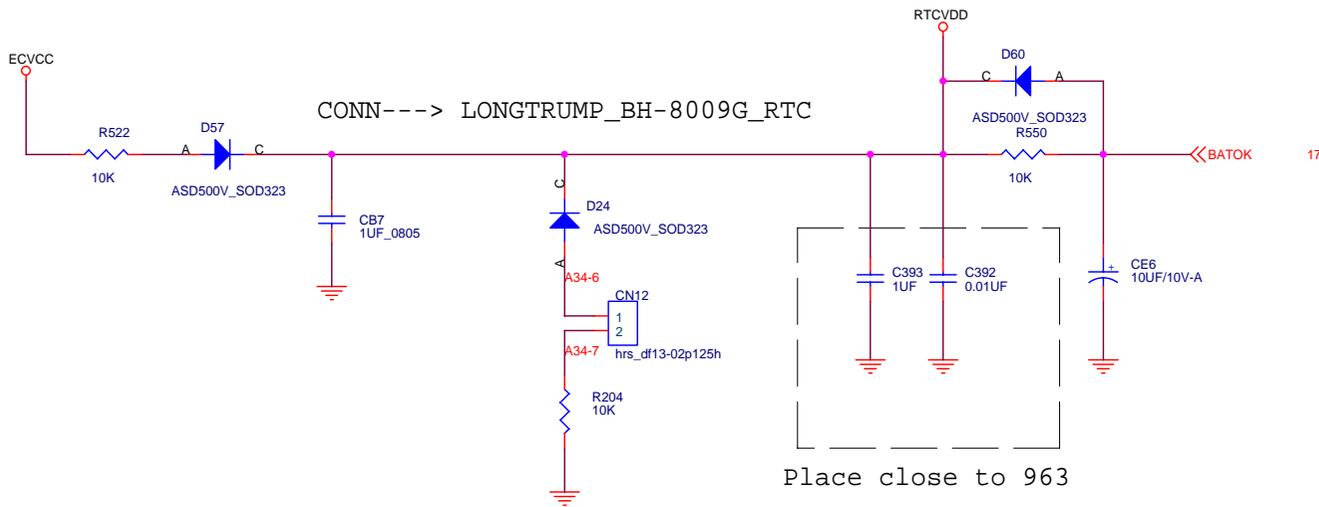
Date: Friday, November 07, 2003 Sheet 32 of 42



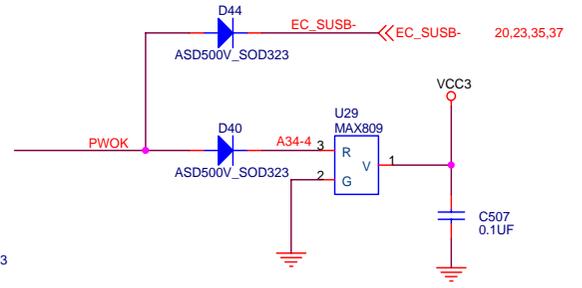
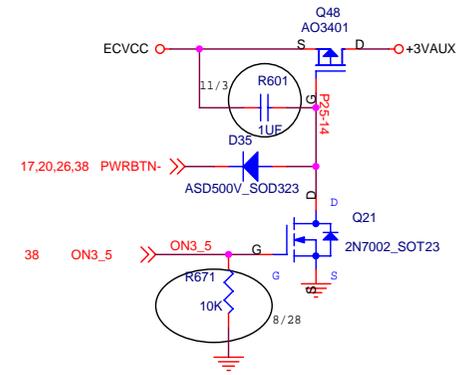
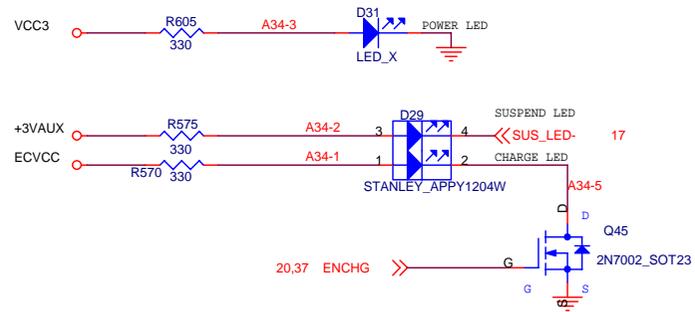
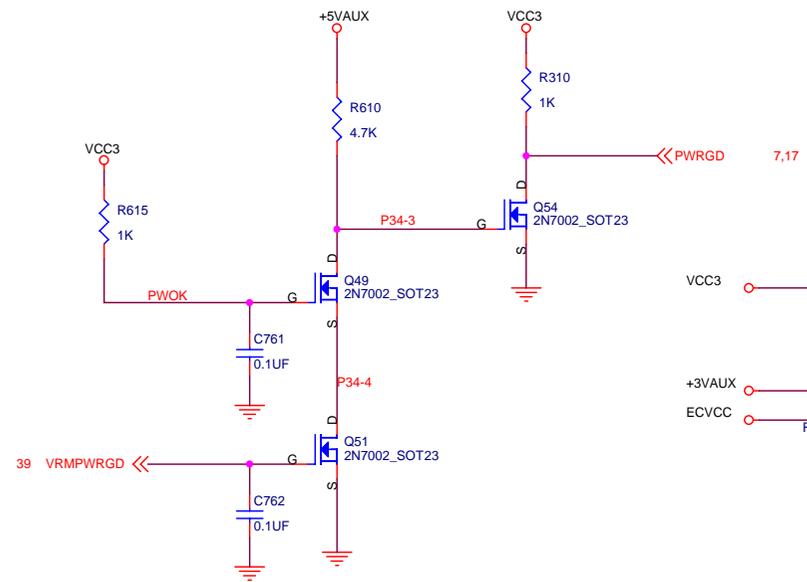
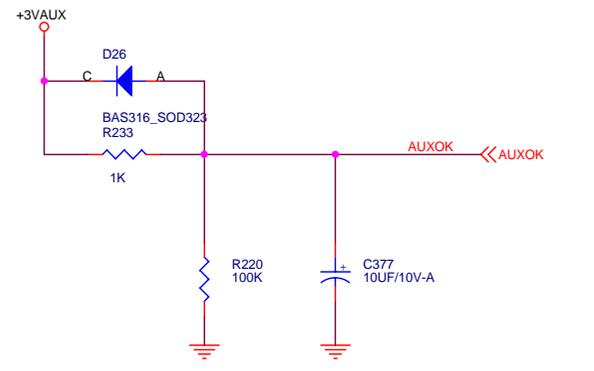
Clock Buffer (DDR)

(OPTIONS)
1: (ICS-93705)

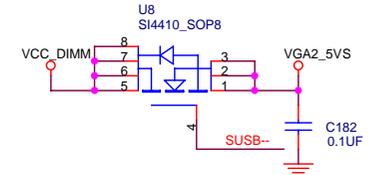
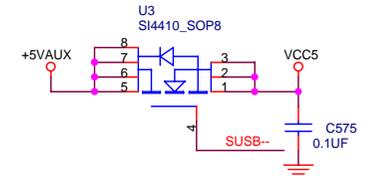
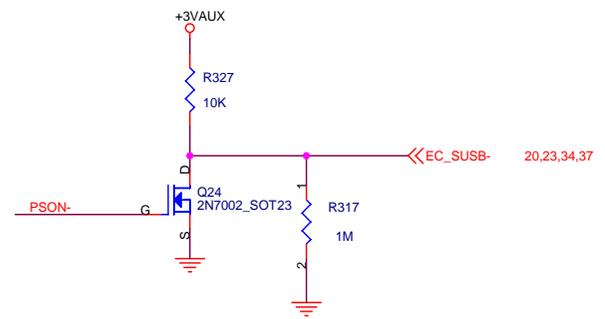
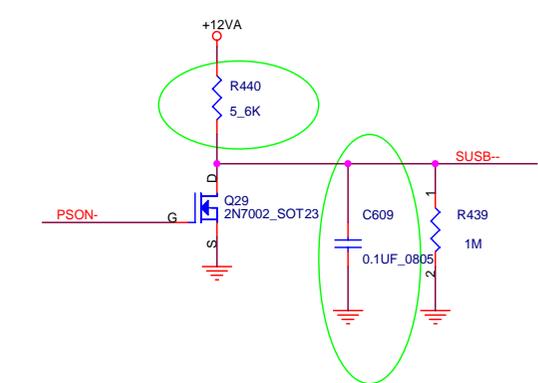
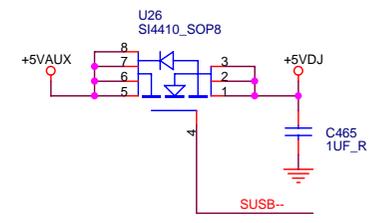
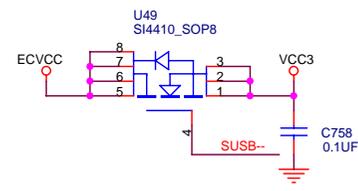
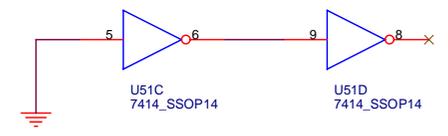
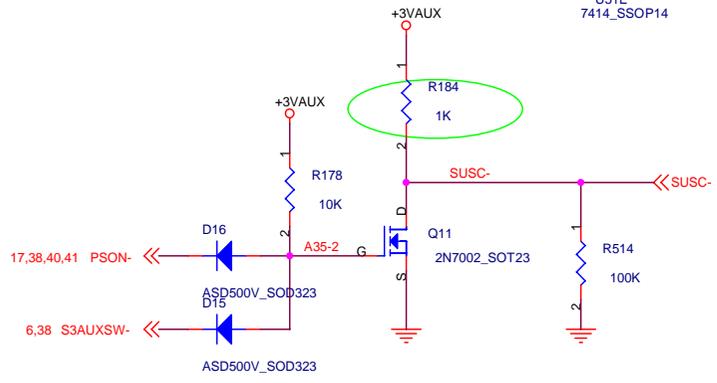
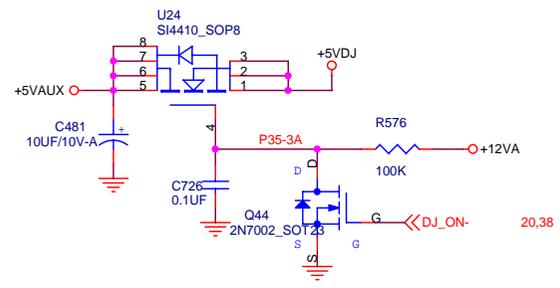
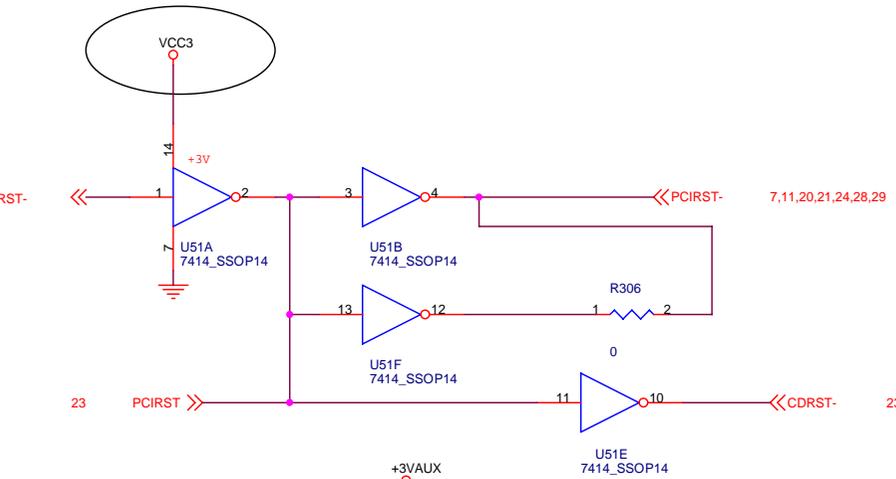




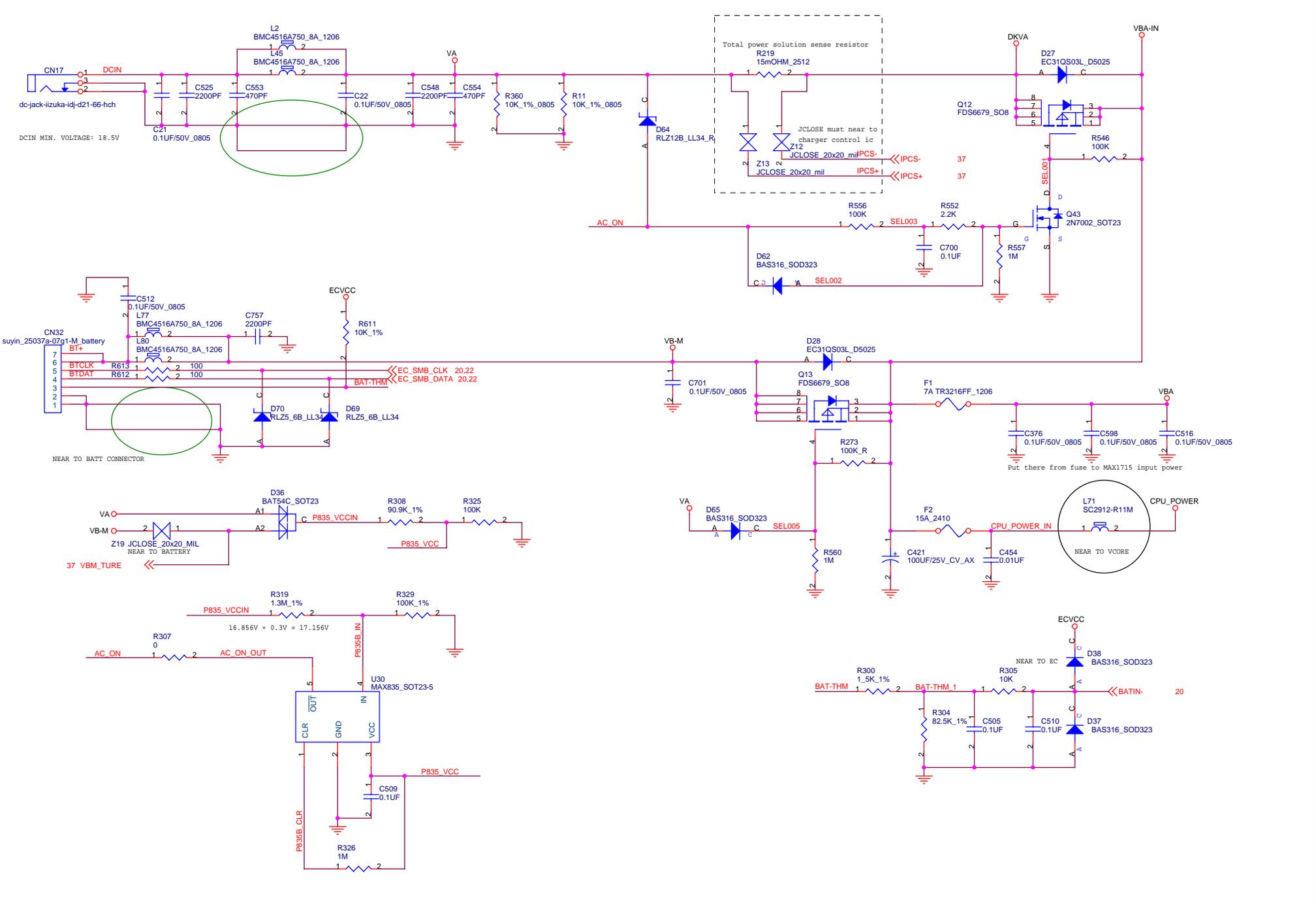
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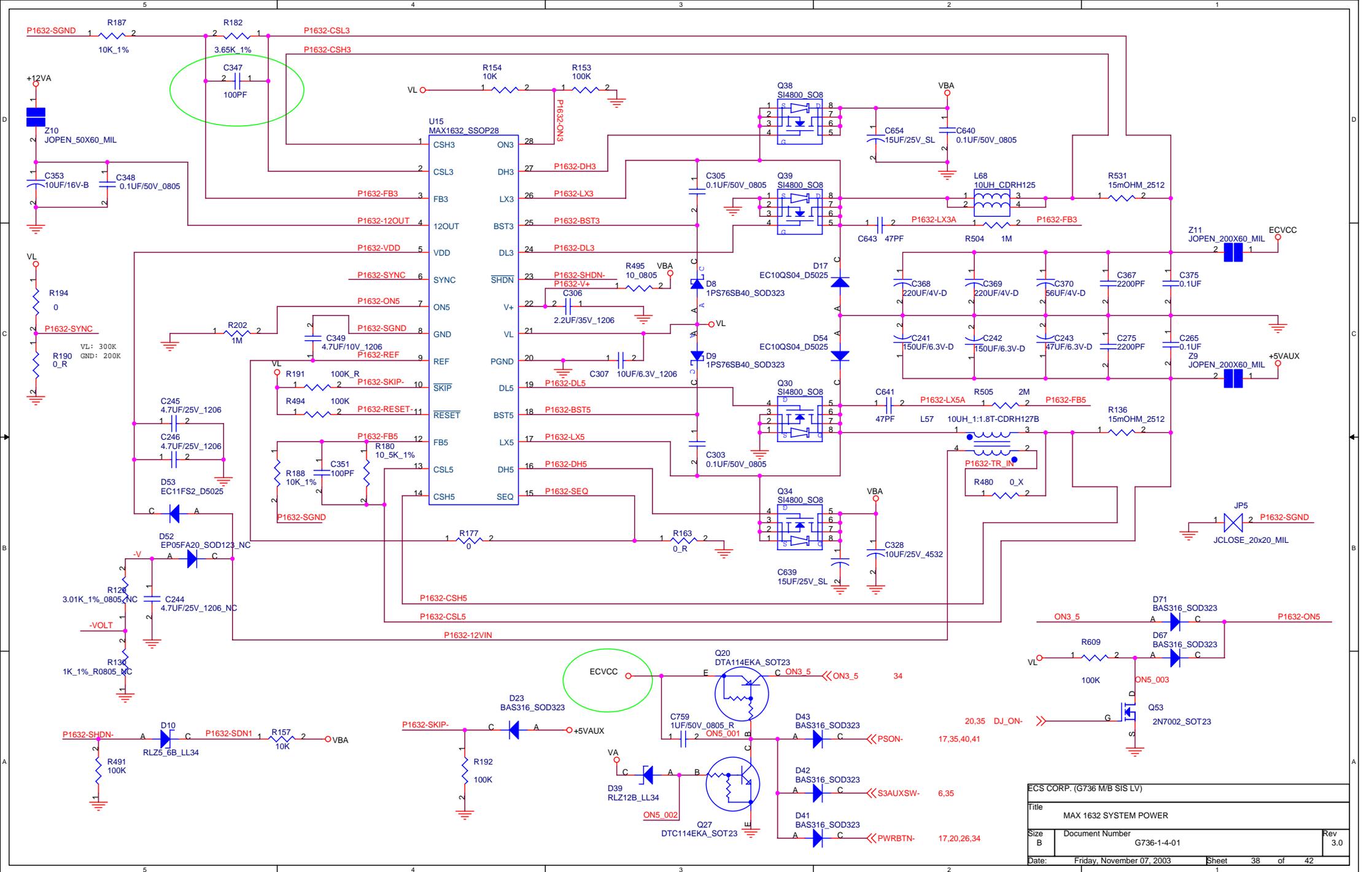
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Title RTC & POWER SWITCH		
Size B	Document Number G736-1-4-01	Rev 3.A
Date: Friday, November 07, 2003	Sheet 34	of 42



ECS CORPORATION		
Title POWER SWITCH		
Size B	Document Number G736-1-4-01	Rev 3.0
Date: Friday, November 07, 2003	Sheet 35	of 42

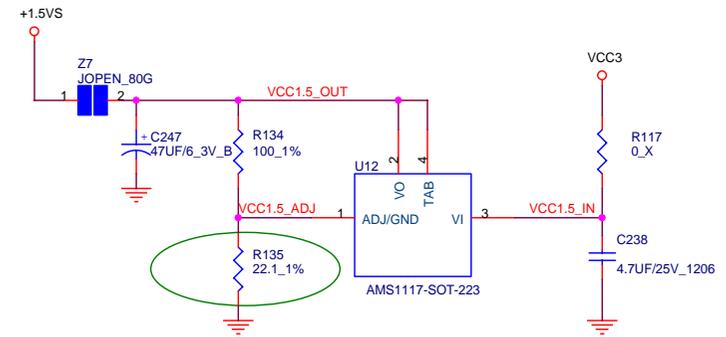
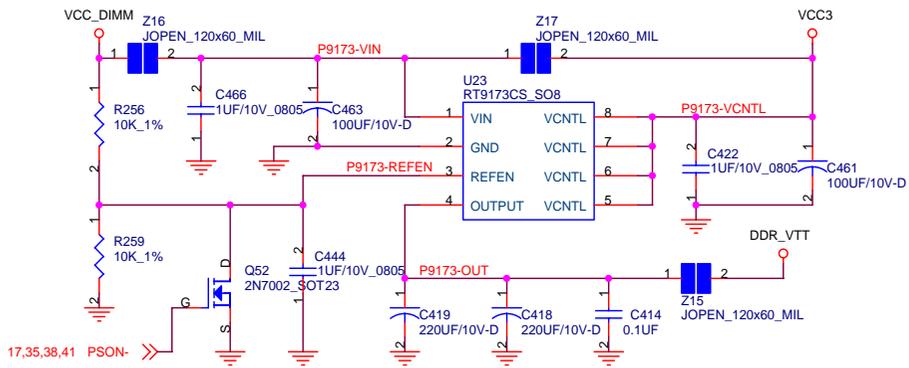
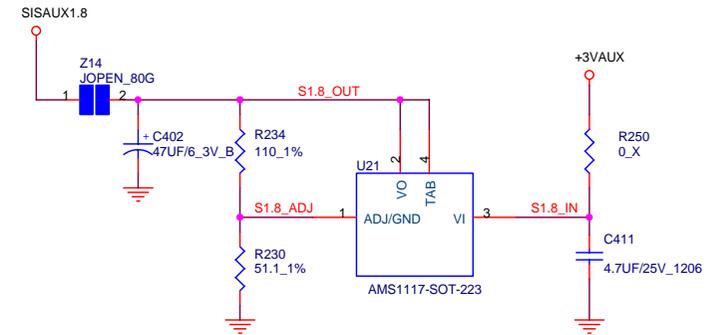
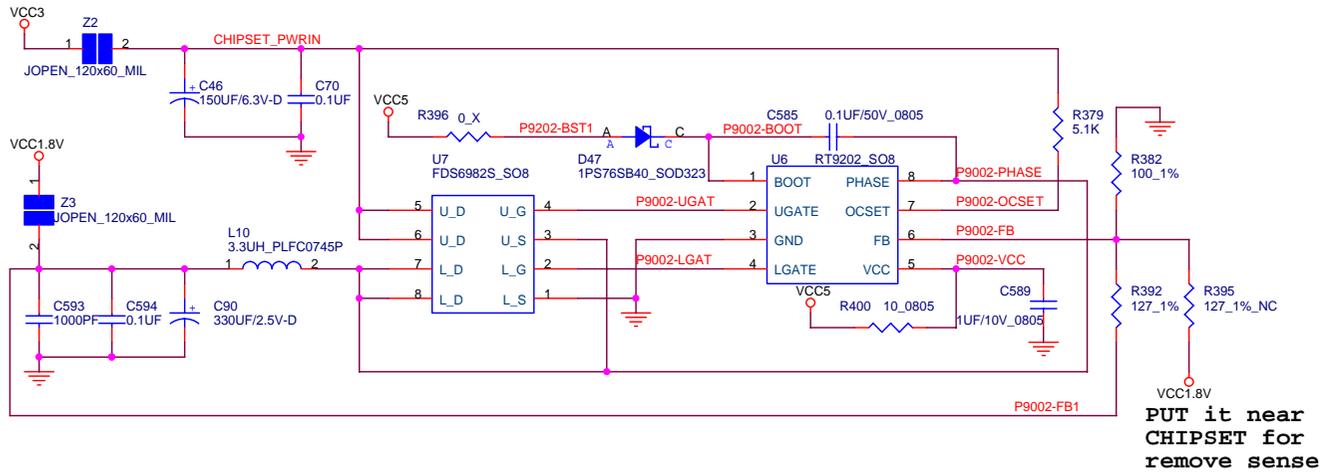


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Size Custom	Document Number G736-1-4-01	Rev 3.0
Date:	Friday, November 07, 2003	Sheet 36 of 42

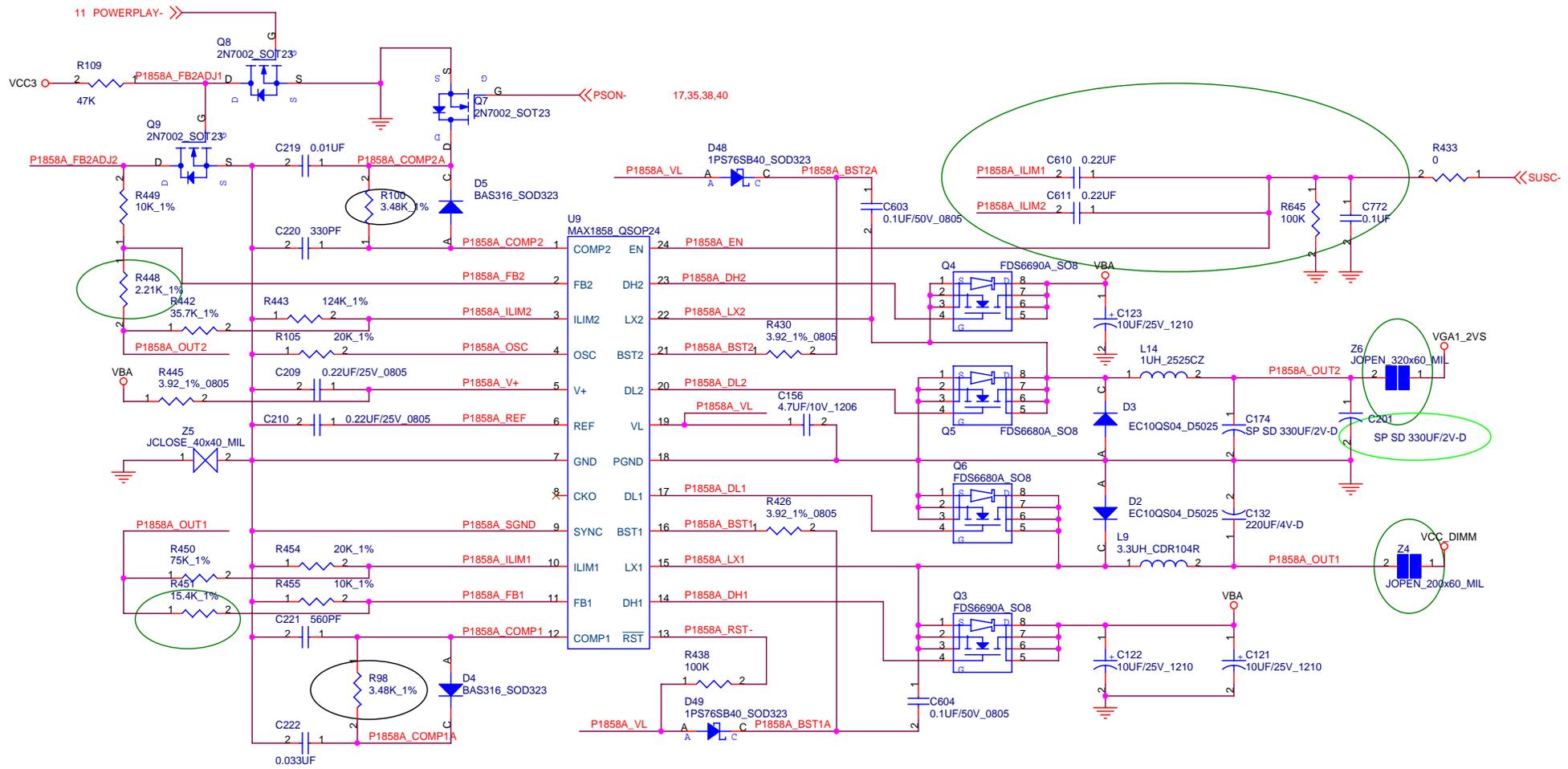


ECS CORP. (G736 M/B SIS LV)		
Title MAX1632 SYSTEM POWER		
Size B	Document Number G736-1-4-01	Rev 3.0
Date: Friday, November 07, 2003	Sheet 38	of 42

MP5V/3VAUX/±12VA SUPPLY



ECS CORP. (G736 M/B SIS LV)		
Title RT9202 VCC1.8V / RT9173 DDRVTT / LDO		
Size B	Document Number G736-1-4-01	Rev 3.0
Date: Friday, November 07, 2003	Sheet 40	of 42



ECS CORP. (G736 M/B SIS LV)			
Title MAX1858 VGA/RAM POWER			
Size B	Document Number G736-1-4-01		Rev 3.0
Date:	Friday, November 07, 2003	Sheet	41 of 42

HISTORY:

1.0 : INITIAL

2.A : To avoid signal "ON3_5" float

2.B : FOR G736 DEBUG

2.C : AUDIO NOISE BUG FIXED

2.D : FOR AUDIO VOLUME TOO SMALL

3.0 : INITIAL

3.A: WIRELESS LAN AND ADAPTER INSERT

3.B: FOR 1394/USB EMI SOLUTION

3.C: EMI SOLUTION

ECS CORP. (G736 M/B)			
Title		HISTORY	
Size	Document Number	Rev	
B	G736-1-4-01	3.C	
Date:	Friday, November 07, 2003	Sheet	42 of 42