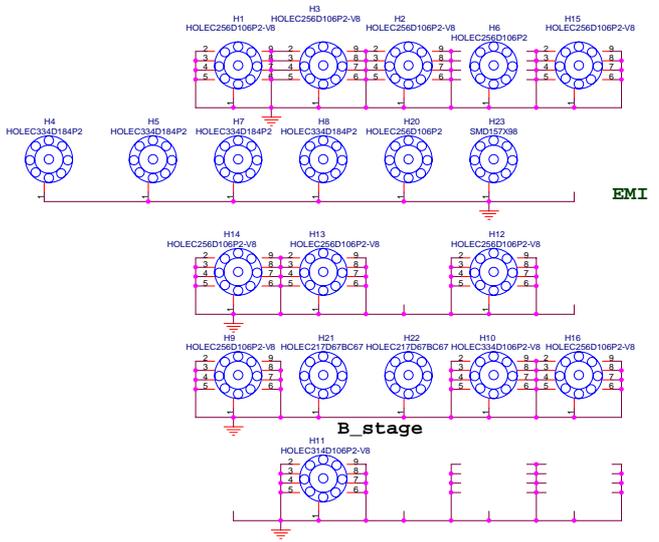


- 01:G733 M/B DESCRIPTION
- 02:CPU_FSBI
- 03:CPU_FSBII
- 04:CPU_POWER
- 05:SIS650-1
- 06:SIS650-2
- 07:SIS650-3
- 08:SIS650-4
- 09:SIS962-1
- 10:SIS962-2
- 11:SIS962-3
- 12:SIS962-4
- 13:CLOCK GENERATOR
- 14:CLOCK BUFFER
- 15:DDR_SODIMM
- 16:DDR_TERMINATION RESISTOR
- 17:CRT & P.P.
- 18:AUDIO & AMP
- 19:HDD & CD-ROM CONN.
- 20:KBC_PC87591
- 21:SPUER I/O & IR
- 22:SIS301
- 23:LVDS & LED & TOUCH PAD
- 24:PCI 1410
- 25:MINIPCI & PCMCIA SLOT
- 26:POWER SWITCH
- 27:USB CONN.
- 28:LAN
- 29:IEEE 1394
- 30:RTC & POWER SWITCH
- 31:SELECTOR
- 32:MAX1772 CHARGER
- 33:MAX1632 SYSTEM POWER
- 34:HIP6301 CPU POWER
- 35:VGA & DDR POWER
- 36:DDR_VTT & STANDY BY POWER
- 37:HISTORY



X = 0805 TYPE
 Y = 1206 TYPE
 C3216 = A TYPE
 C3528 = B TYPE
 C6032 = C TYPE
 C7343 = D TYPE

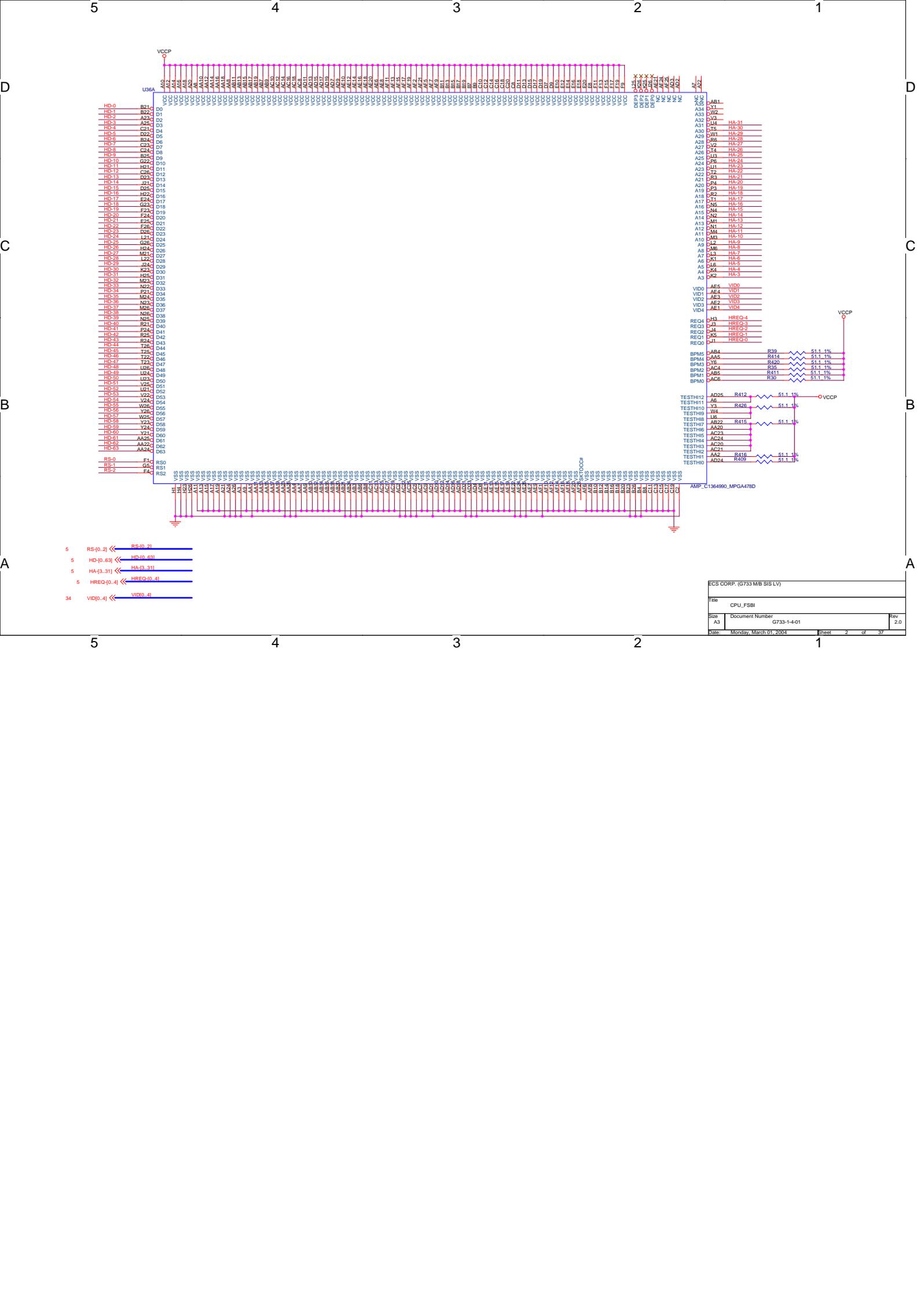
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DATE	05/27	05/20	05/20	05/20	05/27	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	03/01	05/20

PAGE	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37
REV	2.0	2.0	2b	2.0	2a	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2b
DATE	05/20	05/20	05/27	05/20	05/21	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/20	05/27

P/N:KK0G733011020

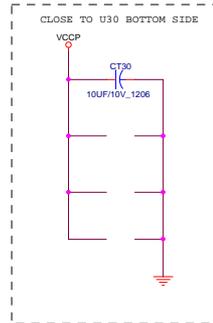
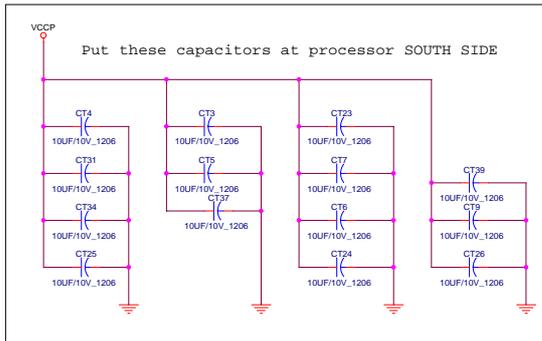
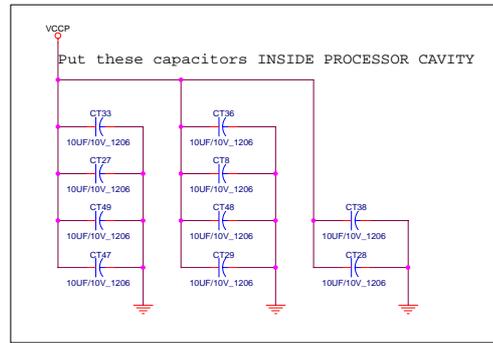
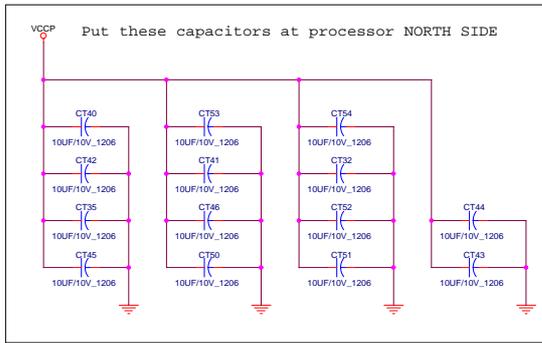
Checked by	Approved by	Designed by

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Size: A3	Document Number: G733-1-4-01	Rev: 2.0
Date: Monday, March 01, 2004	Sheet: 1	of 37

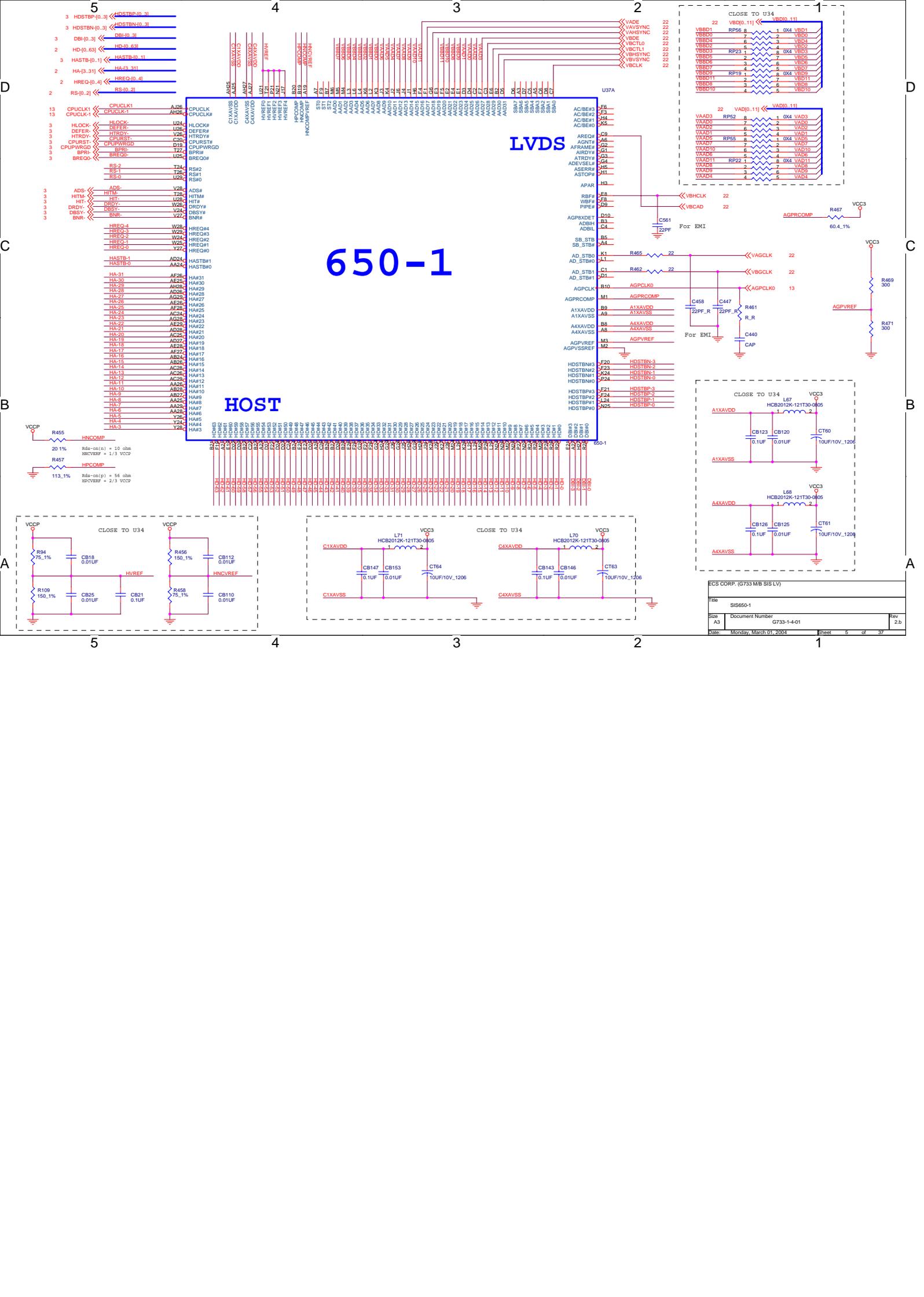


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- 5 HD[0..63] << HD[0..63]
- 5 HA[3..31] << HA[3..31]
- 5 HREQ[0..4] << HREQ[0..4]
- 34 VIDI[0..4] << VIDI[0..4]

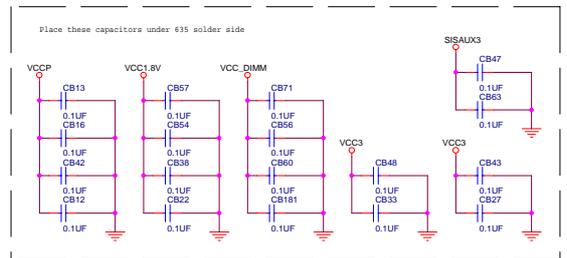
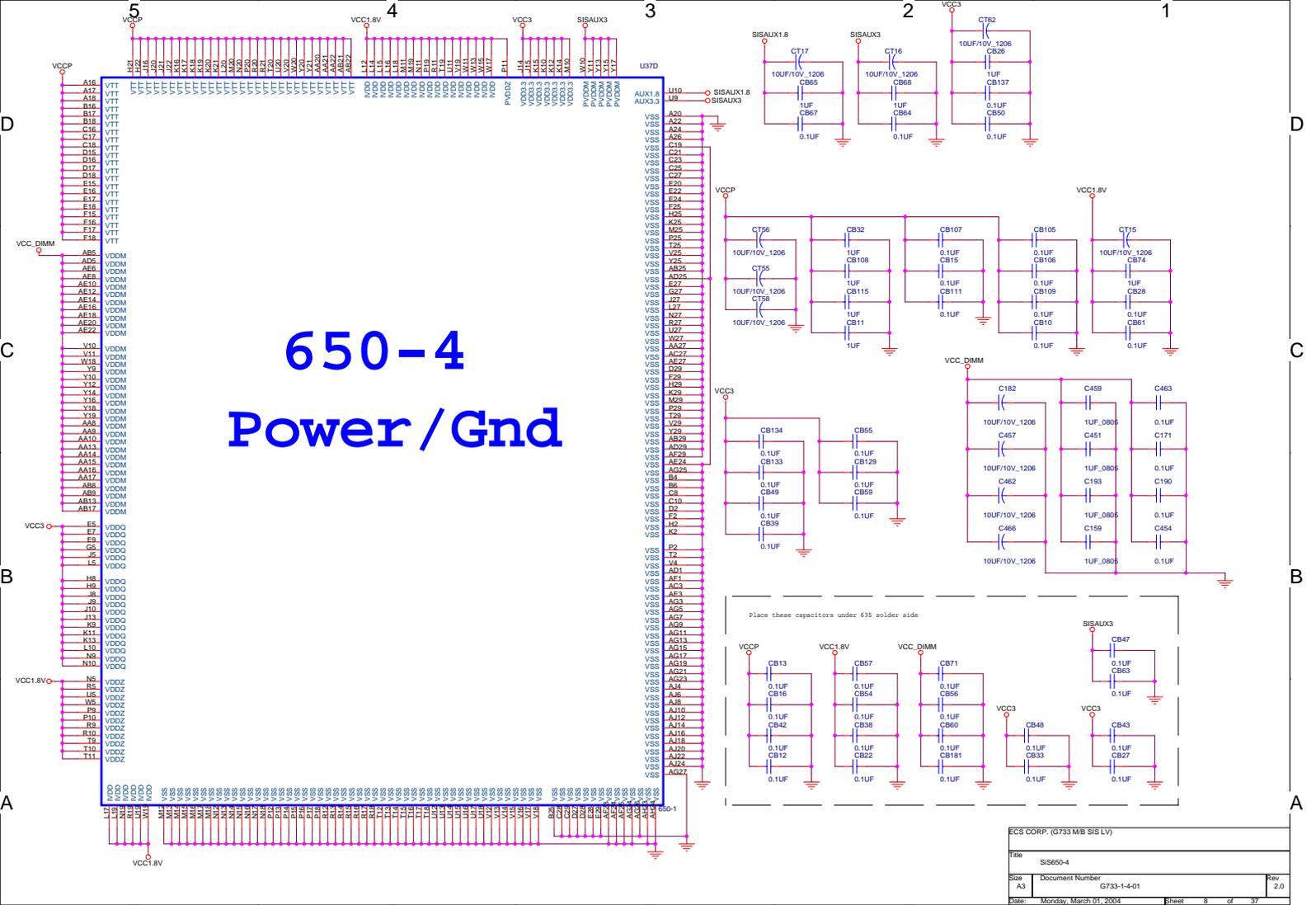
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Size A3	Document Number G733-1-4-01
Date: Monday, March 01, 2004	Sheet 2 of 37
	Rev 2.0



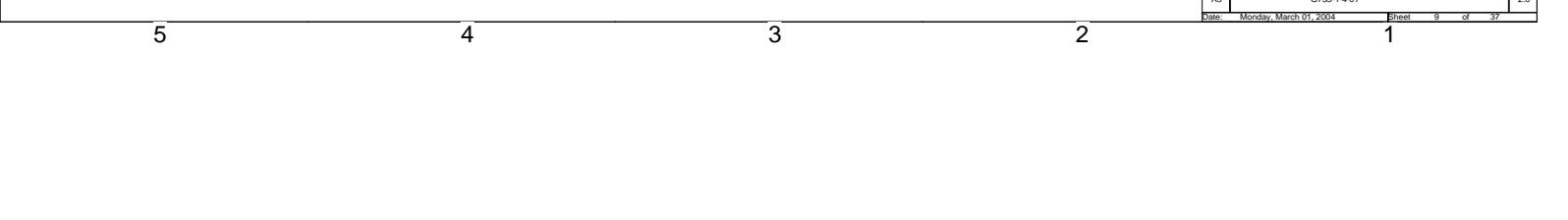
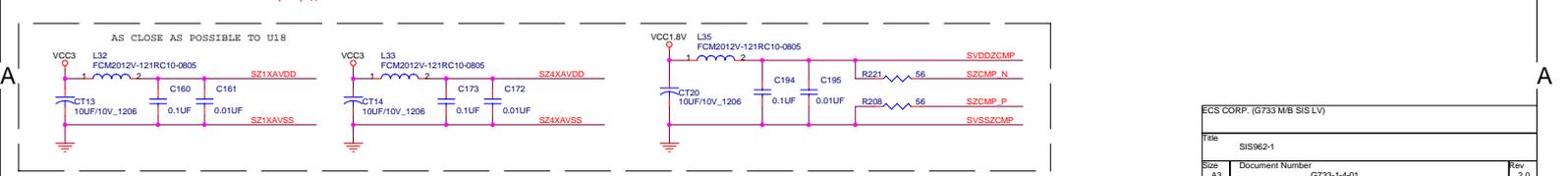
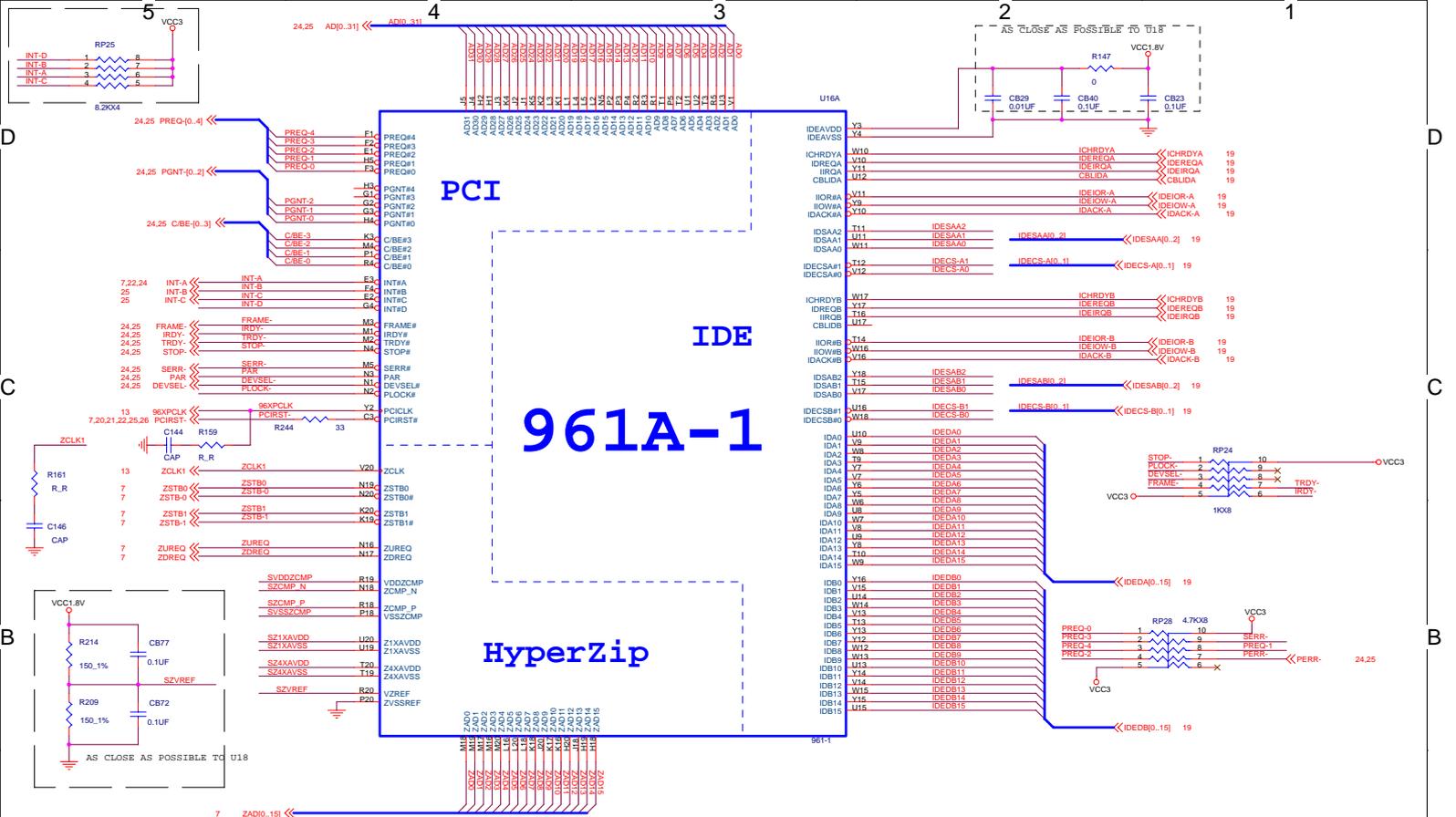
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File CPU_POWER		
Size A3	Document Number G733-1-4-01	Rev Z.0
Date: Monday, March 01, 2004	Sheet 4 of 37	



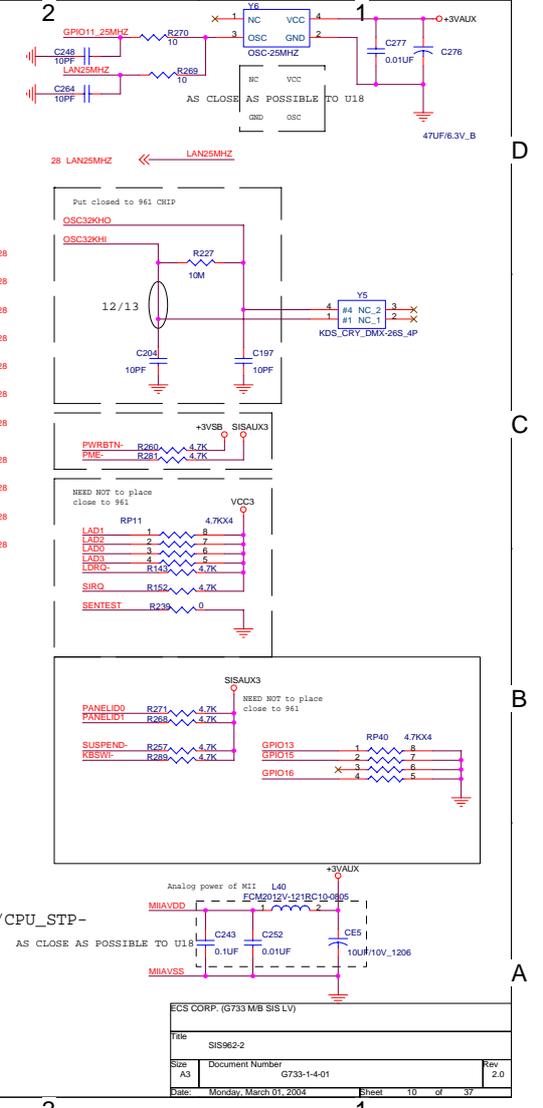
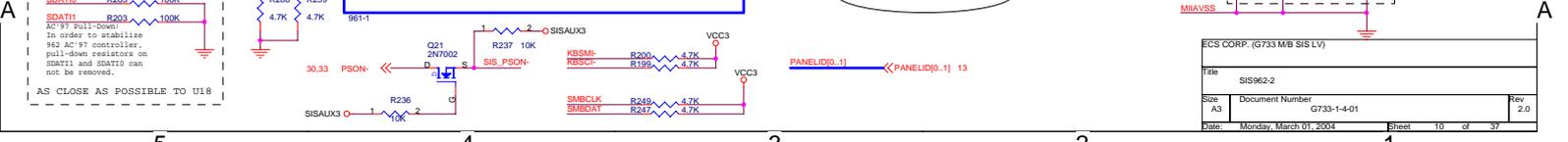
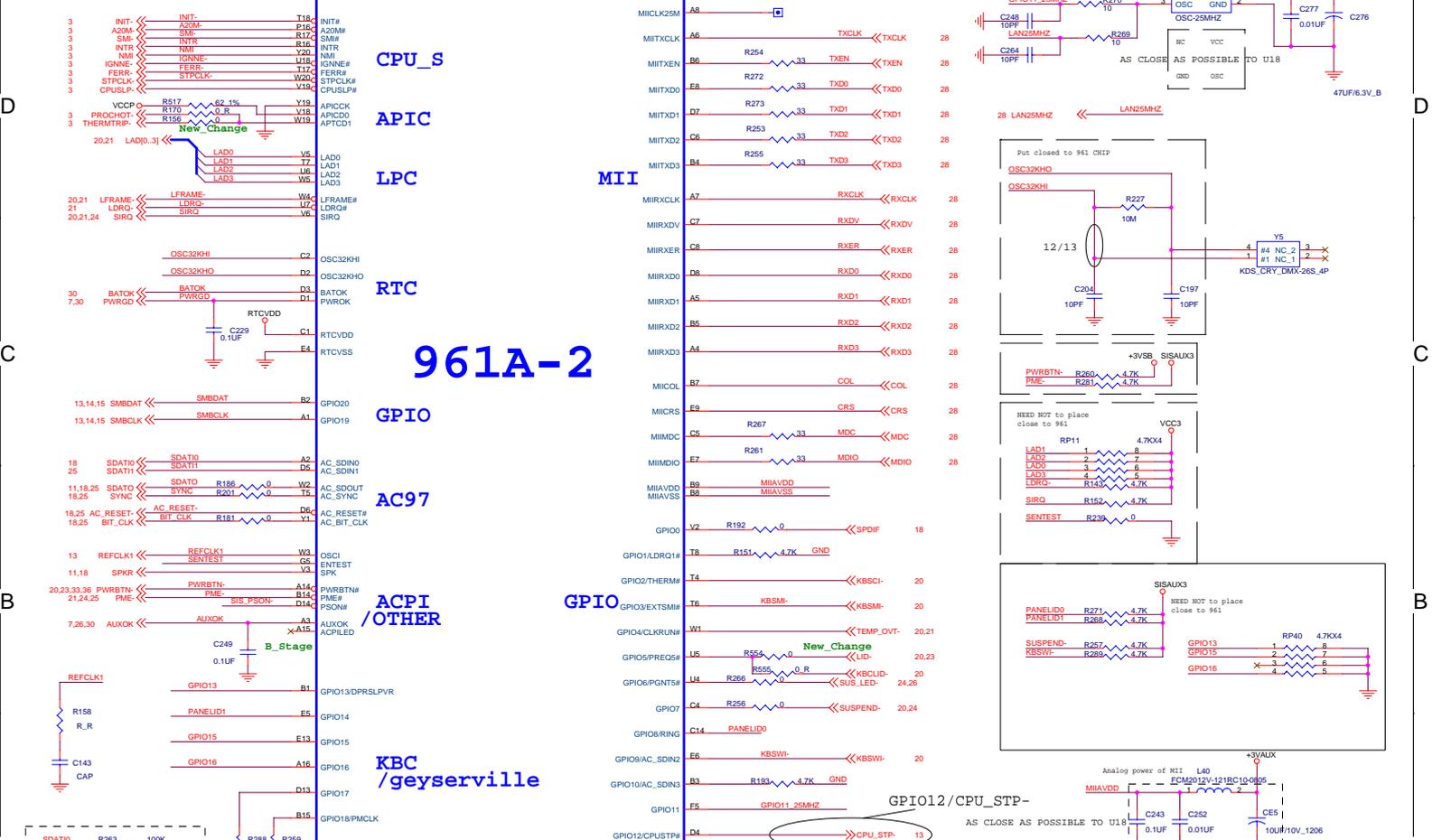
650-4 Power / Gnd



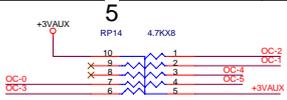
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Size	Document Number
A3	G733-1-4-01
Date	Monday, March 01, 2004
Sheet	8 of 37
Rev	Z.0



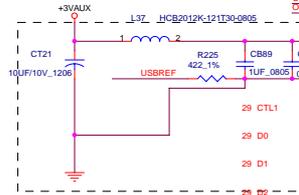
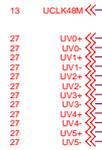
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Size: A3	Document Number: G733-1-4-01
Date: Monday, March 01, 2004	Sheet: 9 of 37
	Rev: Z.0



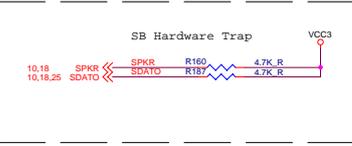
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Size: A3	Document Number: G733-1-4-01
Date: Monday, March 01, 2004	Sheet: 10 of 37



They don't need to be placed near the S1S961

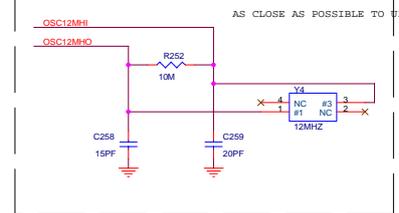
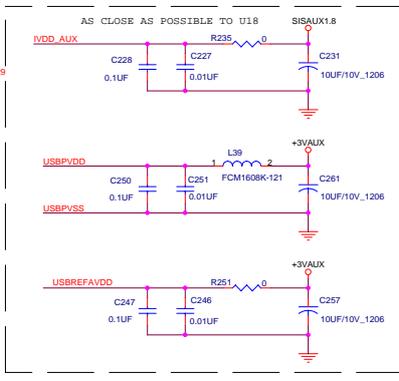
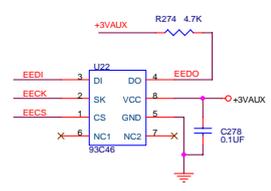
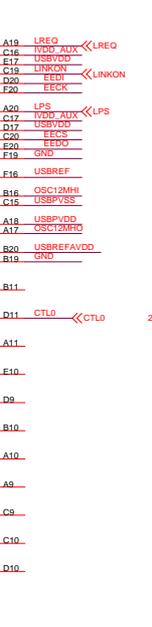
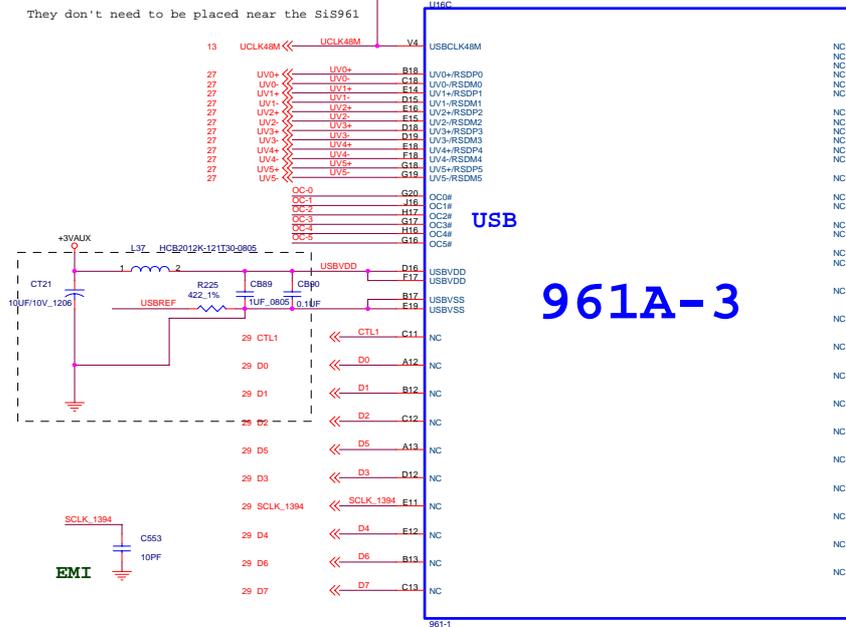


EMI

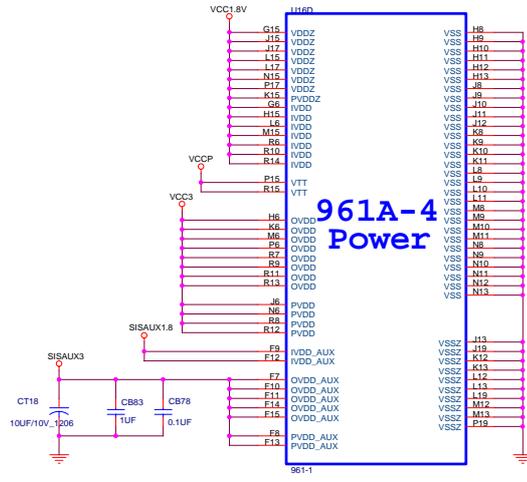
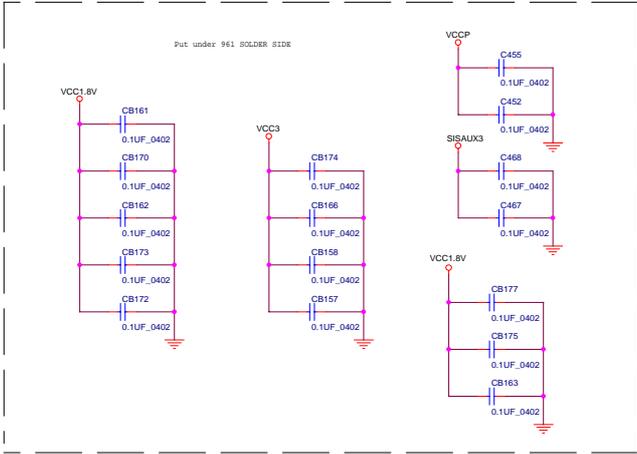
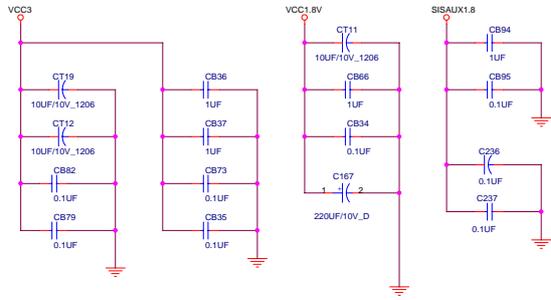


	0	1	Default
SPKR (LPC addr mapping)	disable	enable	0
SDATO (PCI CLK PLL)	enable	disable	0
OC2- (SB debug mode)	enable	disable	1
OC5- (Trap mode)	PCI AD	ROM	1

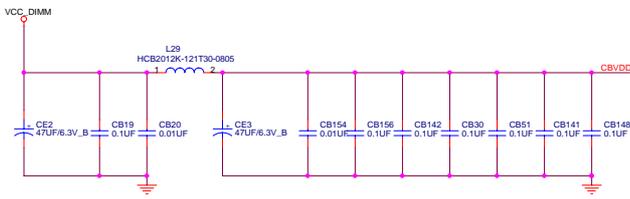
961A-3



ECS CORP. (G733 MB SIS LV)			
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Size	Document Number	G733-1-4-01	Rev
A3			Z.0
Date	Monday, March 01, 2004	Sheet	11 of 37

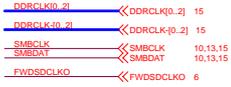
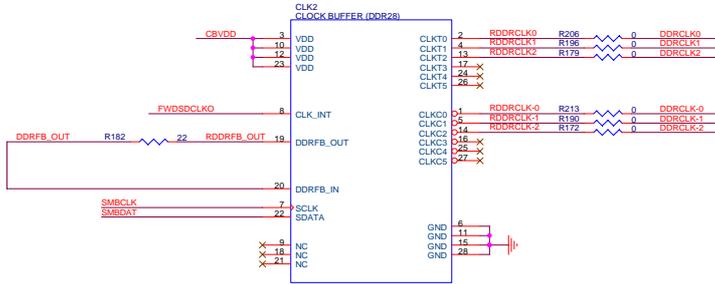


ECS CORP. (G733 MB SIS LV)	
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Size A3	Document Number G733-1-4-01
Date: Monday, March 01, 2004	Sheet 12 of 37
	Rev. Z.0

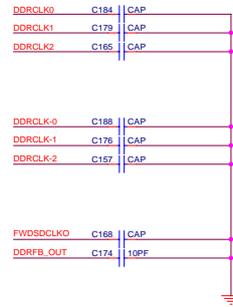


Clock Buffer (DDR)

(0PFC08B)
1: (SC9-93705)



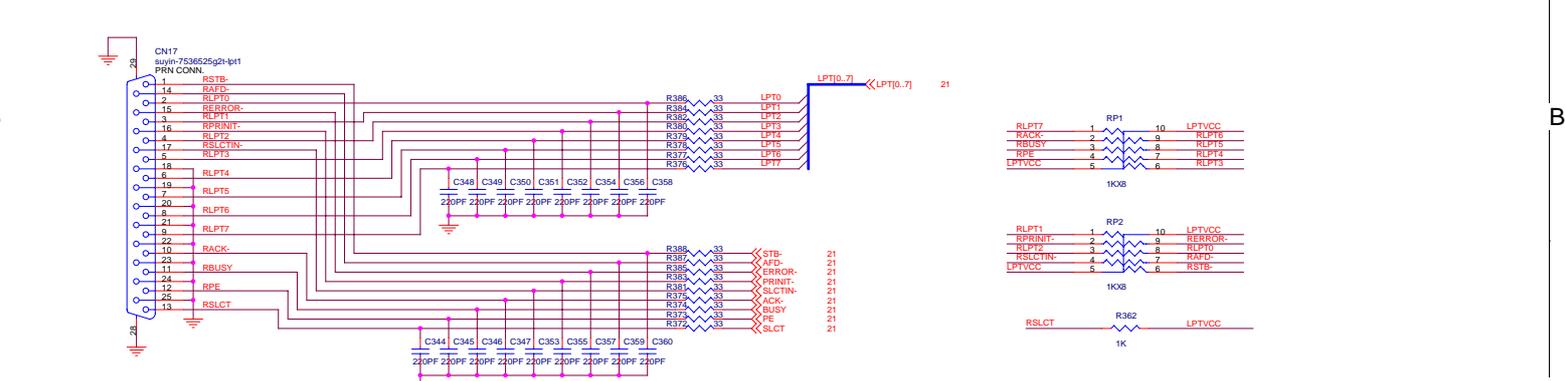
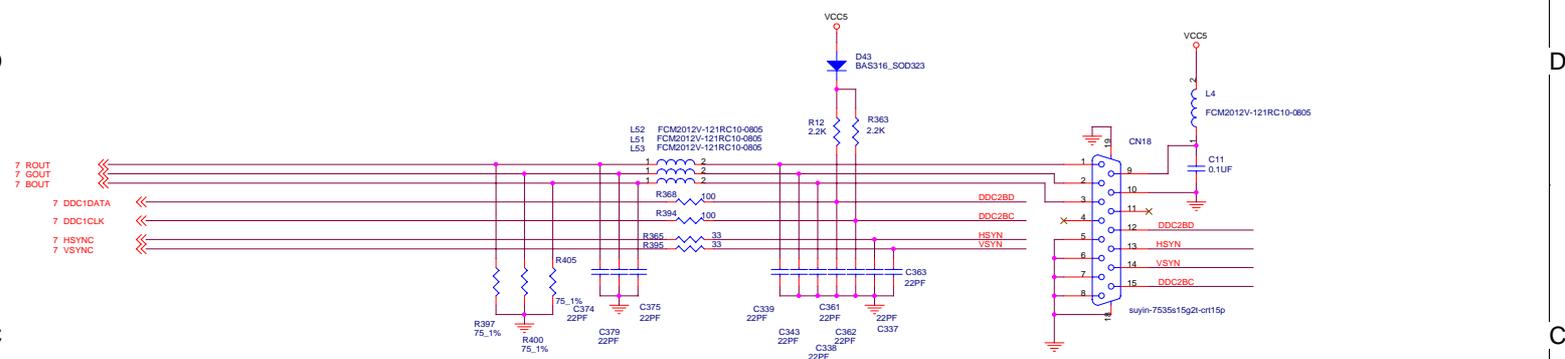
By-Pass Capacitors
Place near to the Clock Buffer

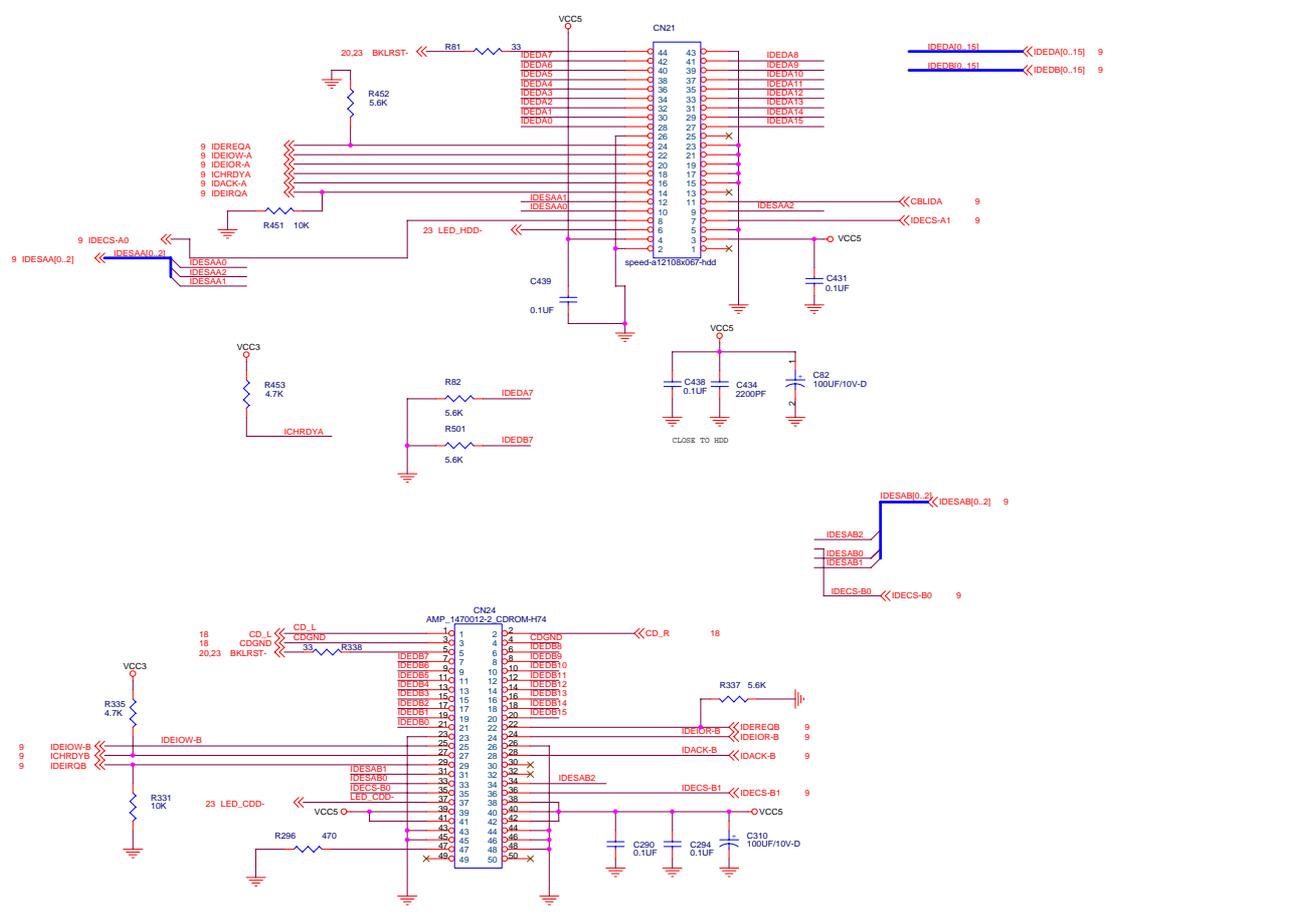


ECS CORP. (G733 MB SIS LV)			
Title CLOCK BUFFER			
Size A3	Document Number G733-1-4-01		Rev 2.0
Date Monday, March 01, 2004	Sheet 14	of 37	

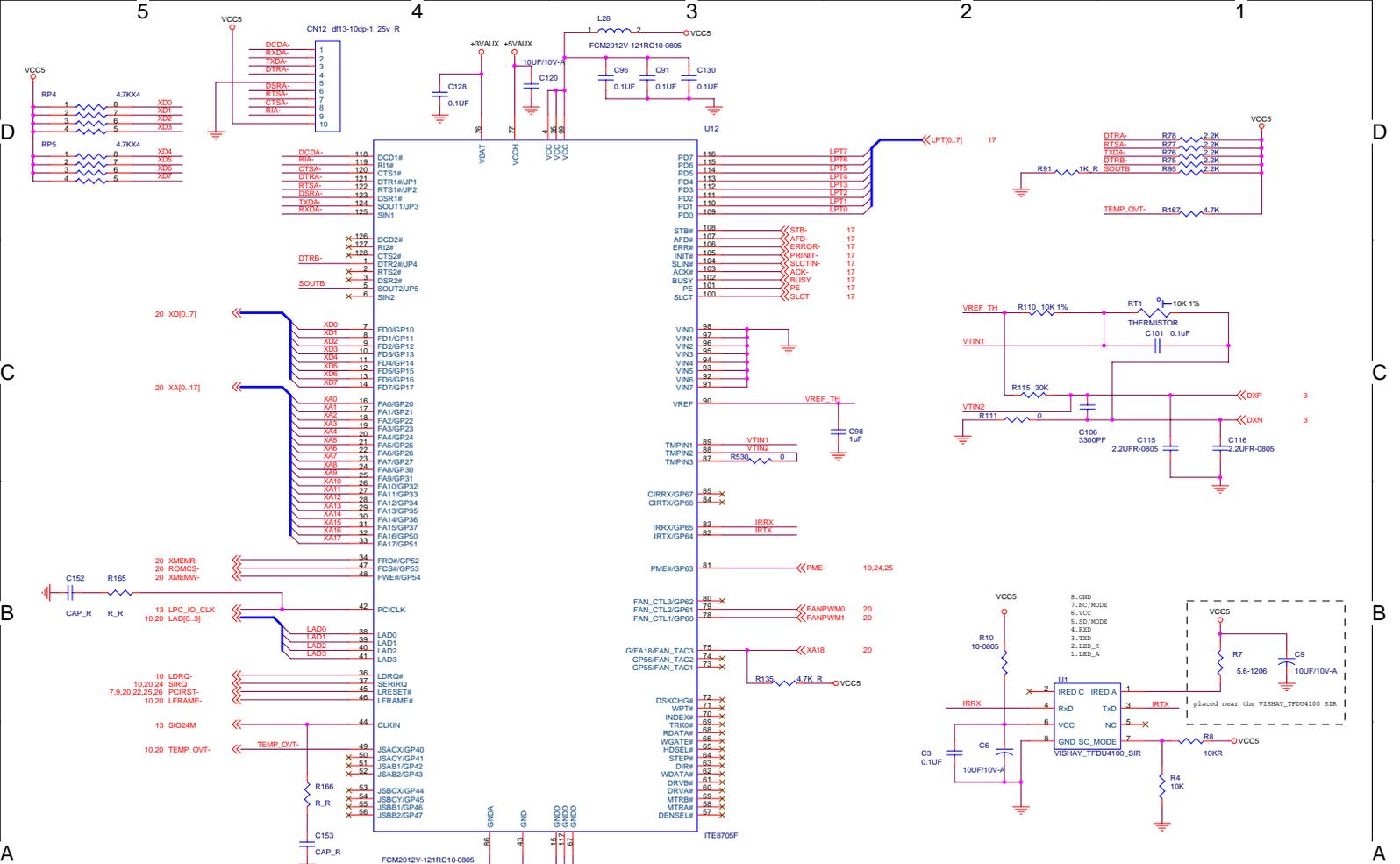


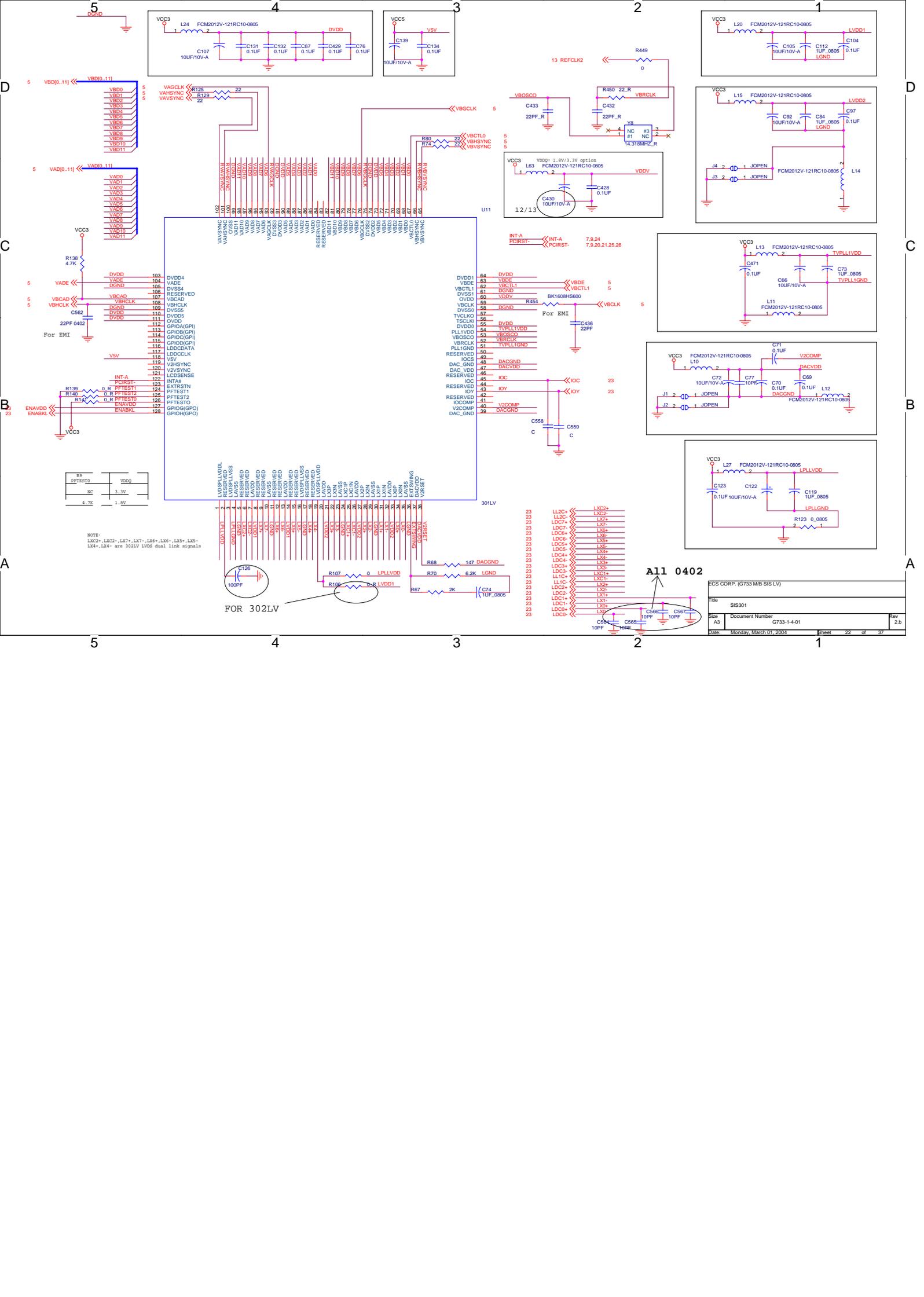
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Title: DDR_SODIMM	
Size: A3	Document Number: G733-1-4-01
Date: Monday, March 01, 2004	Sheet: 15 of 37
Rev: Z.0	





ECS CORP. (G733 MB SIS LV)			
Title HDD & CD-ROM CONN.			
Size A3	Document Number G733-1-4-01		Rev 2.0
Date Monday, March 01, 2004	Sheet 18	of 37	





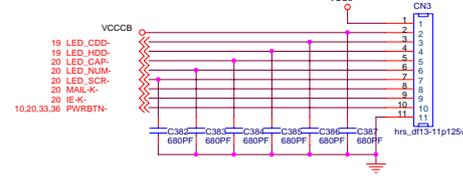
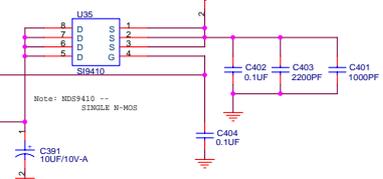
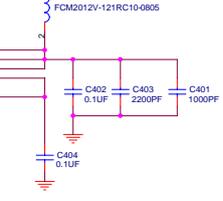
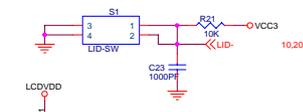
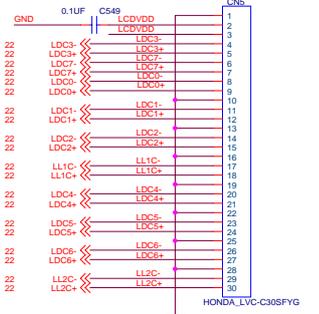
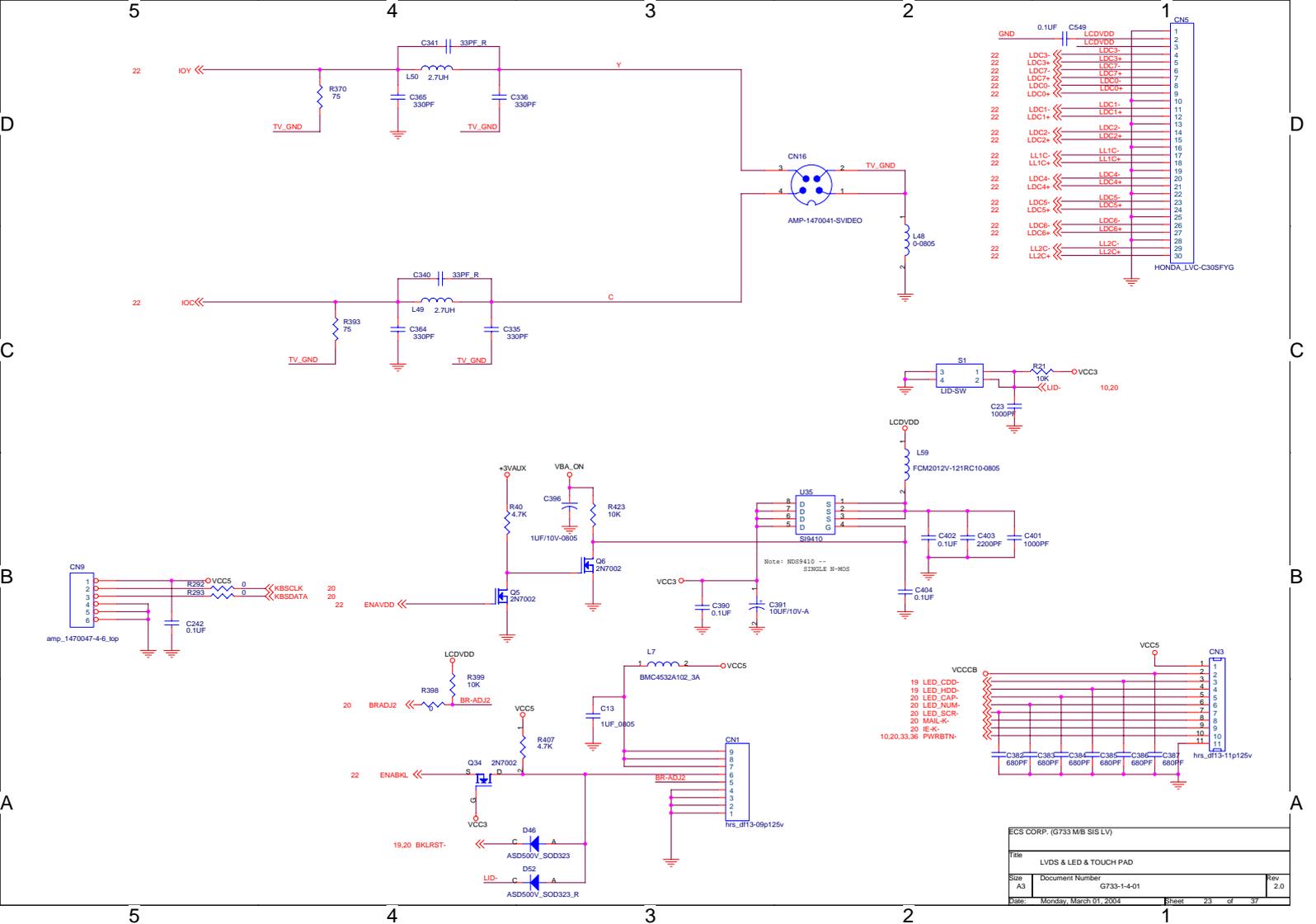
REF	VDD0
REFPFD0	3.3V
MC	1.8V
4.7E	1.8V

NOTE:
 LX02+, LX02-, LX7+, LX7-, LX6+, LX6-, LX5+, LX5-, LX4+, LX4- are 302LV LVDS dual link signals

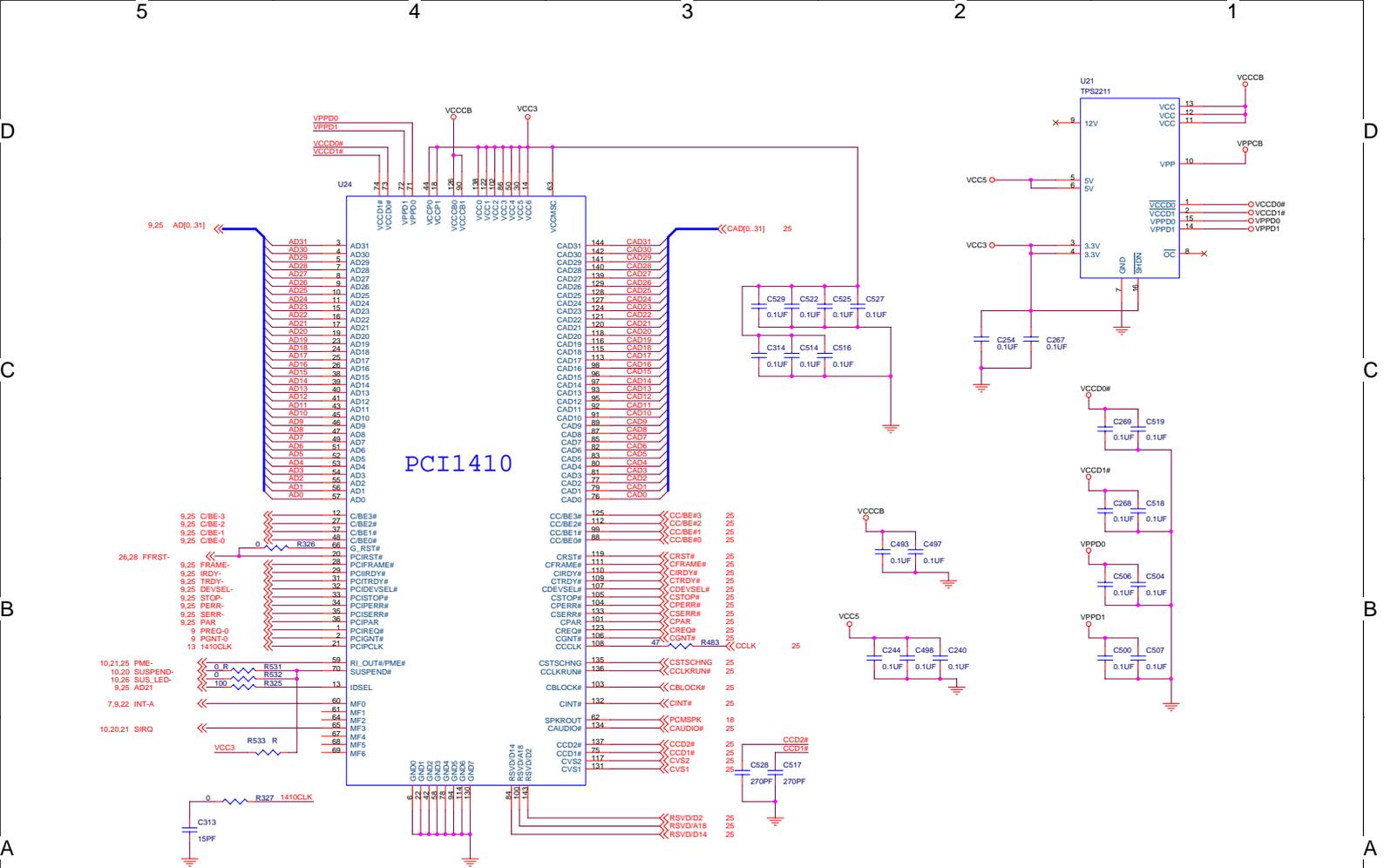
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Date: Monday, March 01, 2004	Sheet: 22 of 37

All 0402

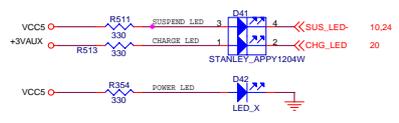
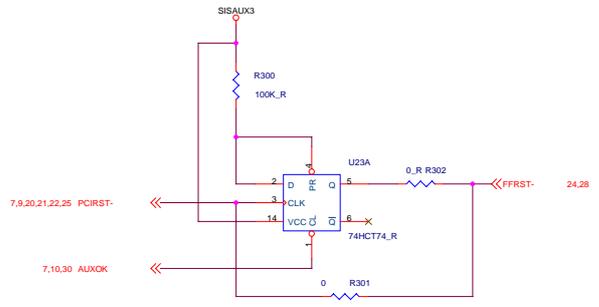
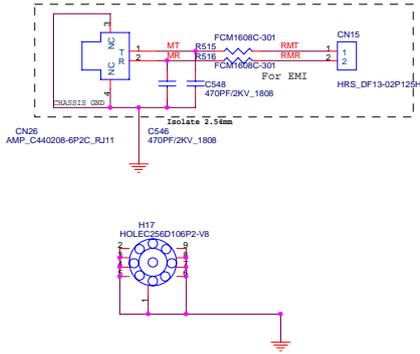
FOR 302LV



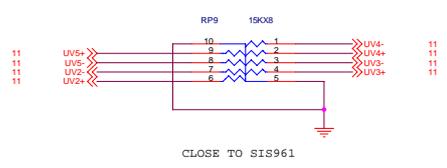
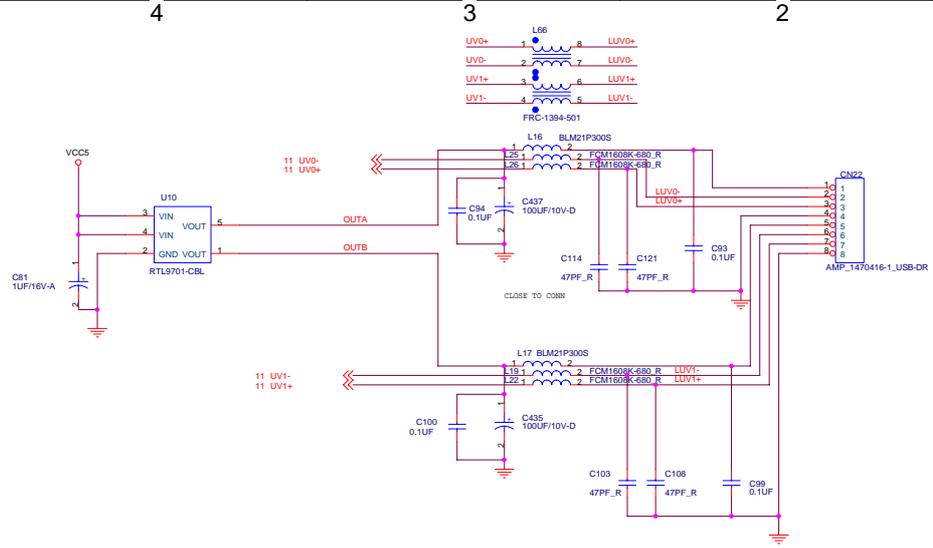
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Title: LVDS & LED & TOUCH PAD			
Size: A3	Document Number: G733-1-4-01	Rev: 2.0	
Date: Monday, March 01, 2004	Sheet: 23	of 37	



ECS CORP. (G733 MB SIS LV)	
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Size A3	Document Number G733-1-4-01
Date Monday, March 01, 2004	Sheet 24 of 37
	Rev Z.a

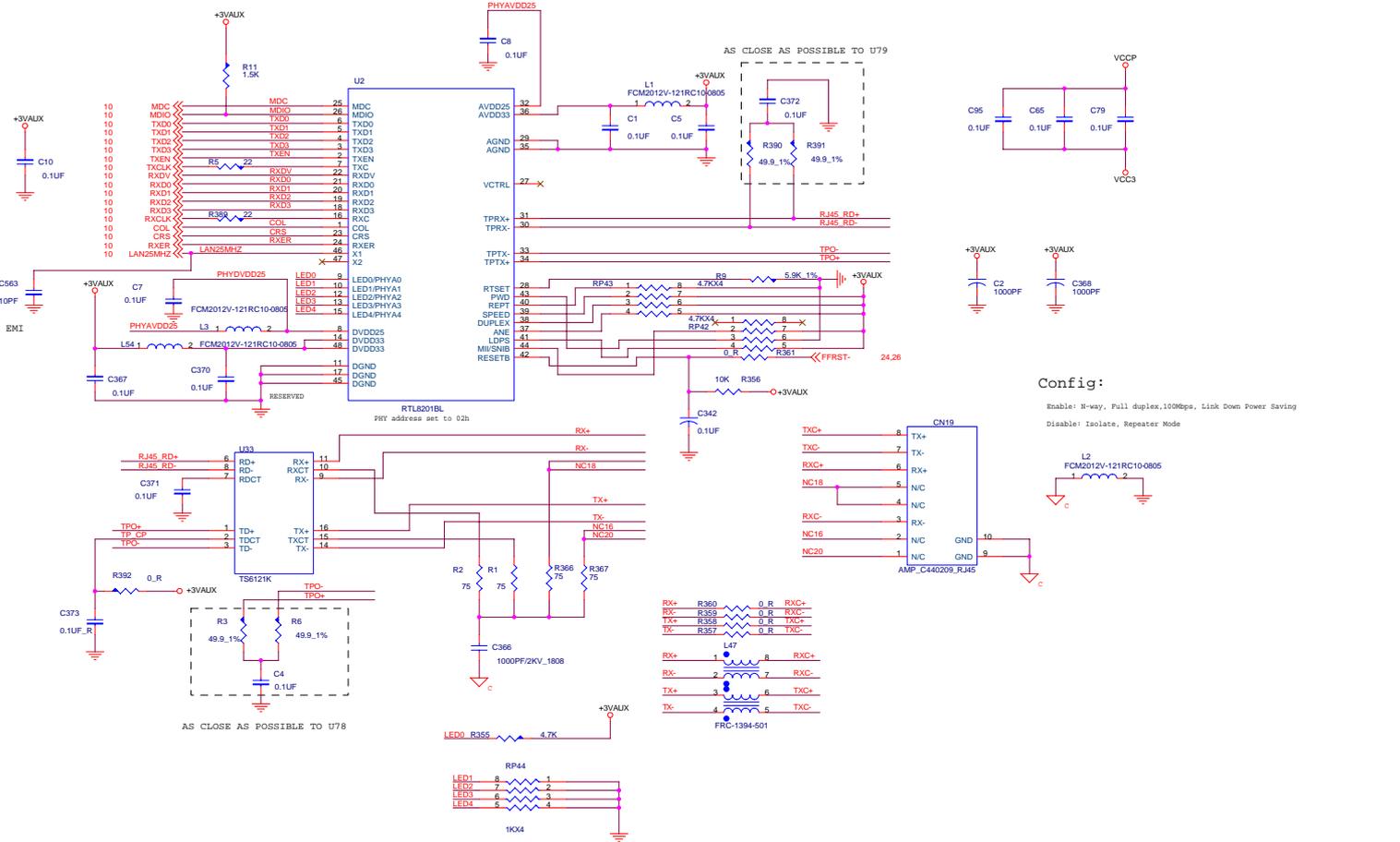


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Size: A3	Document Number: G733-1-4-01
Date: Monday, March 01, 2004	Sheet: 26 of 37
	Rev: 2.0

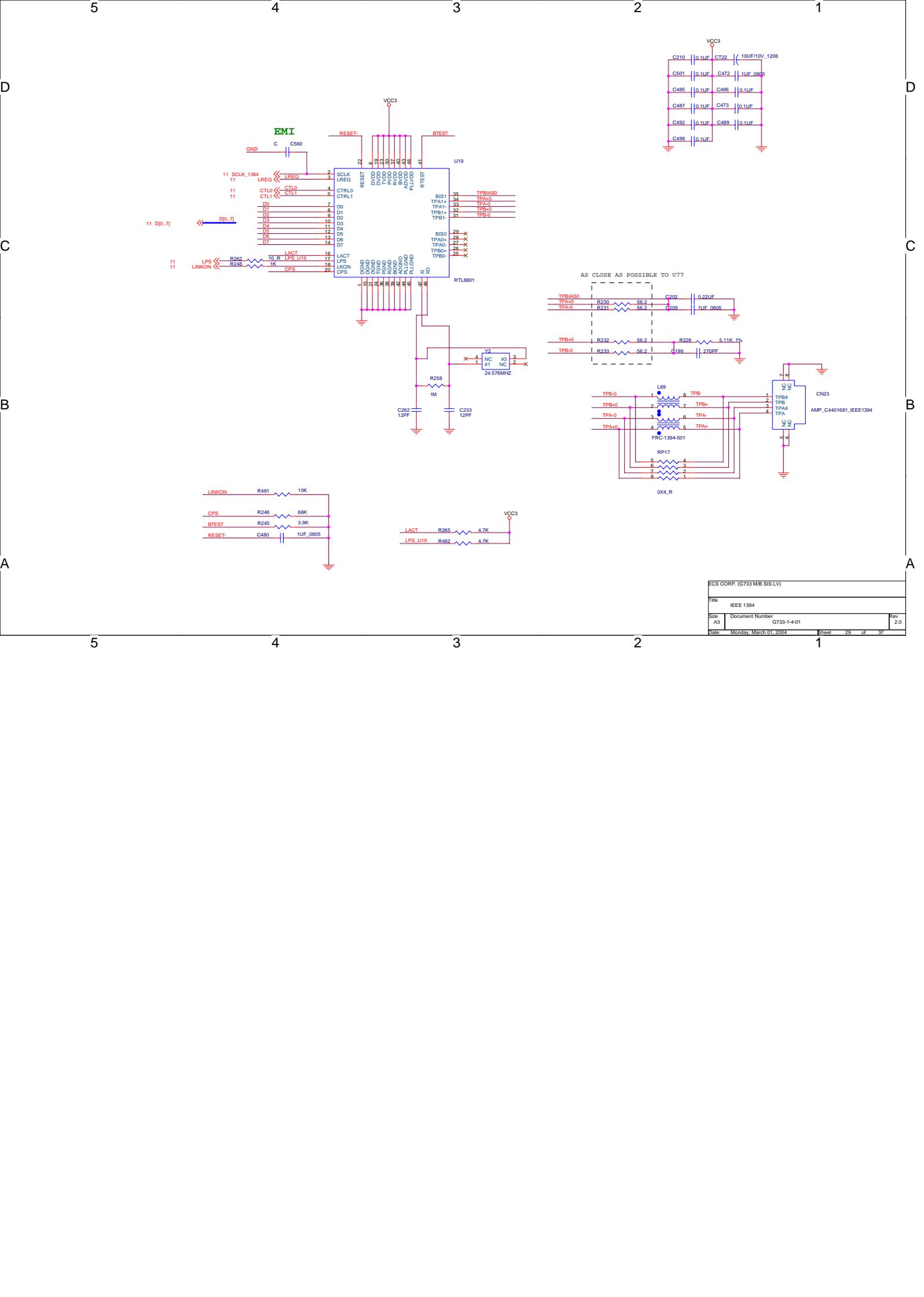


ECS CORP. (G733 MB SIS LV)			
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Size A3	Document Number G733-1-4-01		Rev 2.0
Date Monday, March 01, 2004	Sheet 27	of 37	

RTL8201

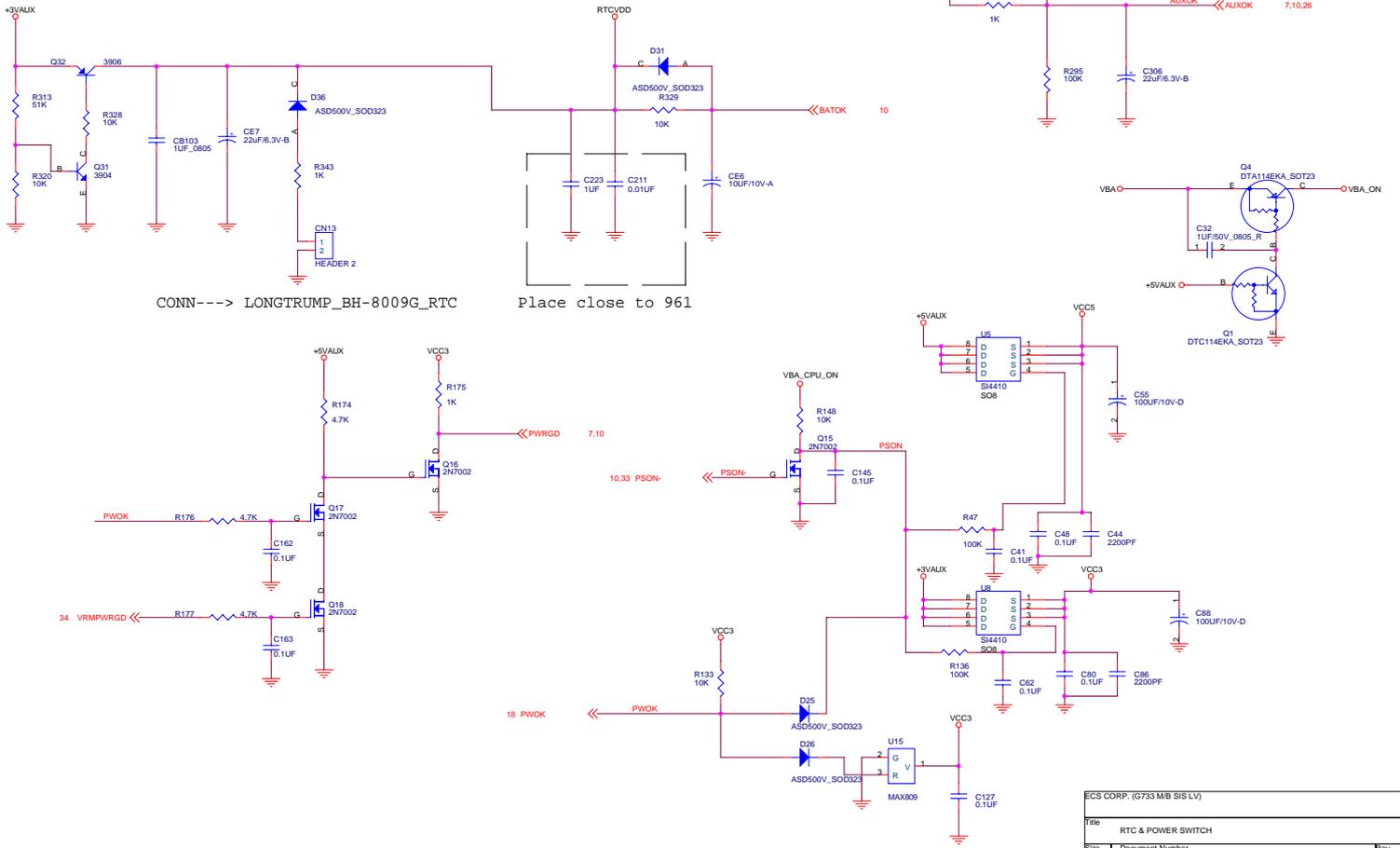


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Title: LAN	
Size: A3	Document Number: G733-1-4-01
Date: Monday, March 01, 2004	Sheet: 28 of 37
Rev: 2.0	



ECS CORP. (G733 MB SIS LV)			
Title			
IEEE 1394			
Size	Document Number	Rev	
A3	G733-1-4-01	Z.0	
Date:	Mondsy, March 01, 2004	Sheet	29 of 37

RTC
 NOTE!
 1.The RTCVDD is 3V
 2.Decoupling capacitor must be close to 650 RTCVDD pin.
 3.RTC circuit must strictly follow SiS's recommended design
 SiS is not responsible for RTC problems from foreign designs.



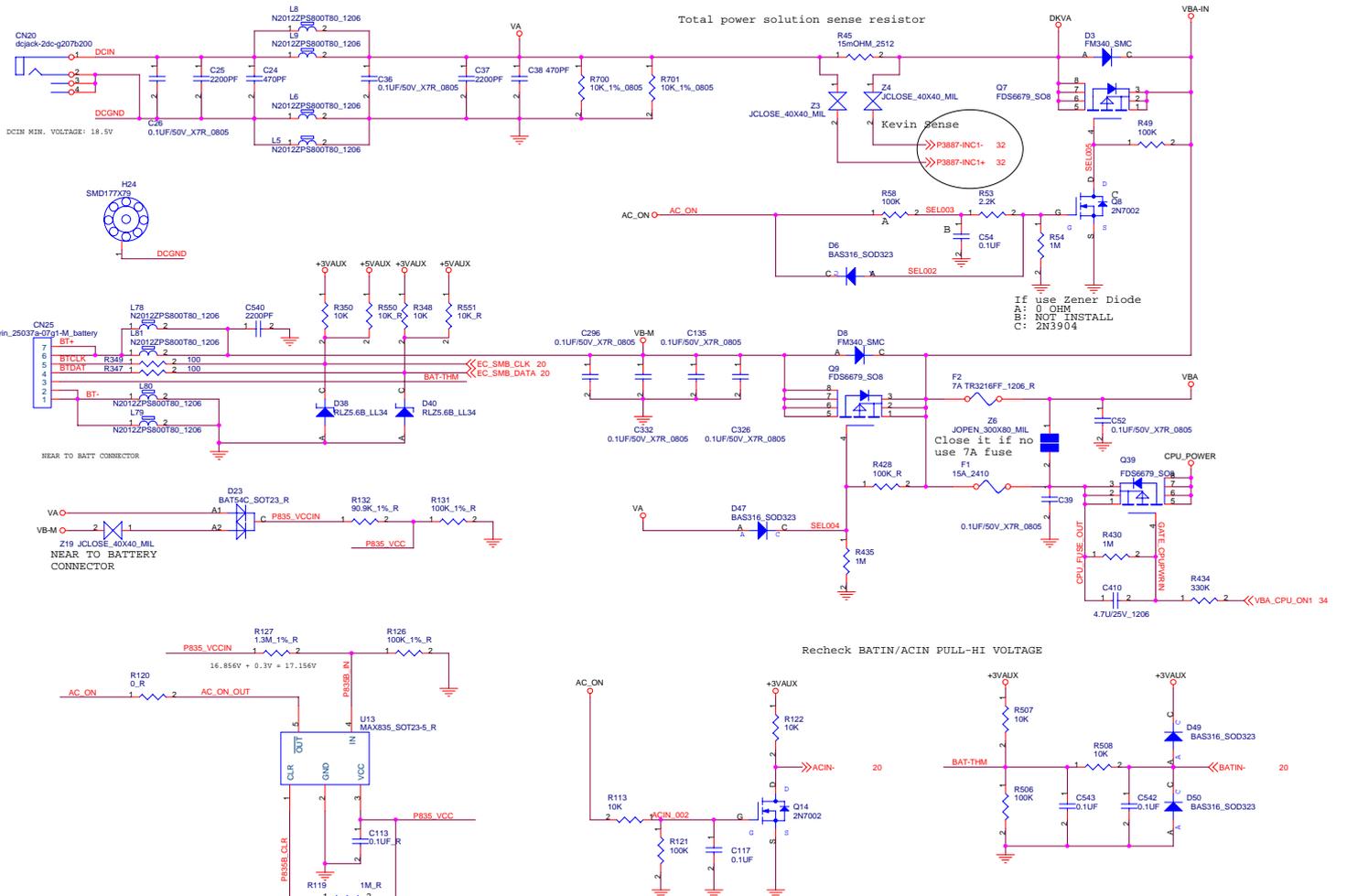
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4

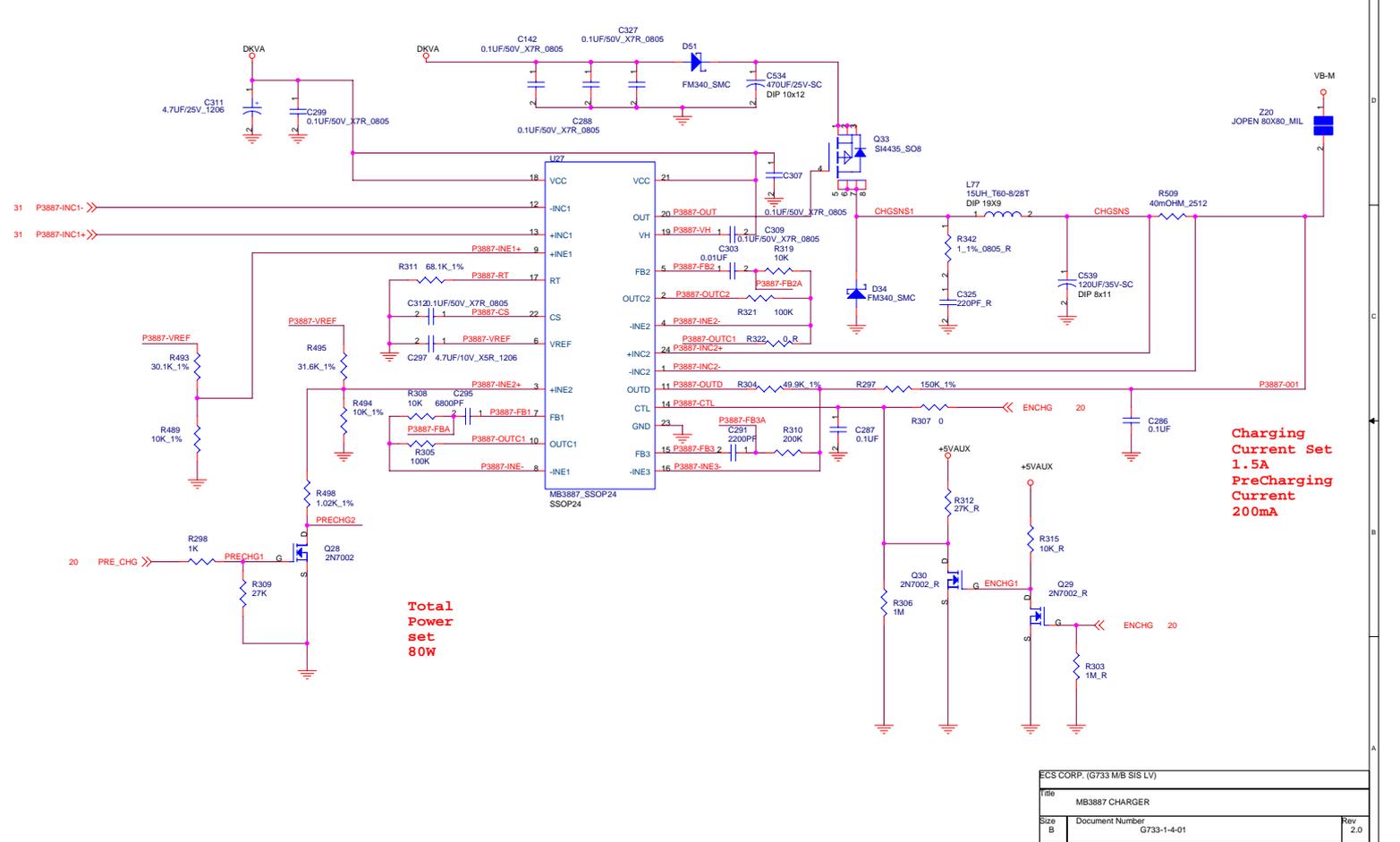
3

2

1



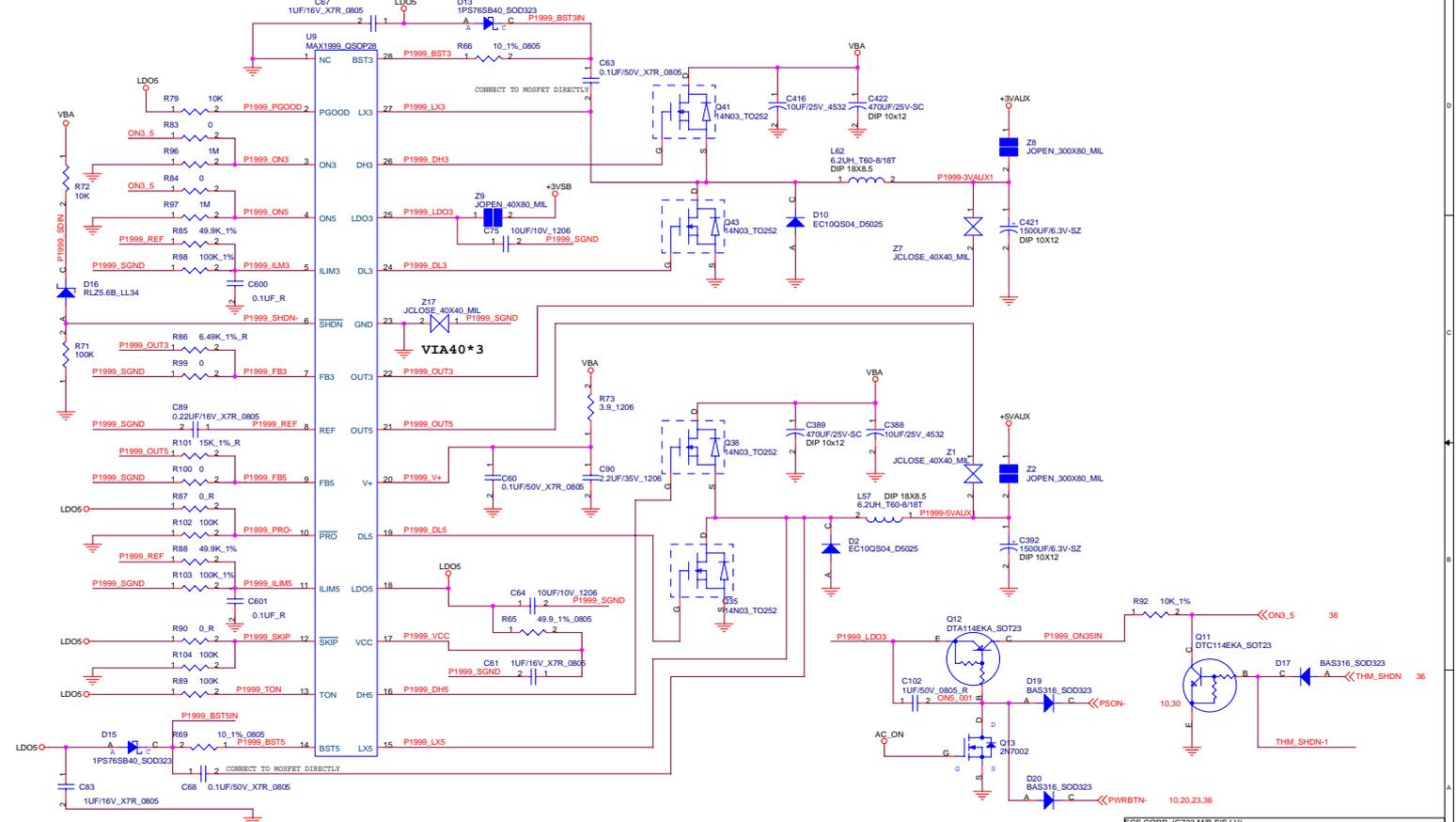
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Size	Document Number	Rev
Custom	G733-1-4-01	2.0
Date	Monday, March 01, 2004	Sheet 31 of 37



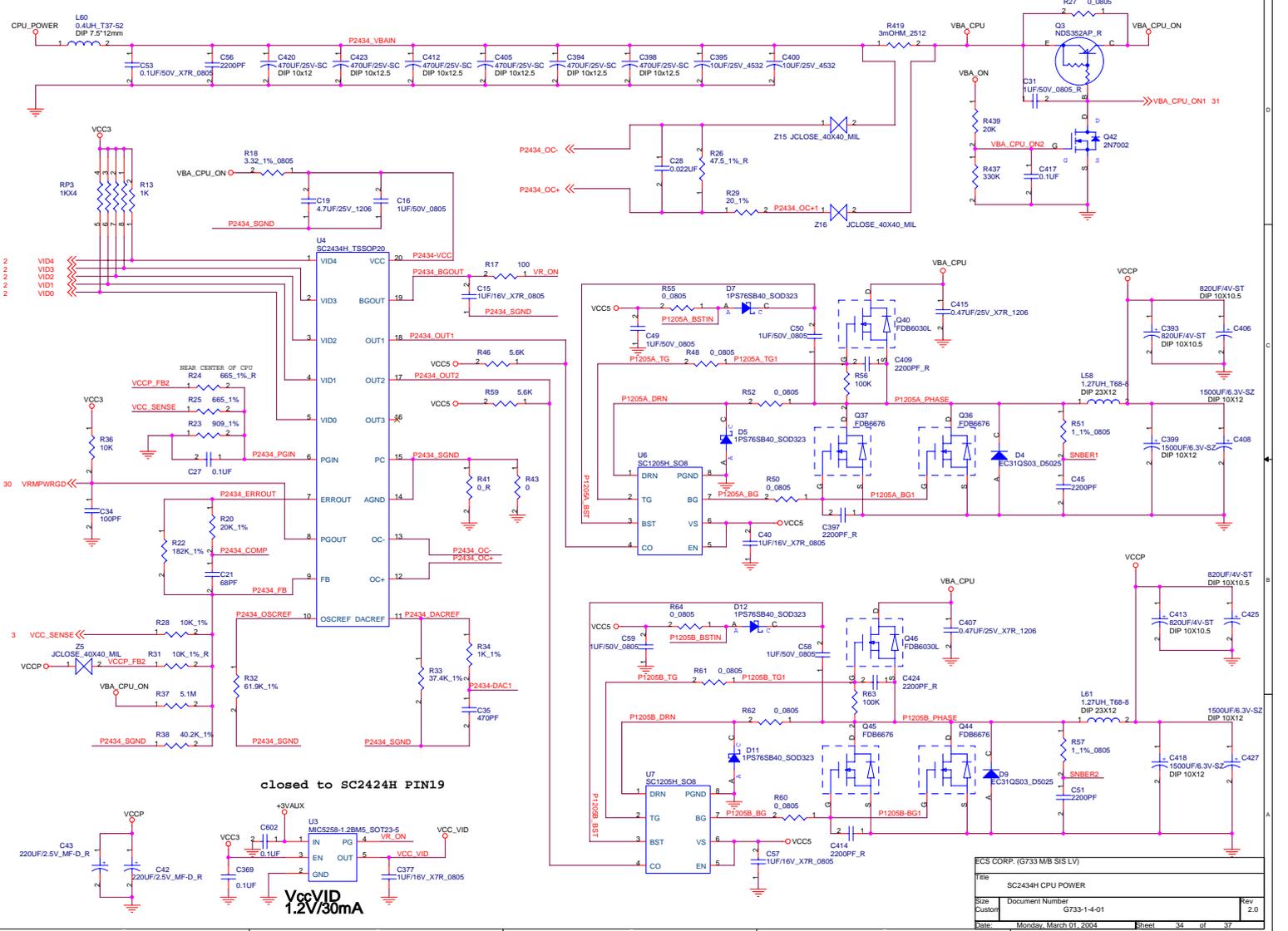
Total
Power
set
80W

Charging
Current Set
1.5A
PreCharging
Current
200mA

ECS CORP. (G733 MB SIS LV)		
Title MB3887 CHARGER		
Size B	Document Number G733-1-4-01	Rev 2.0
Date Monday, March 01, 2004	Sheet 32	of 37



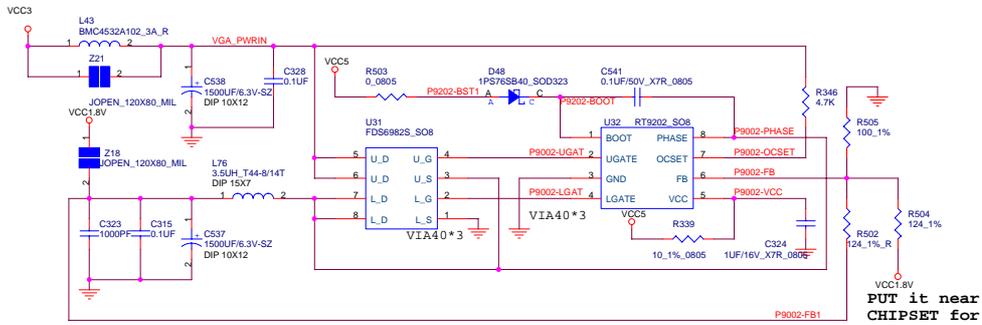
ECS CORP. (G733 M/B SIS LV)			
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Date: Monday, March 01, 2004	Sheet: 33	of 37	



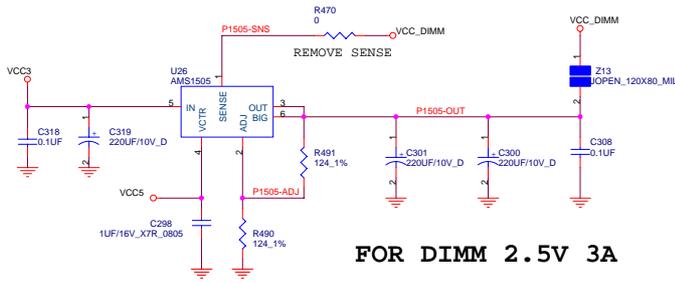
closed to SC2424H PIN19

VccVID
1.2V/30mA

ECS CORP. (G733 MB SIS LV)		
File	SC2434H CPU POWER	
Site	Document Number	Rev
Custom	G733-1-4-01	2.0
Date:	Monday, March 01, 2004	Sheet 34 of 37

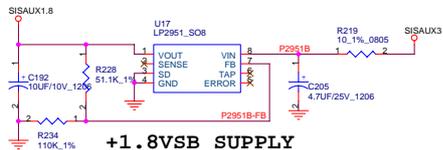
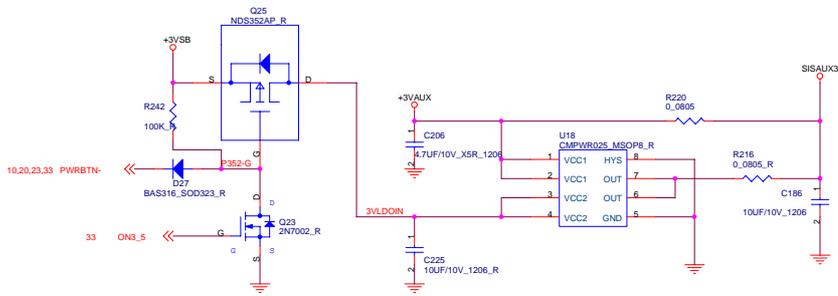


FOR SIS chip 1.8V 3A

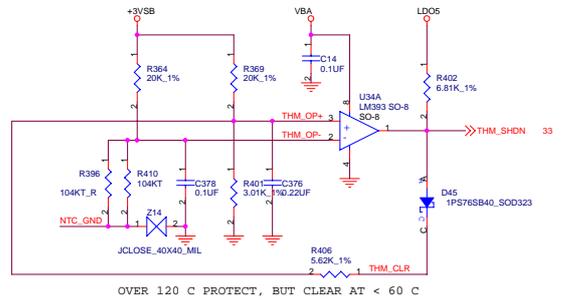


FOR DIMM 2.5V 3A

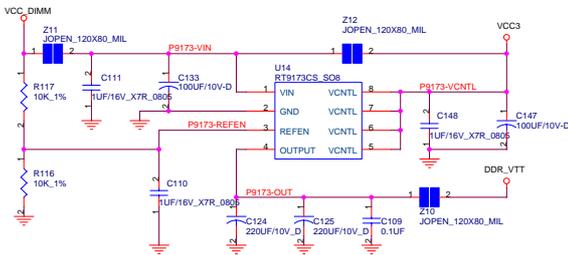
ECS CORP. (G731 MB SIS LV)		
Title VGA & DDR POWER		
Size B	Document Number G731-1-4-01	Rev 2.0
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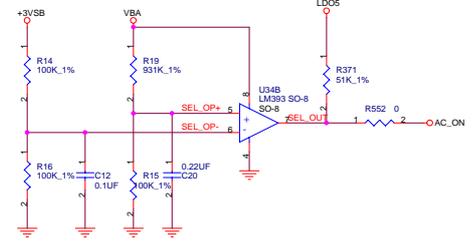
**+1.8VSB SUPPLY
30mA FOR SIS
1.8AUX**



OVER 120 C PROTECT, BUT CLEAR AT < 60 C



FOR DDR_VTT 1.25V 1.5A



ECS CORP. (G733 MB SIS LV)		
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HISTORY:

1.0 : INITIAL

1.a : R473 750ohm change to 10ohm. It fixed
PCMIA Modem dial-tone too small.

1.b : C458,C447,C374,C375,C379,C436 change to 22PF for EMI.

Add C561,C562 22PF For EMI.

C553 change to 10PF For EMI.

R514,R512,R515,R516 change to FCM1608C-301 For EMI.

C544,C545,C547 change to 680PF For EMI.

CP1,CP2,CP3,CP4,CP5,CP6 change to 180PF capacitor array For EMI.

L66 change to 150ohm common choke For EMI.

Add C563 10PF For EMI.

RP56,RP23,RP19,RP52,RP55,RP22,R454 change to BK1608HS600 For EMI.

Change the Ground of C365 and C364 from TV_GND to Digital GND.

2.0 : INITIAL

2.a : R327 75 ohm change to 0 ohm for EMI

2.b : Add C564,C565,C566,C567 10PF capacitors for EMI

Del C458,C477 22PF capacitor for EMI

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Title HISTORY		
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