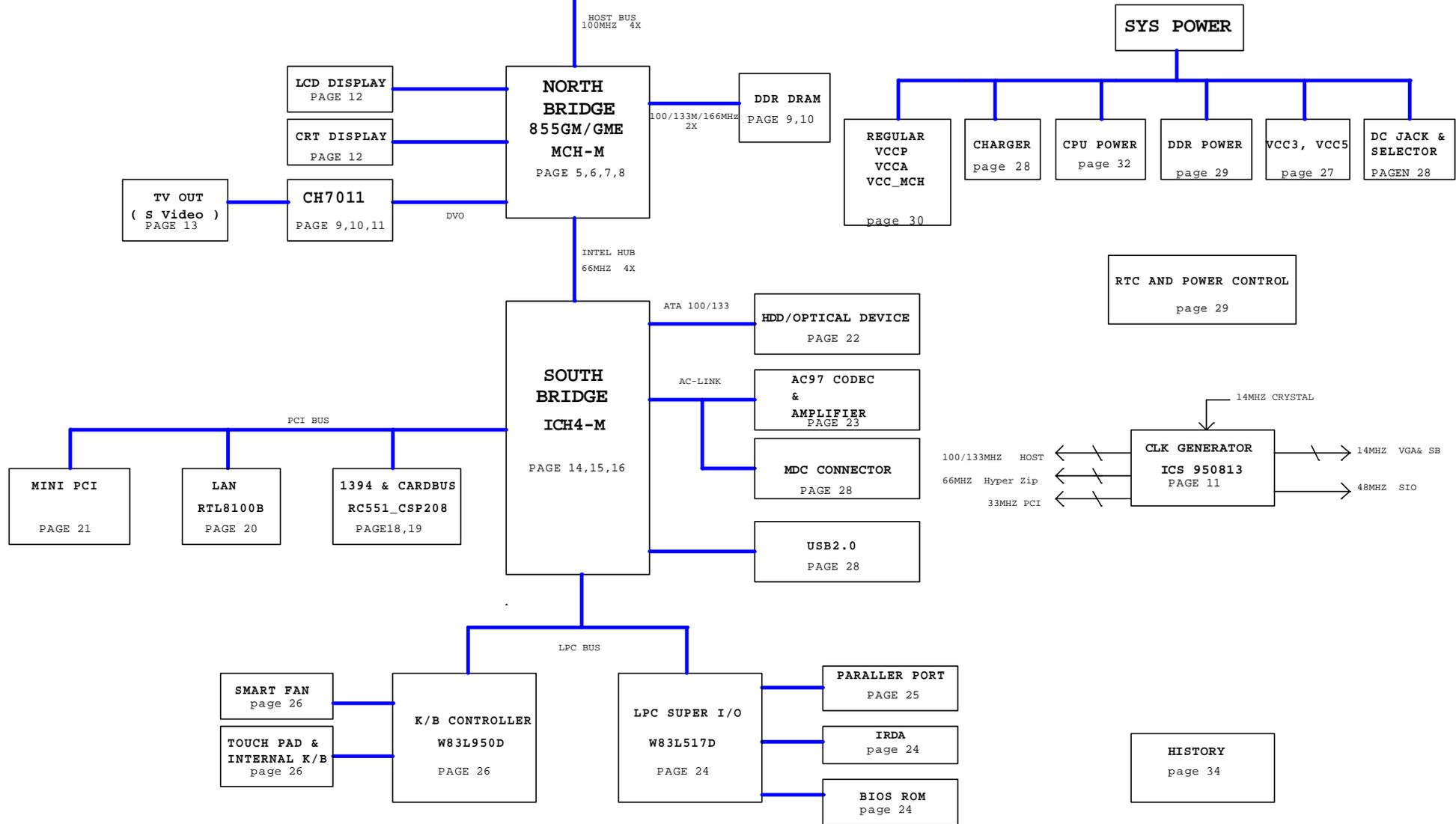


G553

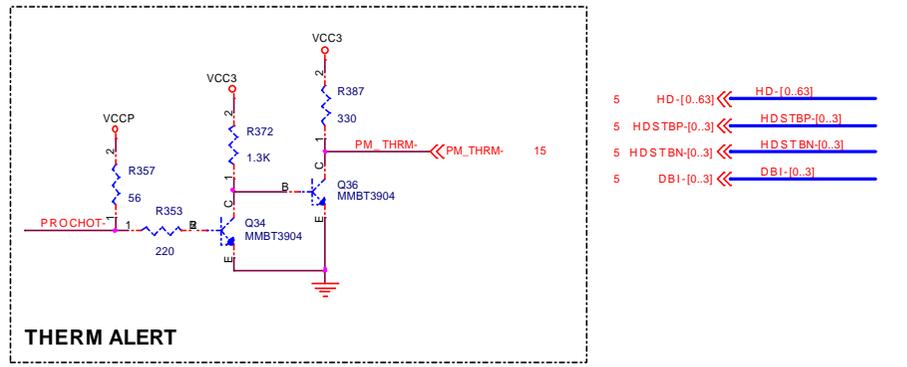
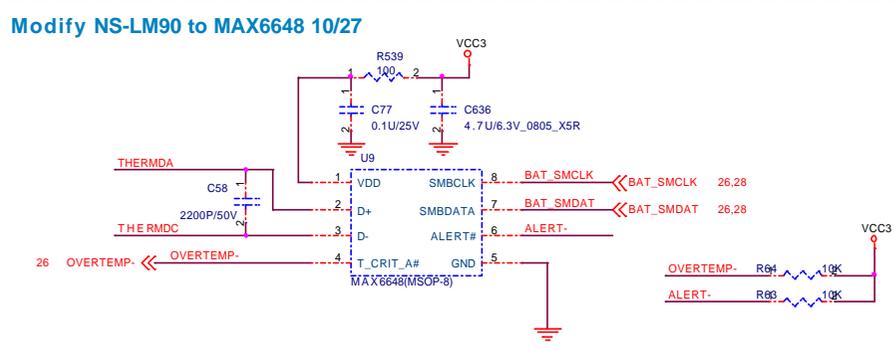
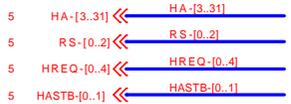
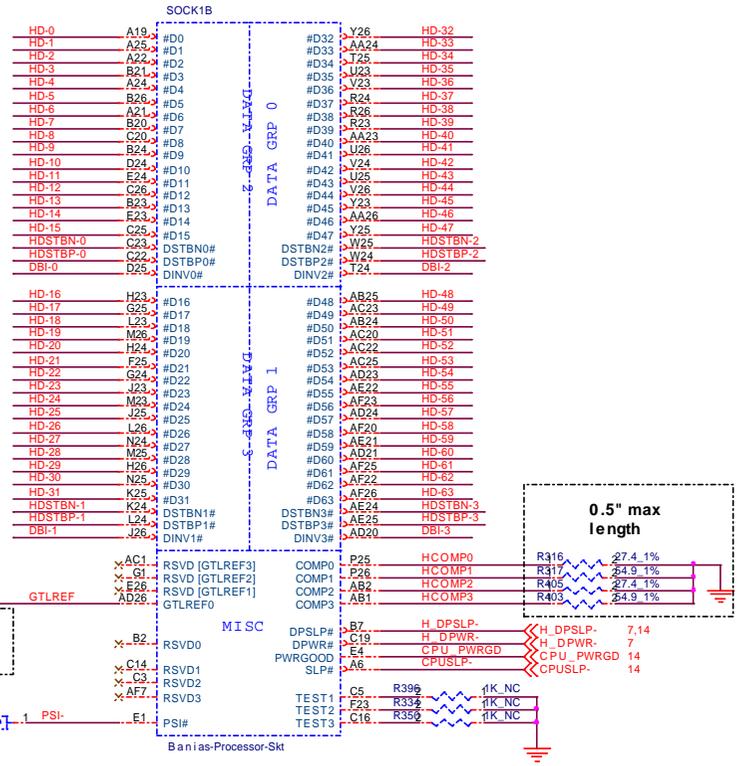
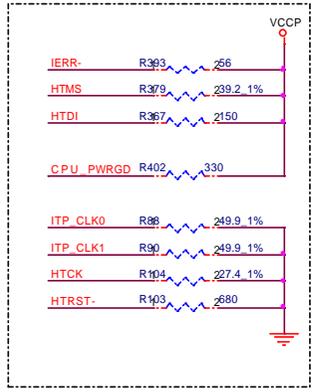
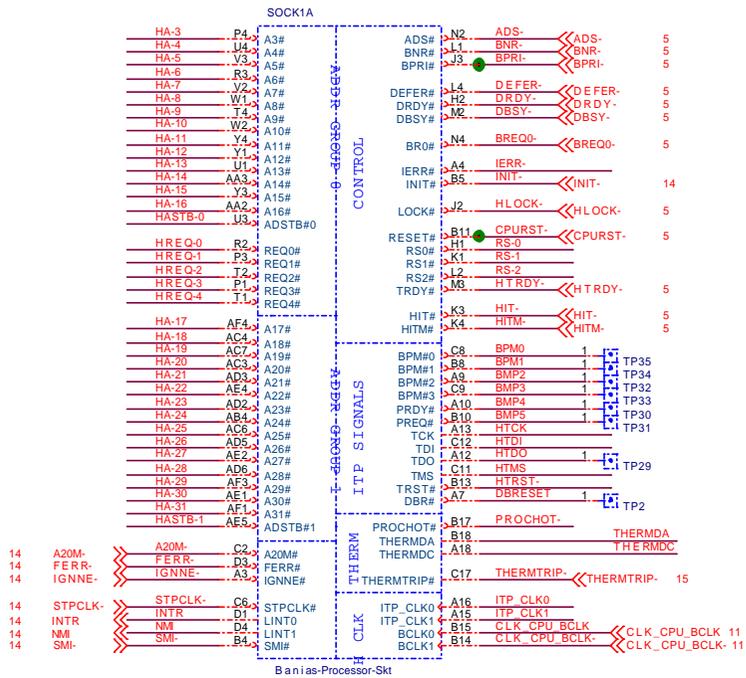
SOCKET 479
Banias/Dothan
478 uFCPGA
 PAGE 2,3,4



PAGE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
REV	V.3.0																	
DATE	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31
PAGE	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36
REV	V.3.0																	
DATE	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31	2003/12/31			

P/N : 15-F44-013000

Approved by	Checked by	Designed by
ELITEGROUP COMPUTER SYSTEM CO., LTD.		
P. Leader	File G553 BLOCK DIAGRAM	
Size C	Document Number G553-1-401	Rev 3.0
Date	Tuesday, January 06, 2004	Sheet 1 of 34



8

7

6

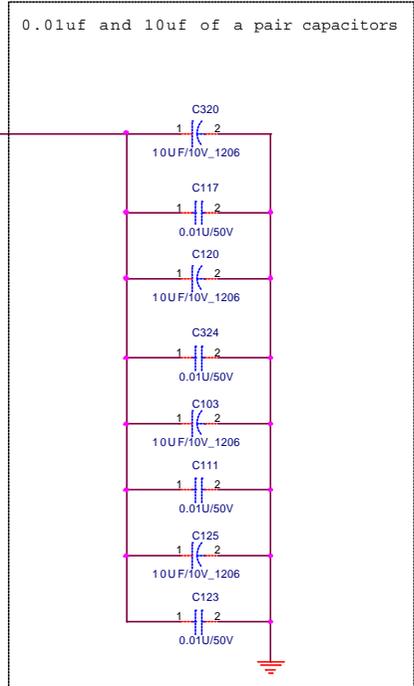
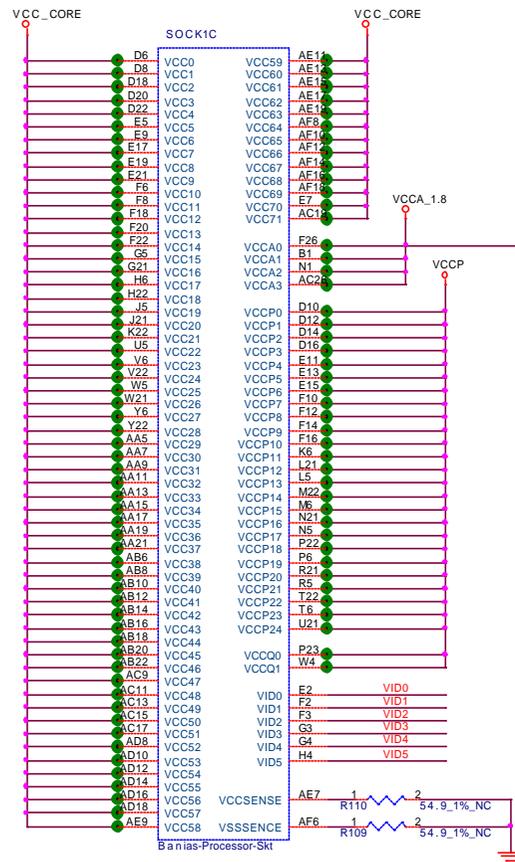
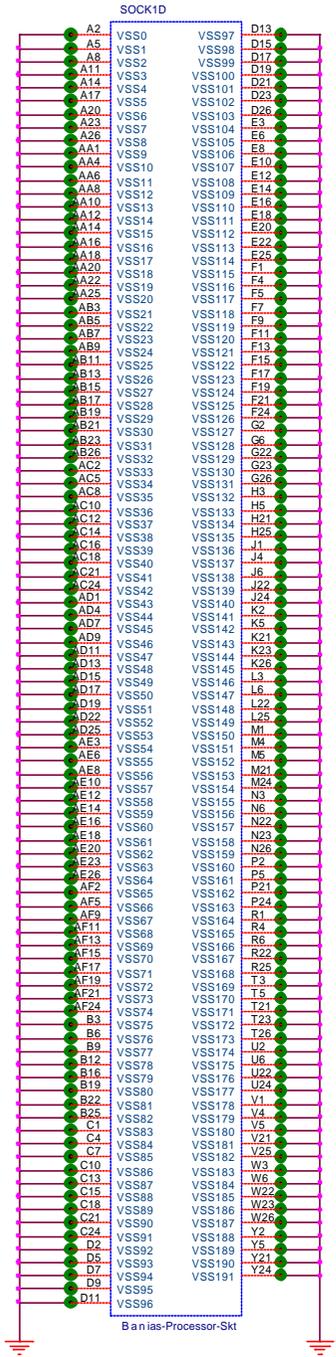
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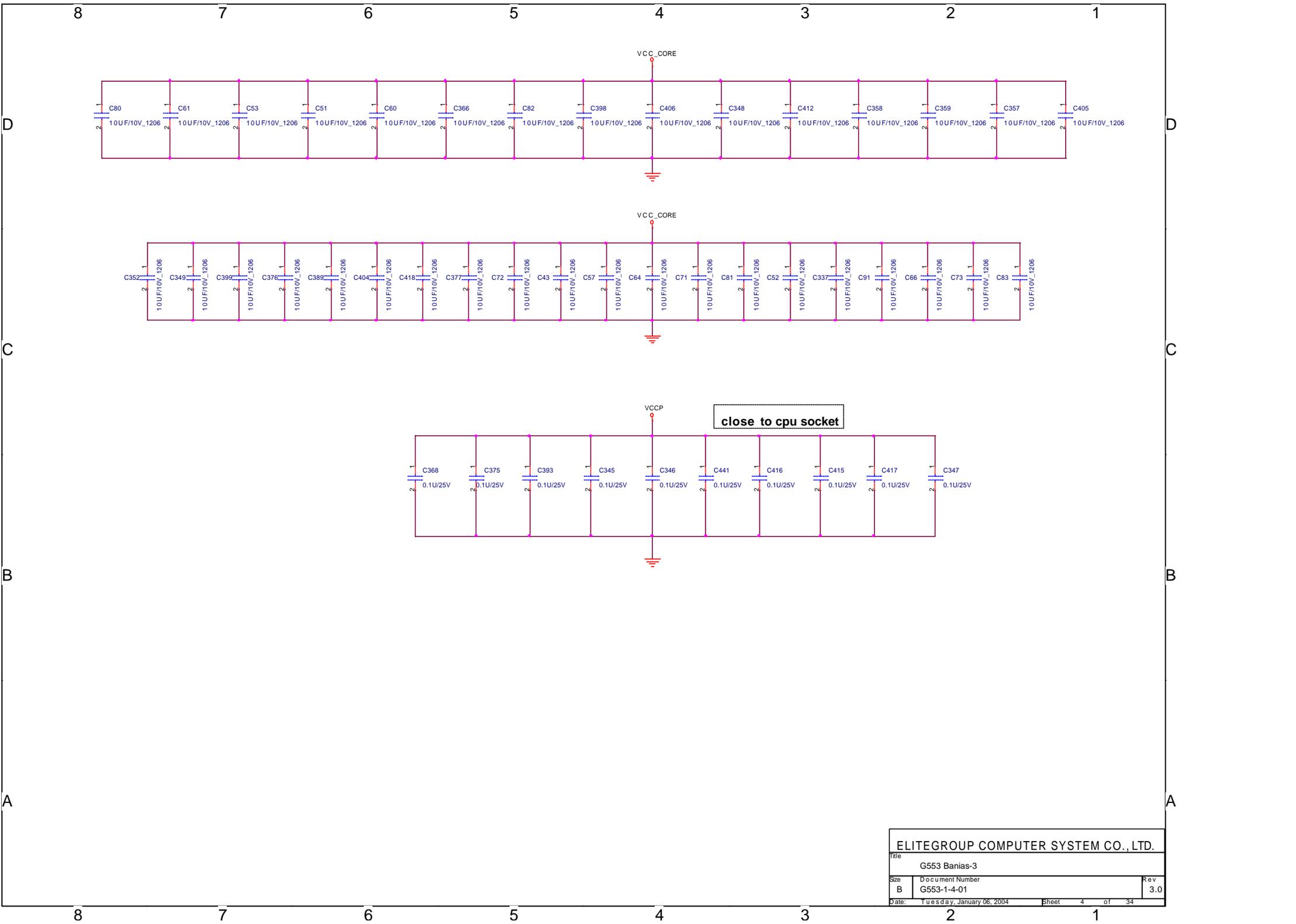
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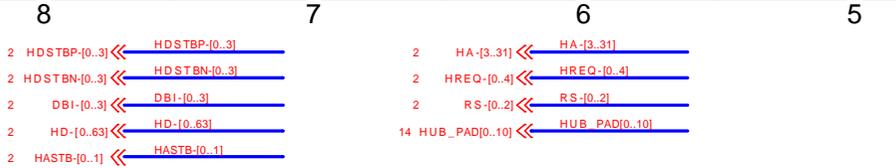


LAYOUT NOTE: Provide a test point (with no stub) to connent differential probe between VCCSENSE and VSSSENSE at the location where the two 54.9 ohm resistors terminate the 55 ohm transmission lines.

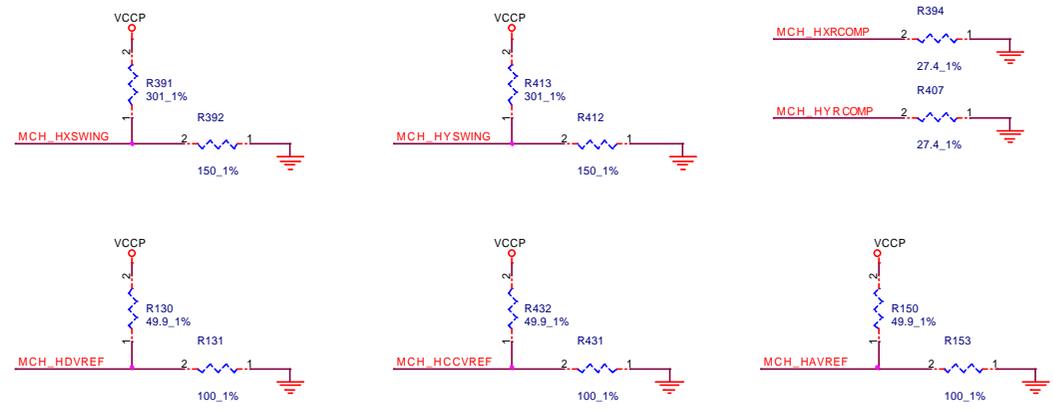


ELITEGROUP COMPUTER SYSTEM CO., LTD.		
Title G553 Banias-3		
Size B	Document Number G553-1-4-01	Rev 3.0
Date Tuesday, January 06, 2004	Sheet 4	of 34

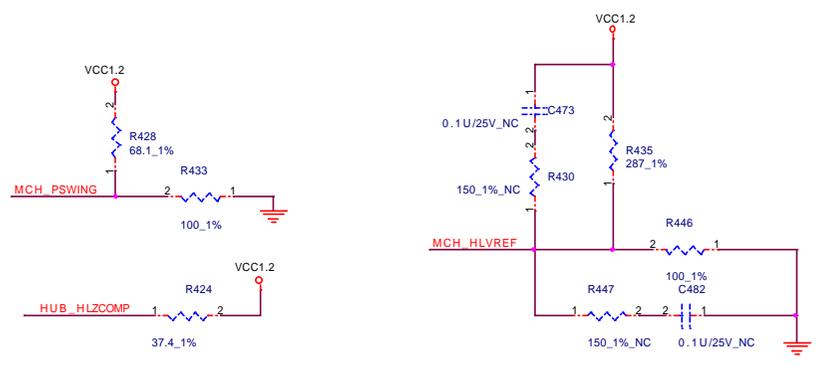
GMCH Compensation & Reference Voltages



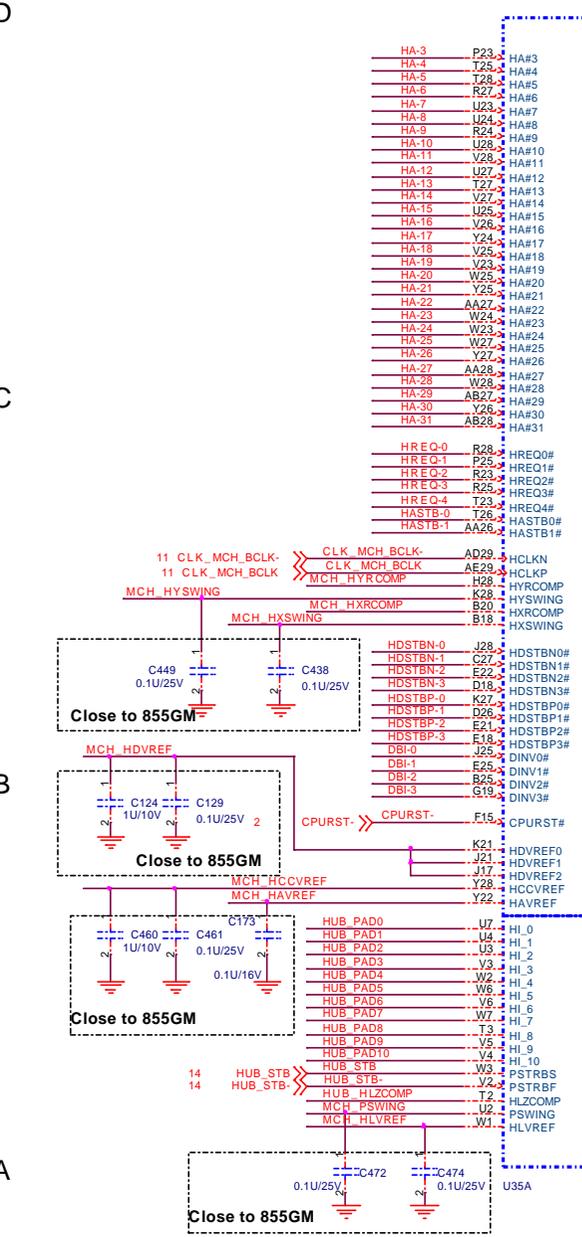
HOST



Hub Interface



855GM-1 HOST

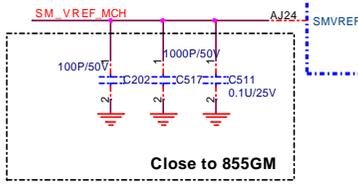


	855GM	855GME
R428	49.9_1%	68.1_1%
R424	27.4_1%	37.4_1%
R435	243_1%	287_1%

ELITEGROUP COMPUTER SYSTEM CO., LTD.		
G553 855GM (HOST/AGP)		
Size	Document Number	Rev
A3	G553-1-4-01	3.0
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DDR Serirs Termination

MD2	RP33D	5	4	10	8P4R	0402	/RMD2
MD3	RP33A	8	1	10	8P4R	0402	/RMD3
MD7	RP33B	7	2	10	8P4R	0402	/RMD7
MD6	RP33C	6	3	10	8P4R	0402	/RMD6
MD4	RP15A	8	1	10	8P4R	0402	/RMD4
MD1	RP15B	7	2	10	8P4R	0402	/RMD1
MD0	RP15C	6	3	10	8P4R	0402	/RMD0
MD5	RP15D	5	4	10	8P4R	0402	/RMD5
MD11	RP14A	8	1	10	8P4R	0402	/RMD11
MD14	RP14B	7	2	10	8P4R	0402	/RMD14
MD8	RP14C	6	3	10	8P4R	0402	/RMD8
MD12	RP14D	5	4	10	8P4R	0402	/RMD12
MD13	RP32A	8	1	10	8P4R	0402	/RMD13
MD9	RP32B	7	2	10	8P4R	0402	/RMD9
MD15	RP32C	6	3	10	8P4R	0402	/RMD15
MD10	RP32D	5	4	10	8P4R	0402	/RMD10
MD20	RP31A	8	1	10	8P4R	0402	/RMD20
MD17	RP31B	7	2	10	8P4R	0402	/RMD17
MD18	RP31C	6	3	10	8P4R	0402	/RMD18
MD19	RP31D	5	4	10	8P4R	0402	/RMD19
MD23	RP13A	8	1	10	8P4R	0402	/RMD23
MD21	RP13B	7	2	10	8P4R	0402	/RMD21
MD22	RP13C	6	3	10	8P4R	0402	/RMD22
MD16	RP13D	5	4	10	8P4R	0402	/RMD16
MD24	RP30A	8	1	10	8P4R	0402	/RMD24
MD28	RP30B	7	2	10	8P4R	0402	/RMD28
MD25	RP30C	6	3	10	8P4R	0402	/RMD25
MD26	RP30D	5	4	10	8P4R	0402	/RMD26
MD31	RP12A	8	1	10	8P4R	0402	/RMD31
MD27	RP12B	7	2	10	8P4R	0402	/RMD27
MD29	RP12C	6	3	10	8P4R	0402	/RMD29
MD30	RP12D	5	4	10	8P4R	0402	/RMD30
MD35	RP11A	8	1	10	8P4R	0402	/RMD35
MD34	RP11B	7	2	10	8P4R	0402	/RMD34
MD36	RP11C	6	3	10	8P4R	0402	/RMD36
MD37	RP11D	5	4	10	8P4R	0402	/RMD37
MD32	RP37A	8	1	10	8P4R	0402	/RMD32
MD33	RP37B	7	2	10	8P4R	0402	/RMD33
MD38	RP37C	6	3	10	8P4R	0402	/RMD38
MD39	RP37D	5	4	10	8P4R	0402	/RMD39
MD42	RP10A	8	1	10	8P4R	0402	/RMD42
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MD47	RP10C	6	3	10	8P4R	0402	/RMD47
MD44	RP10D	5	4	10	8P4R	0402	/RMD44
MD45	RP36A	8	1	10	8P4R	0402	/RMD45
MD41	RP36B	7	2	10	8P4R	0402	/RMD41
MD40	RP36C	6	3	10	8P4R	0402	/RMD40
MD46	RP36D	5	4	10	8P4R	0402	/RMD46
MD54	RP8A	8	1	10	8P4R	0402	/RMD54
MD50	RP8B	7	2	10	8P4R	0402	/RMD50
MD48	RP8C	6	3	10	8P4R	0402	/RMD48
MD53	RP8D	5	4	10	8P4R	0402	/RMD53
MD52	RP35A	8	1	10	8P4R	0402	/RMD52
MD49	RP35B	7	2	10	8P4R	0402	/RMD49
MD55	RP35C	6	3	10	8P4R	0402	/RMD55
MD51	RP35D	5	4	10	8P4R	0402	/RMD51
MD60	RP34A	8	1	10	8P4R	0402	/RMD60
MD56	RP34B	7	2	10	8P4R	0402	/RMD56
MD59	RP34C	6	3	10	8P4R	0402	/RMD59
MD58	RP34D	5	4	10	8P4R	0402	/RMD58
MD63	RP9A	8	1	10	8P4R	0402	/RMD63
MD62	RP9B	7	2	10	8P4R	0402	/RMD62
MD57	RP9C	6	3	10	8P4R	0402	/RMD57
MD61	RP9D	5	4	10	8P4R	0402	/RMD61
/RDS0	R492	2	10	1%	0402	M DS0	
/RDS1	R203	2	10	1%	0402	M DS1	
/RDS2	R203	2	10	1%	0402	M DS2	
/RDS3	R202	2	10	1%	0402	M DS3	
/RDS4	R200	2	10	1%	0402	M DS4	
/RDS5	R198	2	10	1%	0402	M DS5	
/RDS6	R196	2	10	1%	0402	M DS6	
/RDS7	R194	2	10	1%	0402	M DS7	
/RDM0	R207	2	10	1%	0402	M DM0	
/RDM1	R206	2	10	1%	0402	M DM1	
/RDM2	R204	2	10	1%	0402	M DM2	
/RDM3	R201	2	10	1%	0402	M DM3	
/RDM4	R199	2	10	1%	0402	M DM4	
/RDM5	R197	2	10	1%	0402	M DM5	
/RDM6	R195	2	10	1%	0402	M DM6	
/RDM7	R493	2	10	1%	0402	M DM7	

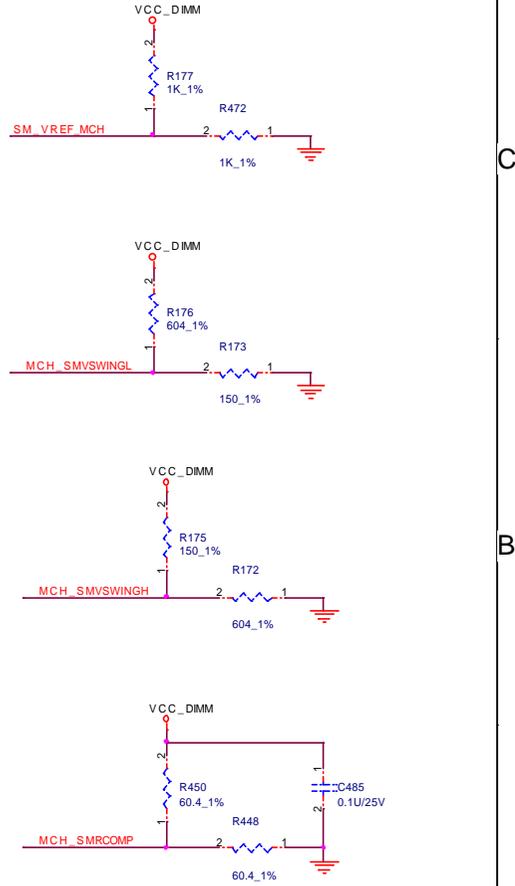


855GM-2 DDR SYSTEM MEMORY

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MD2	AF2	SDQ2
MD3	AF2	SDQ3
MD4	AD3	SDQ4
MD5	AE2	SDQ5
MD6	AG4	SDQ6
MD7	AH3	SDQ7
MD8	AD3	SDQ8
MD9	AG5	SDQ9
MD10	AG7	SDQ10
MD11	AE8	SDQ11
MD12	AF5	SDQ12
MD13	AH4	SDQ13
MD14	AF7	SDQ14
MD15	AF8	SDQ15
MD16	AG8	SDQ16
MD17	AG8	SDQ17
MD18	AH9	SDQ18
MD19	AH9	SDQ19
MD20	AH9	SDQ20
MD21	AD5	SDQ21
MD22	AF10	SDQ22
MD23	AE11	SDQ23
MD24	AH10	SDQ24
MD25	AE11	SDQ25
MD26	AH11	SDQ26
MD27	AF14	SDQ27
MD28	AG11	SDQ28
MD29	AD12	SDQ29
MD30	AF13	SDQ30
MD31	AH13	SDQ31
MD32	AD13	SDQ32
MD33	AG17	SDQ33
MD34	AF19	SDQ34
MD35	AE20	SDQ35
MD36	AD18	SDQ36
MD37	AE18	SDQ37
MD38	AH18	SDQ38
MD39	AH19	SDQ39
MD40	AH20	SDQ40
MD41	AG20	SDQ41
MD42	AF22	SDQ42
MD43	AH22	SDQ43
MD44	AH22	SDQ44
MD45	AH19	SDQ45
MD46	AH21	SDQ46
MD47	AG22	SDQ47
MD48	AE23	SDQ48
MD49	AH23	SDQ49
MD50	AE24	SDQ50
MD51	AH25	SDQ51
MD52	AG23	SDQ52
MD53	AF23	SDQ53
MD54	AE24	SDQ54
MD55	AG25	SDQ55
MD56	AH25	SDQ56
MD57	AE26	SDQ57
MD58	AG26	SDQ58
MD59	AF26	SDQ59
MD60	AG26	SDQ60
MD61	AF26	SDQ61
MD62	AE27	SDQ62
MD63	AD27	SDQ63
	AG14	SDQ64
	AE14	SDQ65
	AE17	SDQ66
	AG14	SDQ67
	AH14	SDQ68
	AE15	SDQ69
	AF16	SDQ70
	AF17	SDQ71

SMA0	AC18	M A0	M A0	9,10
SMA1	AD14	M A1	M A1	9,10
SMA2	AD13	M A2	M A2	9,10
SMA3	AD17	M A3	M A3	9,10
SMA4	AD11	M A4	M A4	9,10
SMA5	AC13	M A5	M A5	9,10
SMA6	AD8	M A6	M A6	9,10
SMA7	AD7	M A7	M A7	9,10
SMA8	AC6	M A8	M A8	9,10
SMA9	AC5	M A9	M A9	9,10
SMA10	AC19	M A10	M A10	9,10
SMA11	AD5	M A11	M A11	9,10
SMA12	AB5	M A12	M A12	9,10
SMA_B1	AD16	M AB1	M AB1	9,10
SMA_B2	AC12	M AB2	M AB2	9,10
SMA_B4	AF11	M AB4	M AB4	9,10
SMA_B5	AD10	M AB5	M AB5	9,10
SCKE0	AC7	CKE0		
SCKE1	AB7	CKE1		
SCKE2	AC9	CKE2		
SCKE3	AC10	CKE3		
SCS0#	AD23	RCS-0		
SCS1#	AC22	RCS-2		
SCS2#	AC25	RCS-3		
SCS3#				
SBA0	AD22	M BS0	M BS0	9,10
SBA1	AD20	M BS1	M BS1	9,10
SRAS#	AC21	M RAS-	M RAS-	9,10
SCAS#	AC24	M CAS-	M CAS-	9,10
SWE#	AD25	M WE-	M WE-	9,10
SCK0	AB2	M CLK0		
SCK40	AA2	M CLK0		
SCK1	AC26	M CLK1		
SCK#1	AB25	M CLK1		
SCK2	AC3	M CLK2		
SCK#2	AD4	M CLK2		
SCK3	AC2	M CLK3		
SCK#3	AD3	M CLK3		
SCK4	AB23	M CLK4		
SCK#4	AB24	M CLK4		
SCK5	AA3	M CLK5		
SCK#5	AB4	M CLK5		
SDQS0	AH5	M DS0		
SDQS1	AH8	M DS2		
SDQS2	AE12	M DS3		
SDQS3	AH17	M DS4		
SDQS4	AE21	M DS5		
SDQS5	AH24	M DS6		
SDQS6	AH27	M DS7		
SDQS7	AD15			
SDQS8	AE5	M DM0		
SDM0	AE6	M DM1		
SDM1	AE9	M DM2		
SDM2	AH12	M DM3		
SDM3	AD19	M DM4		
SDM4	AD21	M DM5		
SDM5	AD24	M DM6		
SDM6	AH28	M DM7		
SDM7	AH19			
SDM8	AH19			

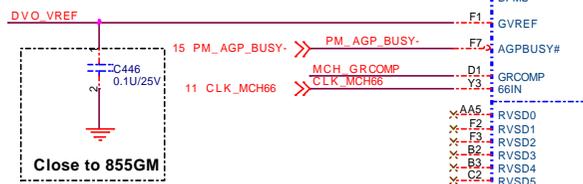
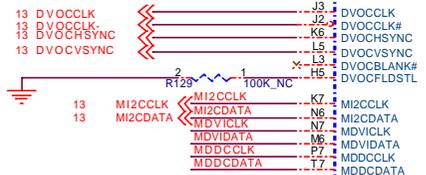
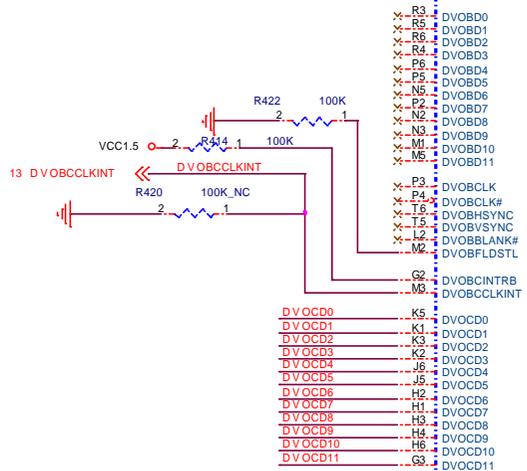
System Memory



ELITEGROUP COMPUTER SYSTEM CO., LTD.

Title	G553-855GM-2 (RAM CONTROLLER)	
Size	Document Number	Rev
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13 DVOCDD[0..11] >> DVOCDD[0..11]



855GM-3

- X-R3 DVOBD0
- X-R5 DVOBD1
- X-R6 DVOBD2
- X-R4 DVOBD3
- X-P6 DVOBD4
- X-P5 DVOBD5
- X-N5 DVOBD6
- X-F2 DVOBD7
- X-N2 DVOBD8
- X-M2 DVOBD9
- X-M1 DVOBD10
- X-M5 DVOBD11
- X-P3 DVOBCLK
- X-P4 DVOBCLK#
- X-T6 DVOBHSYNC
- X-T5 DVOBVSNC
- X-L2 DVOBBLANK#
- X-M2 DVOBFLDSTL
- G2 DVOBCINTRB
- M3 DVOBCKLINT
- K5 DVOCDD0
- K1 DVOCDD1
- K3 DVOCDD2
- K2 DVOCDD3
- K6 DVOCDD4
- J6 DVOCDD5
- J5 DVOCDD6
- H2 DVOCDD7
- H3 DVOCDD8
- H4 DVOCDD9
- H6 DVOCDD10
- G3 DVOCDD11
- J3 DVOCCLK
- J2 DVOCCLK#
- K6 DVOCHSYNC
- L5 DVOCVSYNC
- X-L3 DVOCBLANK#
- X-H5 DVOCFDSTL
- K7 MI2CCLK
- N6 MI2CDATA
- N7 MDVICLK
- M6 MDVIDATA
- L7 MDDCCLK
- L7 MDDCDATA
- E5 ADDID0
- F5 ADDID1
- E3 ADDID2
- E2 ADDID3
- G5 ADDID4
- F4 ADDID5
- G6 ADDID6
- F8 ADDID7
- X-L7 ADDETECT
- D5 DPMS
- F1 GVREF
- F7 AGPBUSY#
- D1 GRCOMP
- Y3 CLK_MCH66
- AA5 RVSD0
- X-F2 RVSD1
- X-F3 RVSD2
- X-B2 RVSD3
- X-B3 RVSD4
- X-C2 RVSD5
- X-C3 RVSD6
- X-C4 RVSD7
- X-D2 RVSD8
- X-D3 RVSD9
- X-D7 RVSD10
- X-L4 RVSD11

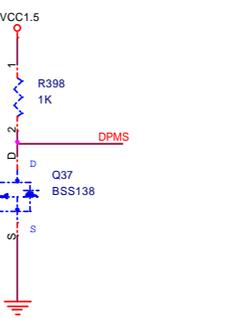
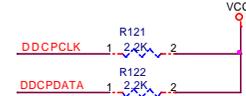
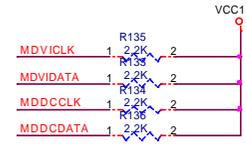
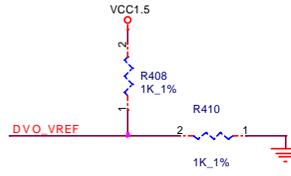
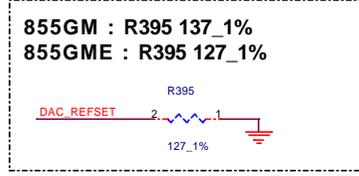
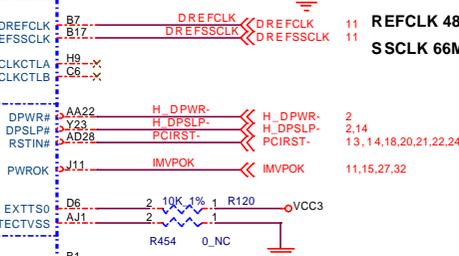
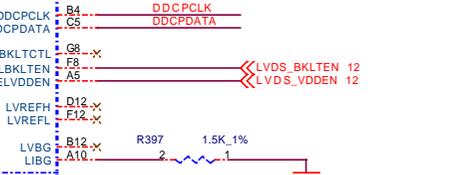
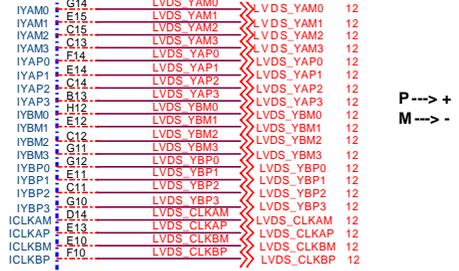
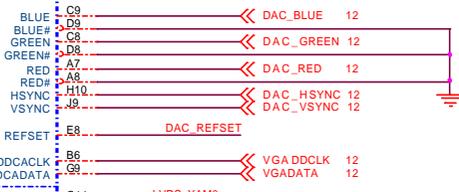
DAC

DVO

LVDS

MISC CLKS

NC



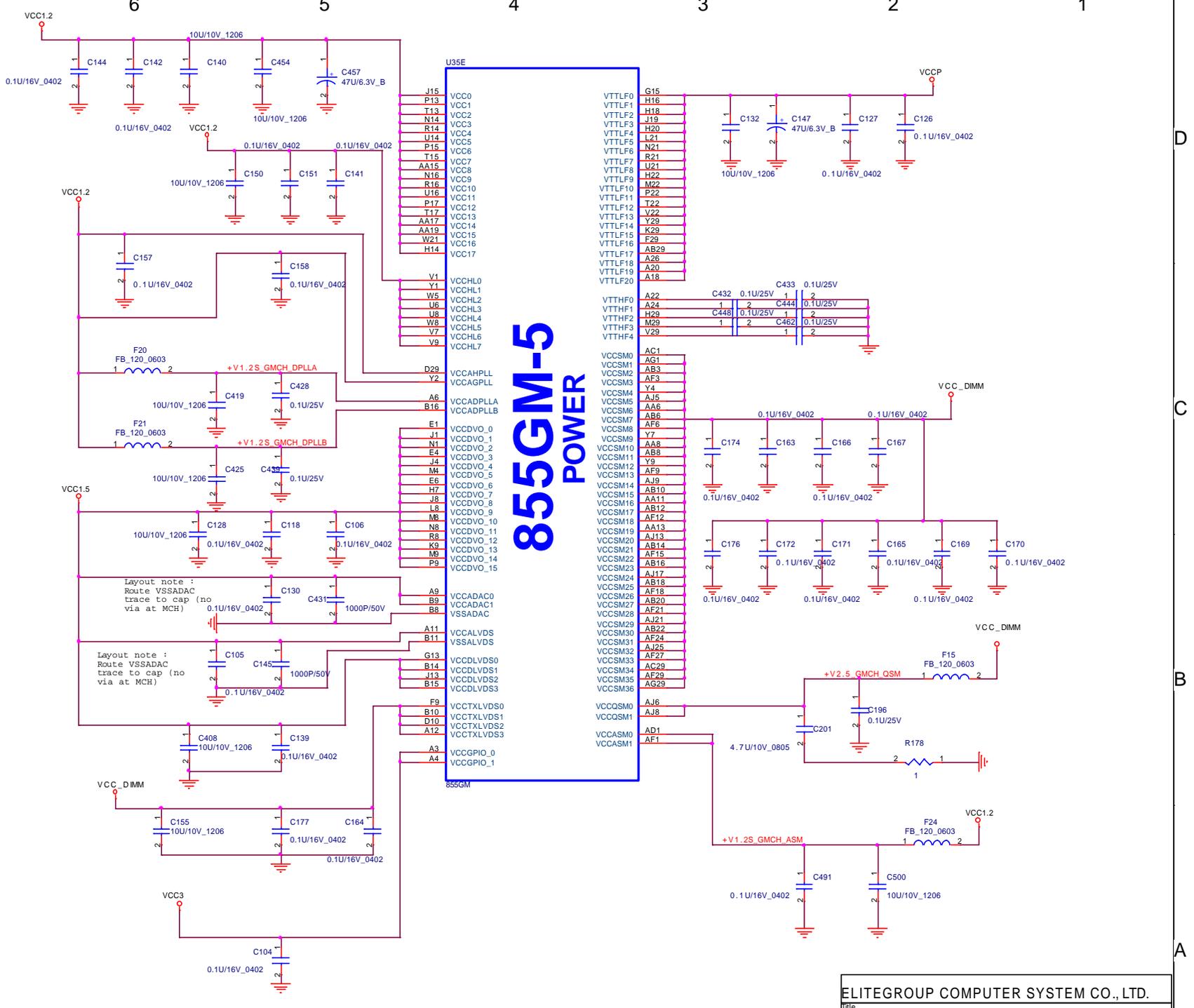
REFCLK 48MHZ
SSCLK 66MHZ

INTEL RECOMMAND

855GM-4 VSS

855GM-5 POWER

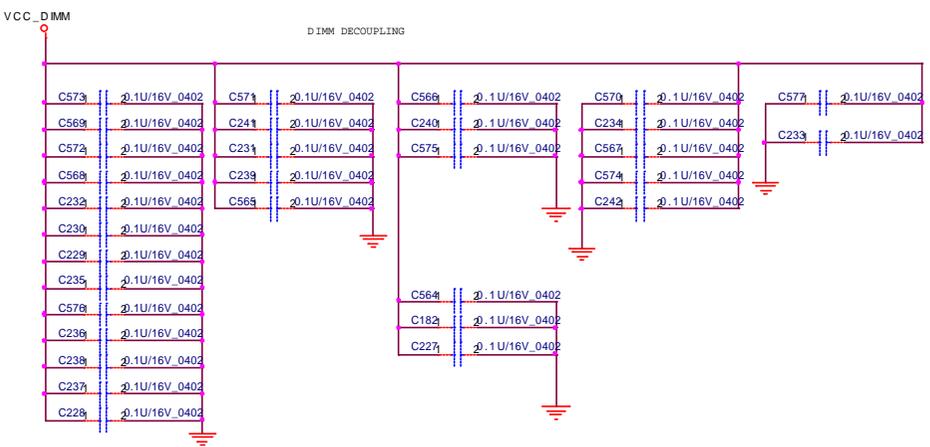
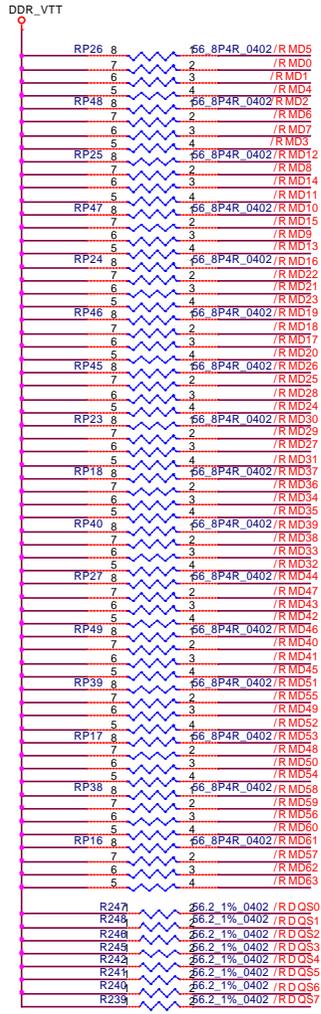
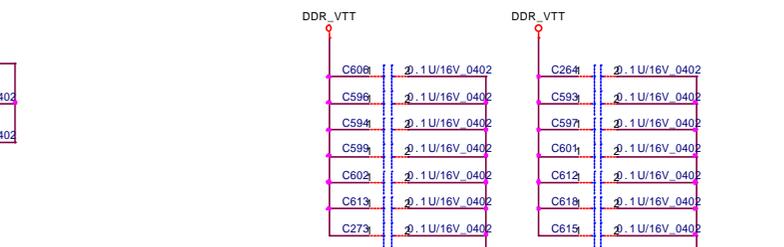
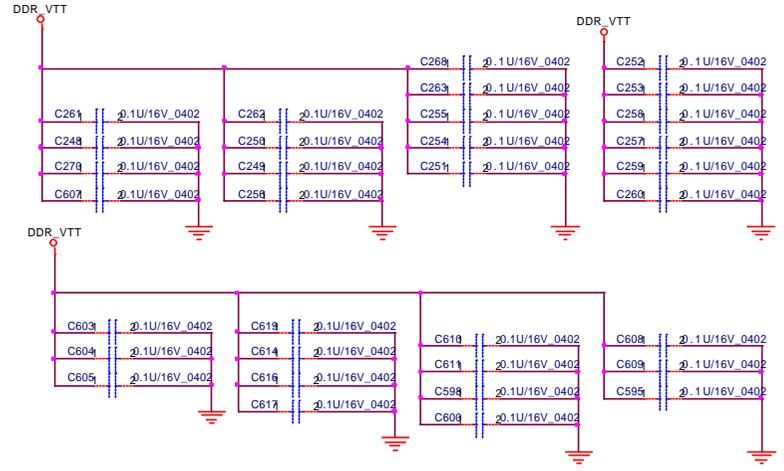
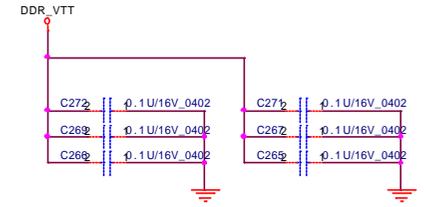
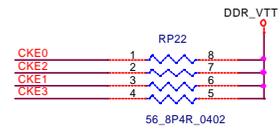
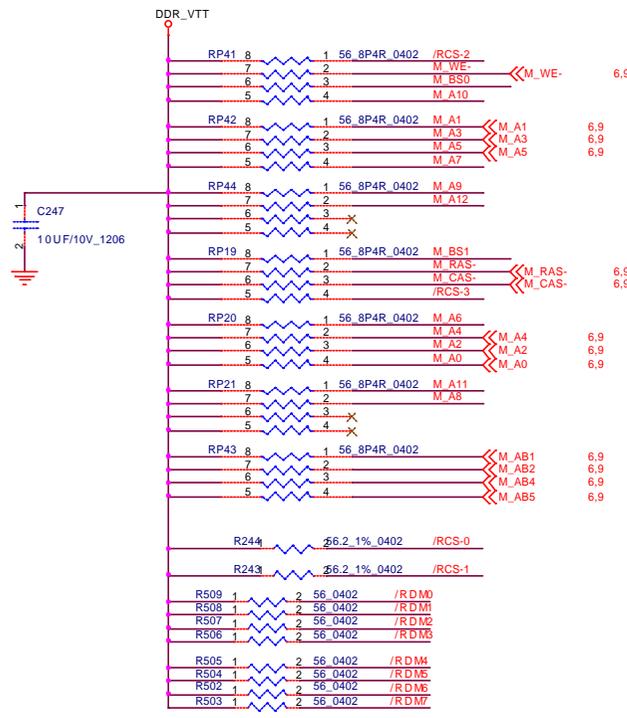
C1	VSS0	VSS91	R17
G1	VSS1	VSS92	U17
L1	VSS2	VSS93	AB17
U1	VSS3	VSS94	AC17
AE1	VSS4	VSS95	F18
R2	VSS5	VSS96	AA18
AG3	VSS6	VSS97	AG18
AS7	VSS7	VSS98	VSS98
AJ3	VSS8	VSS99	A19
D4	VSS9	VSS100	D19
K4	VSS10	VSS101	H19
N4	VSS11	VSS102	AE19
T4	VSS12	VSS103	F20
W4	VSS13	VSS105	VSS105
AA4	VSS14	VSS106	AA20
AC4	VSS15	VSS107	AC20
AE4	VSS16	VSS108	A21
VSS17	VSS17	VSS109	D21
B5	VSS18	VSS110	H21
U5	VSS19	VSS111	U21
Y5	VSS20	VSS112	M21
Y6	VSS21	VSS113	P21
AC6	VSS22	VSS114	V21
C7	VSS23	VSS115	Y21
E7	VSS24	VSS116	AA21
G7	VSS25	VSS117	AB21
J7	VSS26	VSS118	AG21
M7	VSS27	VSS119	A22
R7	VSS28	VSS120	F22
AA7	VSS29	VSS121	J22
AE7	VSS30	VSS122	U22
AJ7	VSS31	VSS123	L22
H8	VSS32	VSS124	N22
K8	VSS33	VSS125	R22
P8	VSS34	VSS126	W22
T8	VSS35	VSS127	AE22
V8	VSS36	VSS129	A23
Y8	VSS37	VSS129	D23
AC8	VSS38	VSS130	AA23
AE8	VSS39	VSS131	AC23
AG8	VSS40	VSS132	AJ23
AS8	VSS41	VSS133	F24
AW8	VSS42	VSS134	H24
U9	VSS43	VSS135	K24
W9	VSS44	VSS136	R24
Y9	VSS45	VSS137	P24
AC9	VSS46	VSS138	T24
AE9	VSS47	VSS139	V24
AG9	VSS48	VSS140	AA24
AS9	VSS49	VSS141	AG24
AW9	VSS50	VSS142	D25
D11	VSS51	VSS143	AA25
F11	VSS52	VSS144	AE25
H11	VSS53	VSS145	G26
AB11	VSS54	VSS146	U26
AC11	VSS55	VSS147	W26
AJ11	VSS56	VSS148	A27
J12	VSS57	VSS149	F27
AA12	VSS58	VSS150	AG27
AG12	VSS59	VSS151	AJ27
A13	VSS60	VSS152	AC28
D13	VSS61	VSS153	C29
F13	VSS62	VSS154	E29
H13	VSS63	VSS154	G29
N13	VSS64	VSS155	J29
R13	VSS65	VSS156	L29
U13	VSS66	VSS157	N29
AB13	VSS67	VSS158	U29
AE13	VSS68	VSS159	W29
AG13	VSS69	VSS160	AA29
AS13	VSS70	VSS161	AJ10
AW13	VSS71	VSS162	AJ12
D15	VSS72	VSS164	AJ20
F15	VSS73	VSS165	C22
H15	VSS74	VSS166	D28
N15	VSS75	VSS167	E28
R15	VSS76	VSS168	L28
U15	VSS77	VSS169	T29
Y15	VSS78	VSS170	AJ26
AC15	VSS79	VSS171	
AE15	VSS80	VSS172	
AG15	VSS81	VSS173	
AS15	VSS82	VSS174	
AW15	VSS83	VSS175	
D16	VSS84	VSS176	
F16	VSS85	VSS177	
H16	VSS86	VSS178	
N16	VSS87	VSS179	
R16	VSS88	VSS180	
U16	VSS89		
Y16	VSS90		



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DDR Parallel Termination

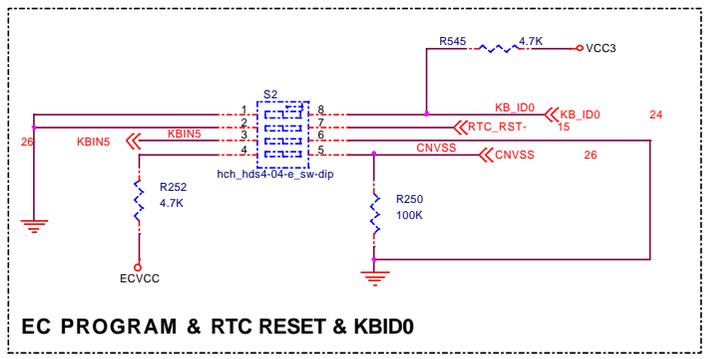
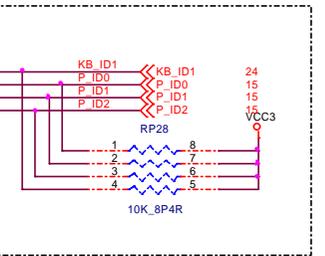
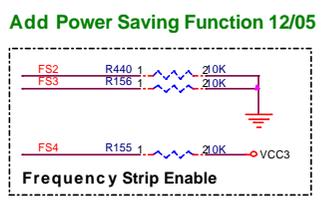
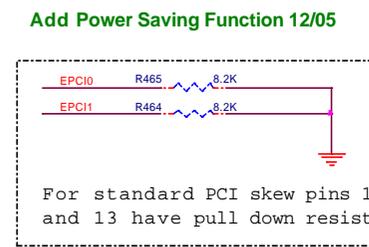
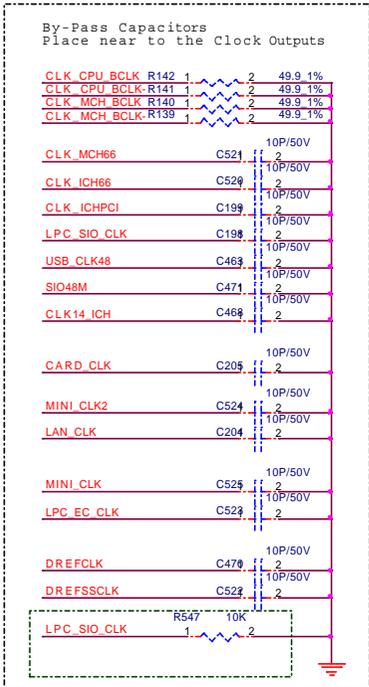
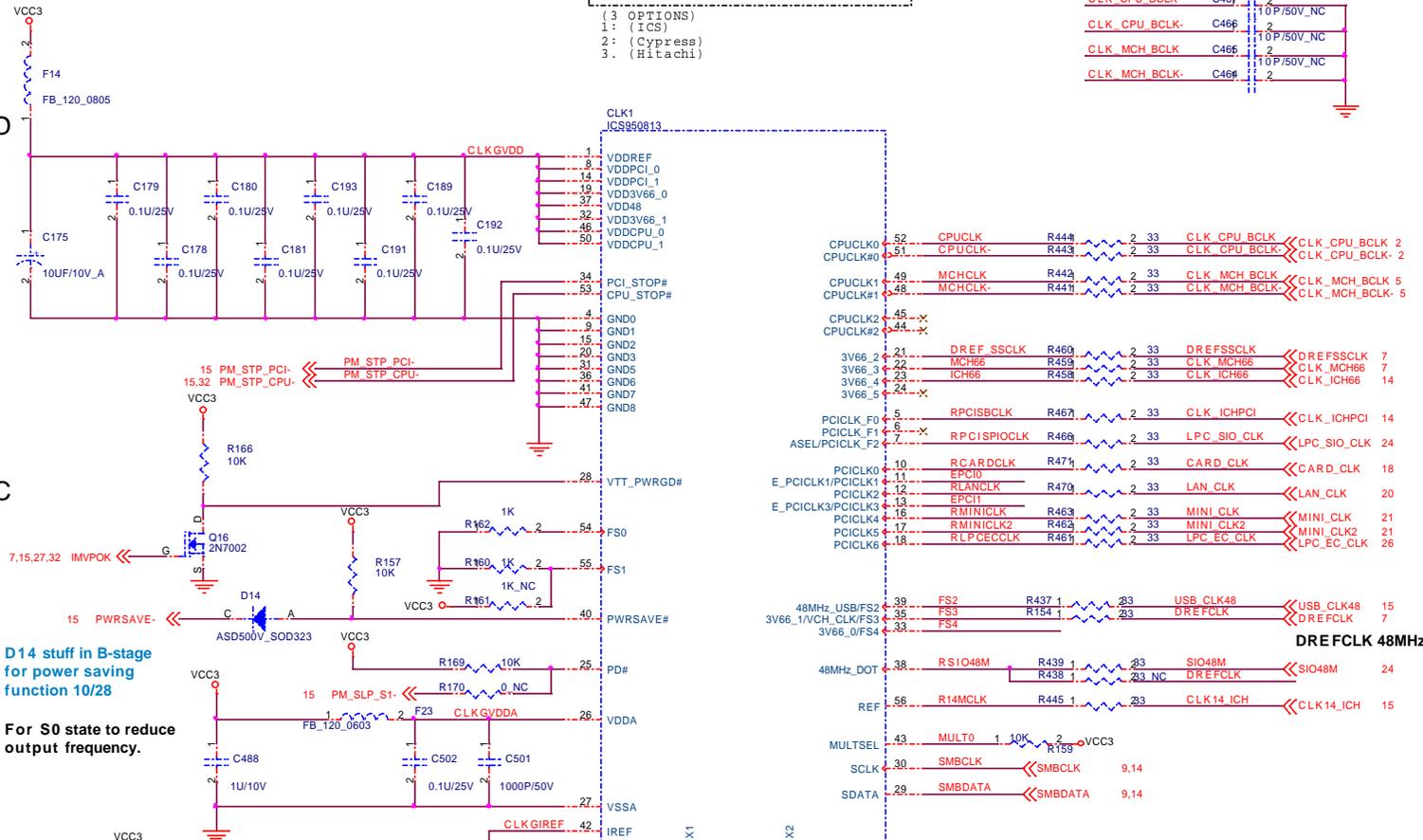
- 5 /RMD[0..63] << /RMD[0..63] 6.9
- /RDS[0..7] << /RDS[0..7] 6.9
- M_A[6..12] << M_A[6..12] 6.9
- /RCS[0..3] << /RCS[0..3] 6.9
- CKE[0..3] << CKE[0..3] 6.9
- M_BS[0..1] << M_BS[0..1] 6.9
- /RDM[0..7] << /RDM[0..7] 6.9



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Main Clock Generator

- (3 OPTIONS)
- 1: (ICS)
- 2: (Cypress)
- 3: (Hitachi)



D14 stuff in B-stage for power saving function 10/28

For S0 state to reduce output frequency.

INTERNAL PULL UP PIN
 ICS 120K Internal Pull up to VDD
 CPU_STOP#
 PCI_STOP#
 PWRSAVE#
 MULTSEL#
 PD#

VCC_GME	1.2V	1.35V
S2-Pin1	O	X
S2-Pin8	X	O

	CPU	AGP	PCI	
General	100MHZ	66MHZ	33MHZ	FS0:0 Defult
PWRSAVE#	80MHZ	53MHZ	26MHZ	FS1:0
				FS2:0
				FS3:0
				FS4:1
				Frequency Strip

PANEL ID SW & KB ID1

INVERTER

LVDS

Modify EMI 12/05

Modify EMI 12/05

Function will not used
Remove D8 PCIRST control 10/27

Enable panel power

LID SWITCH

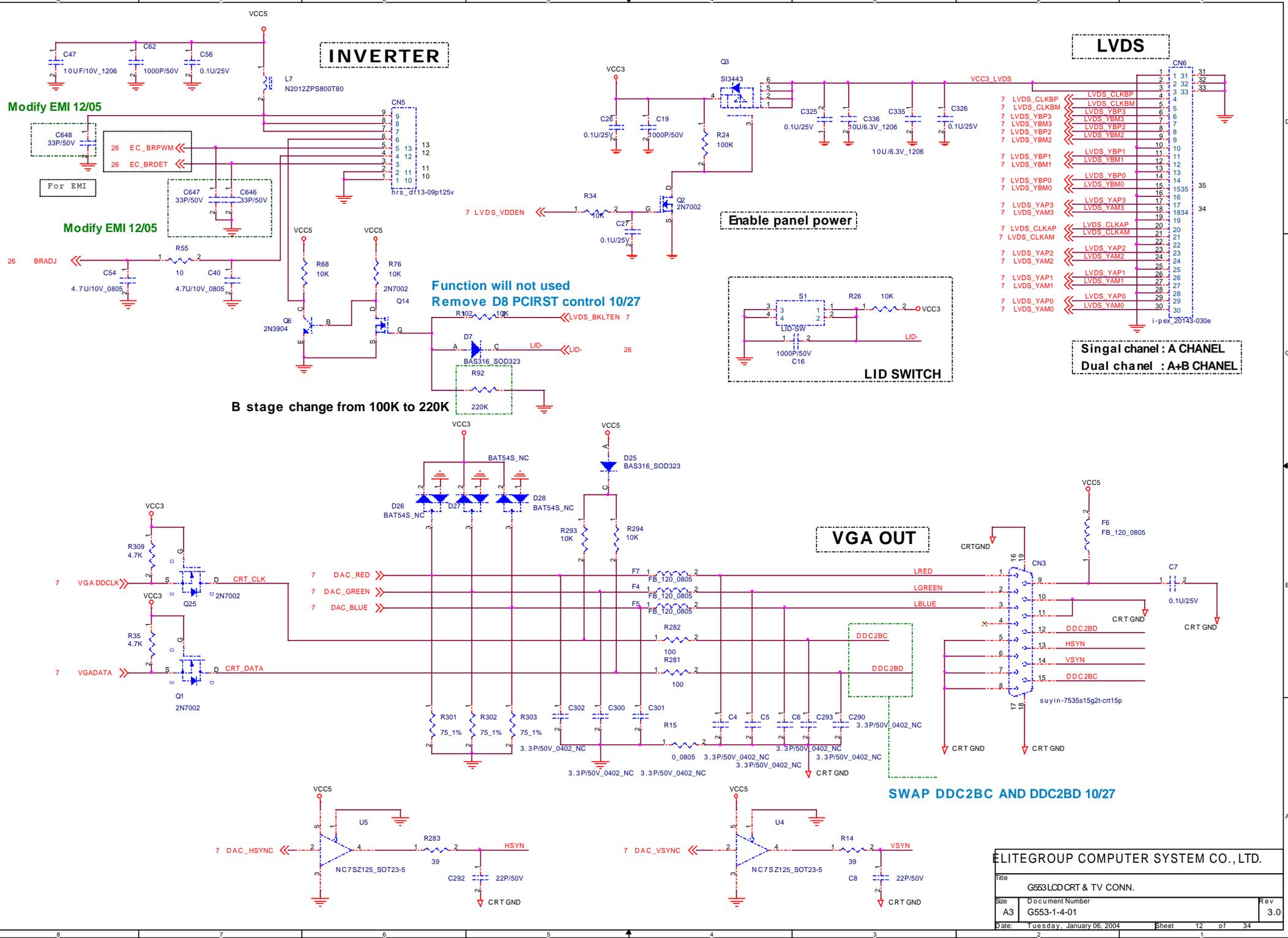
Singal chanel : A CHANEL
Dual chanel : A+B CHANEL

B stage change from 100K to 220K

VGA OUT

SWAP DDC2BC AND DDC2BD 10/27

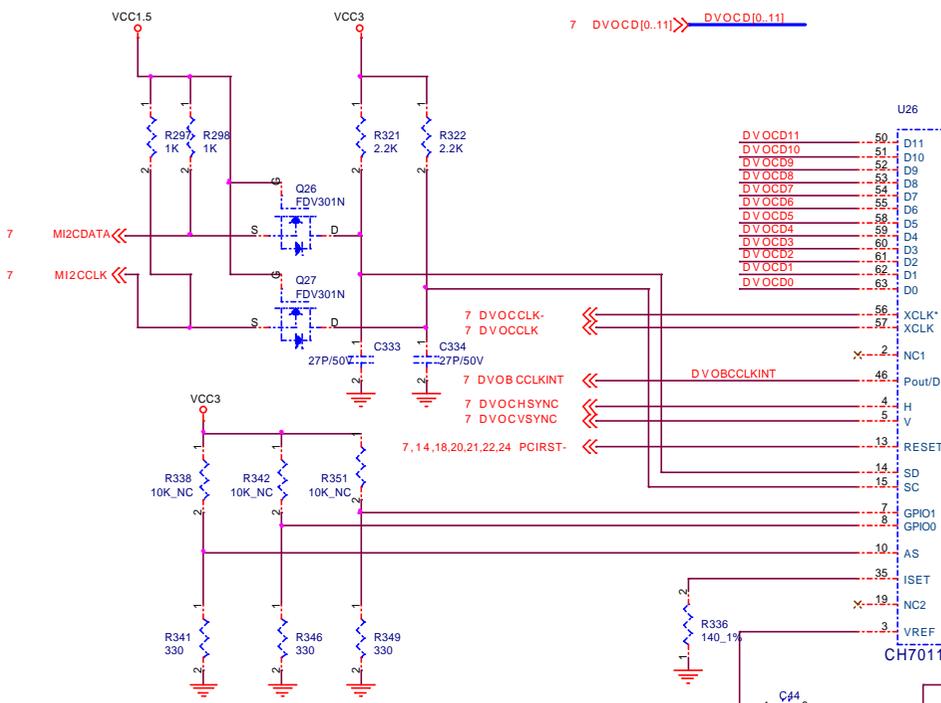
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Title	G553LCD CRT & TV CONN.	
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7 ADDID7 << 1K 1 R127 2

ADD ID Selector
 Default ID: 0 1 1 1 1 1 1
 Stuff:R571

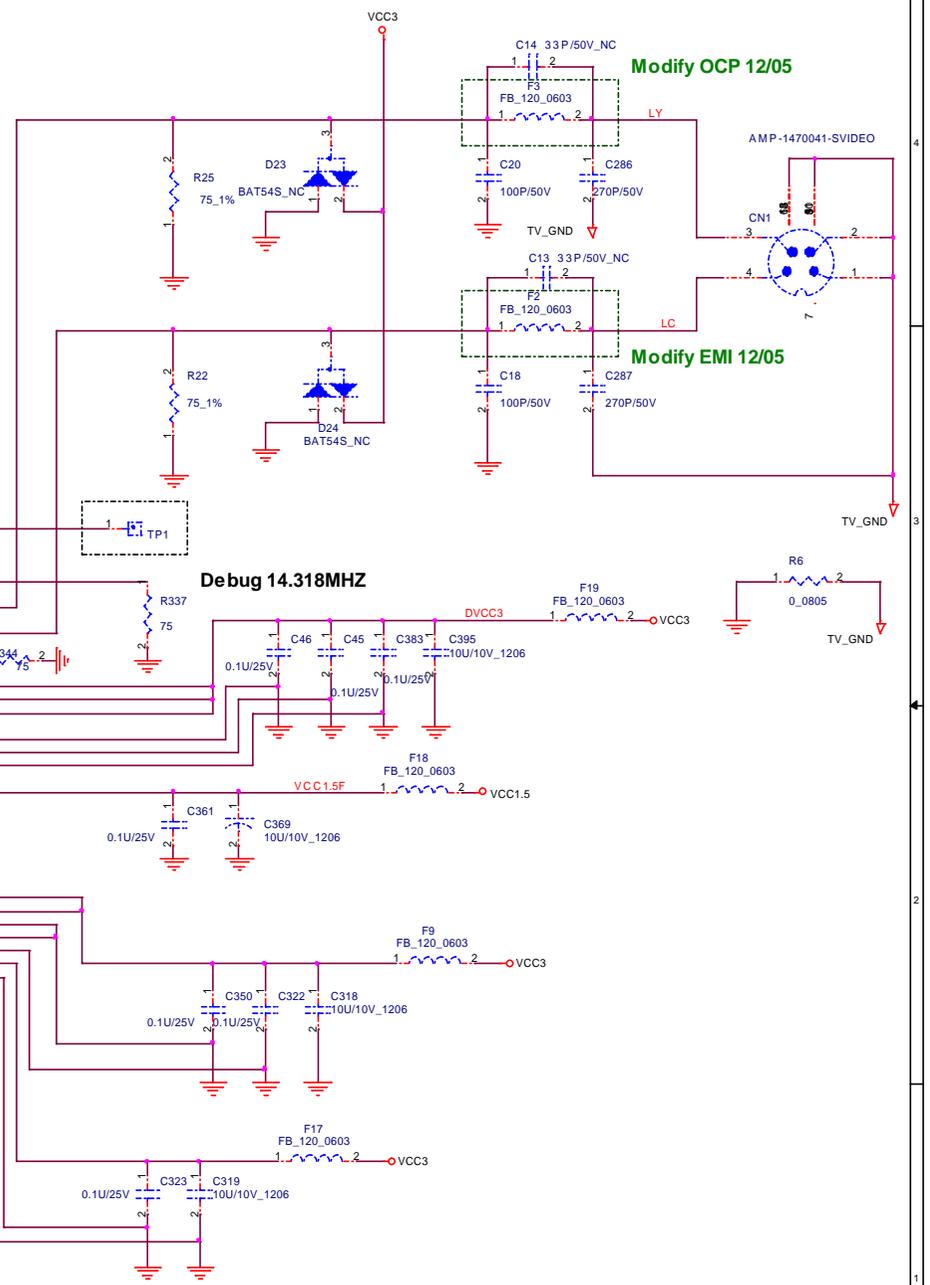
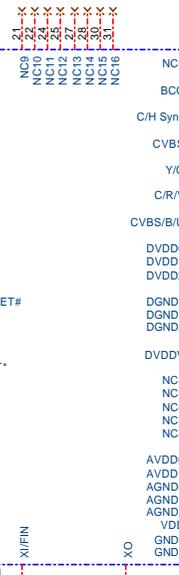
7 DVOC[0..11] >> DVOC[0..11]



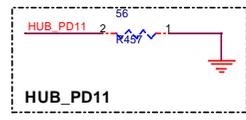
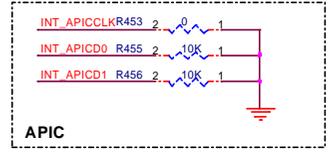
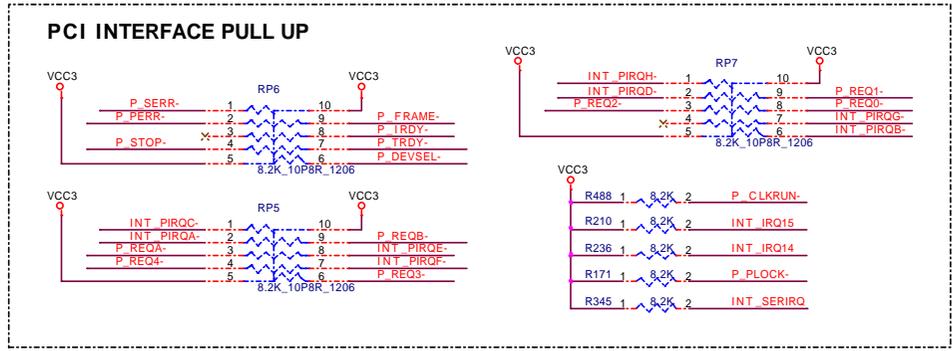
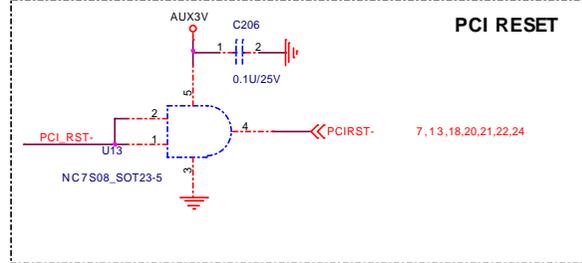
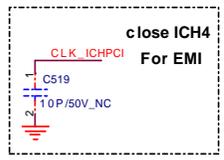
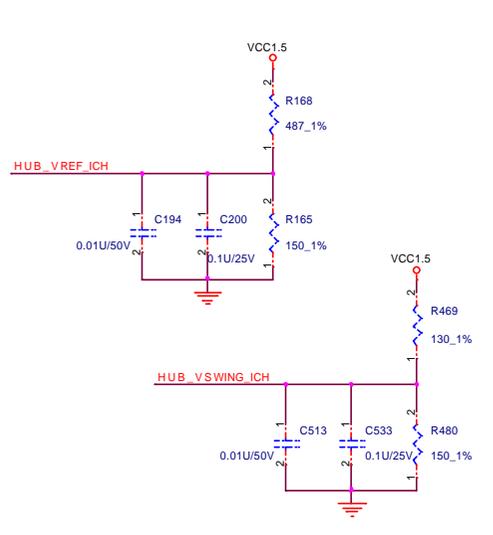
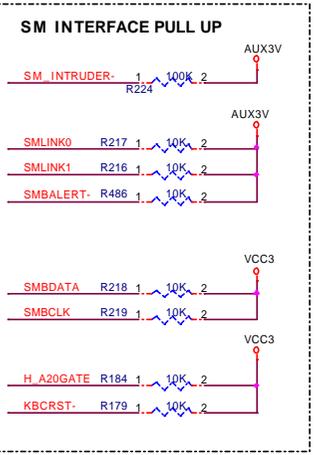
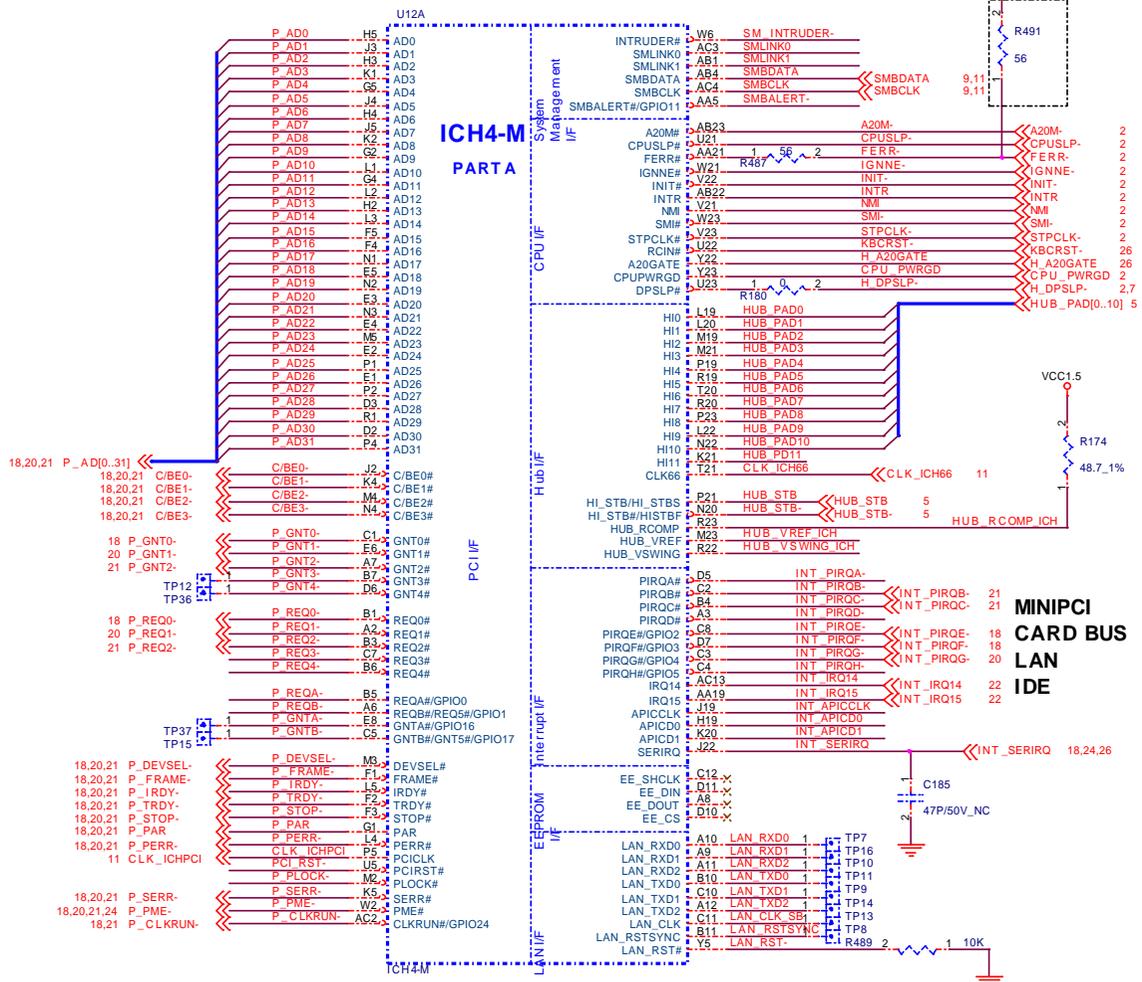
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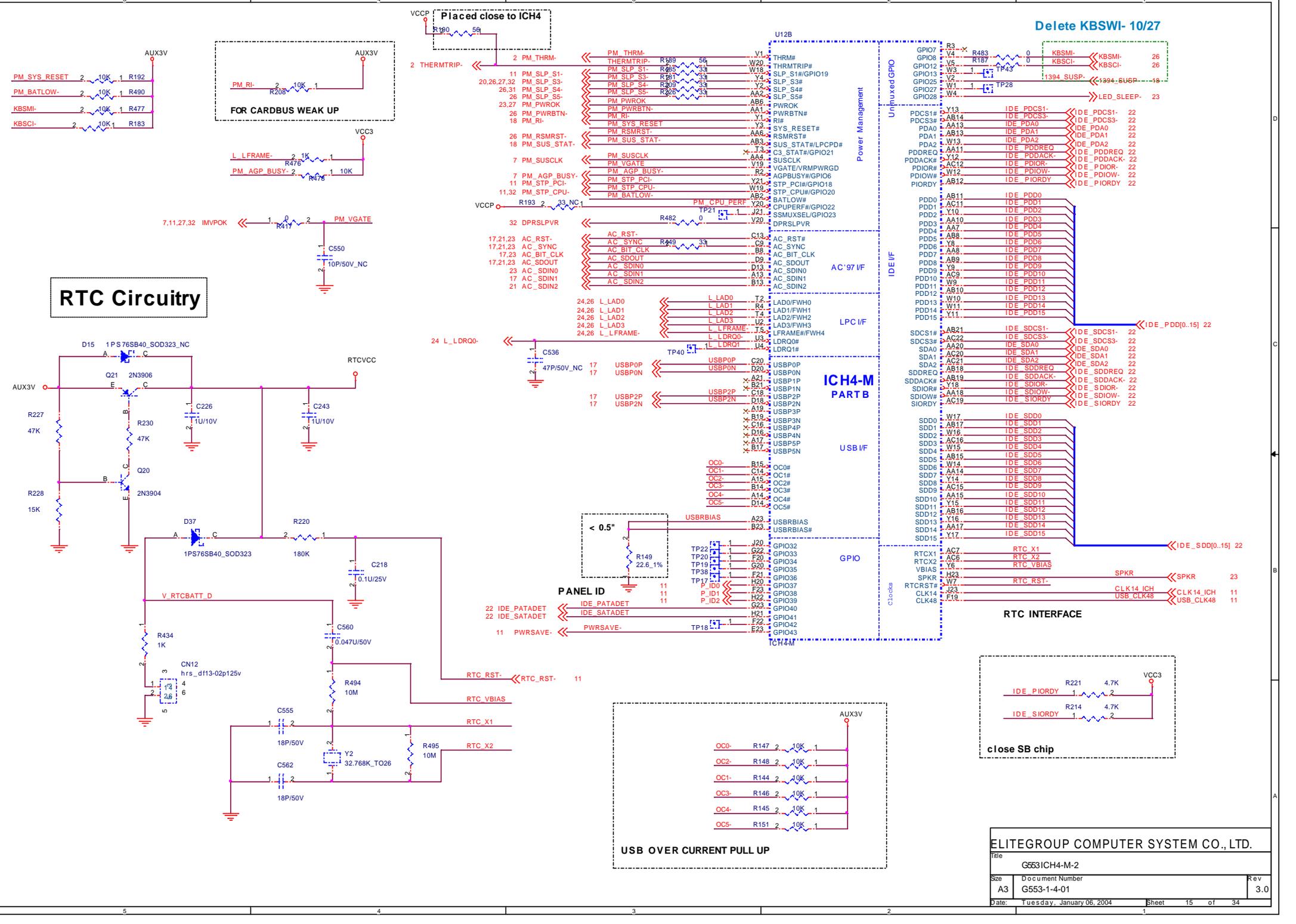
CH7011 Address			
Mode	Stuff	No Stuff	
0X75	R540	R27	
0X76	R27	R540	

NTSC or PAL			
Mode	Stuff	No Stuff	
PAL	R543	R28	
NTSC	R28	R543	

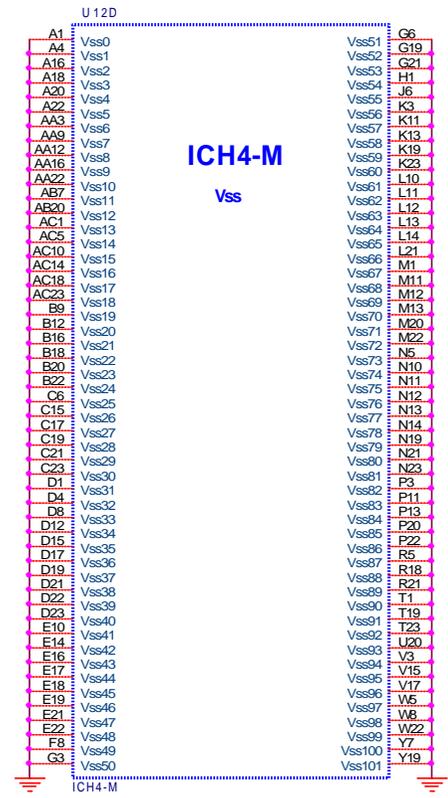
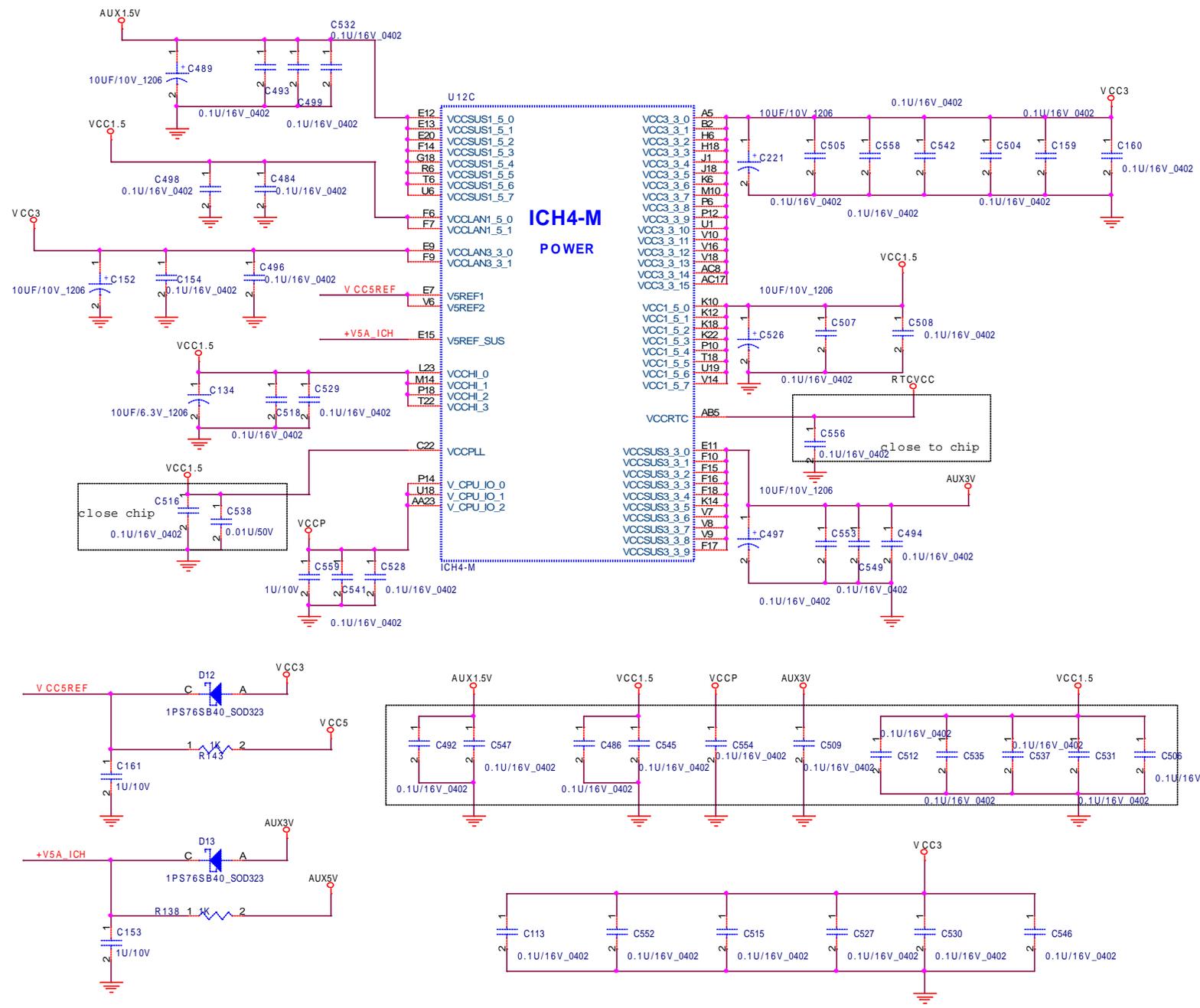


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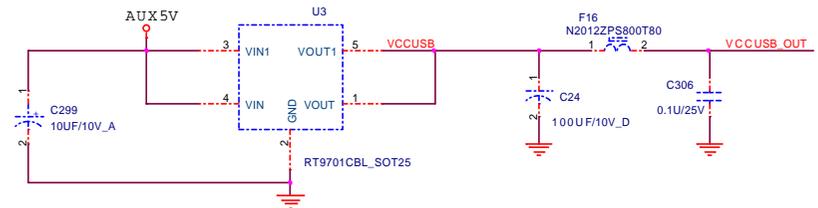


RTC Circuitry

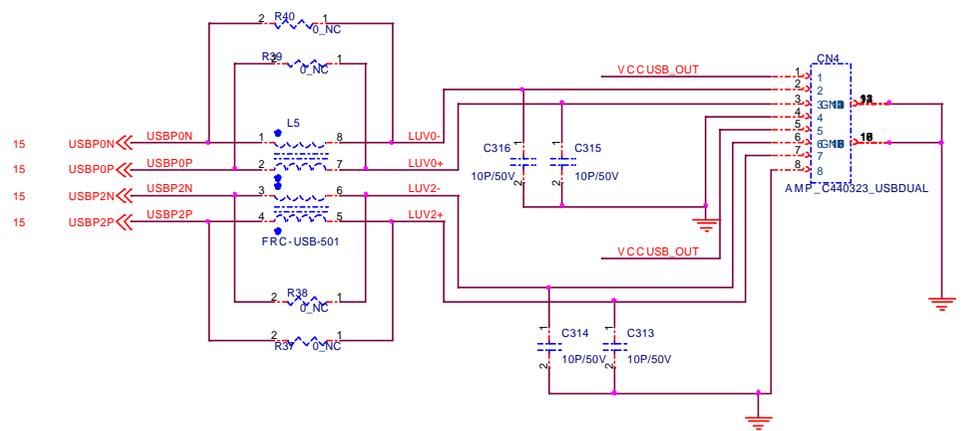
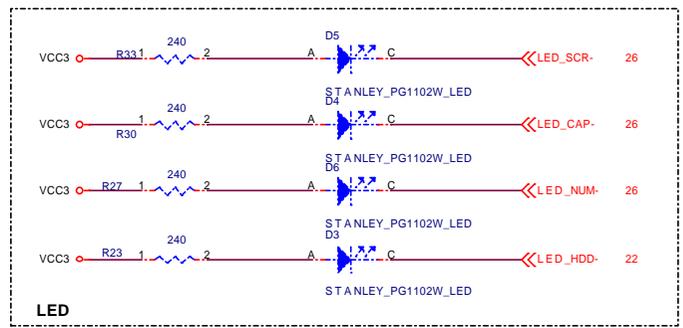


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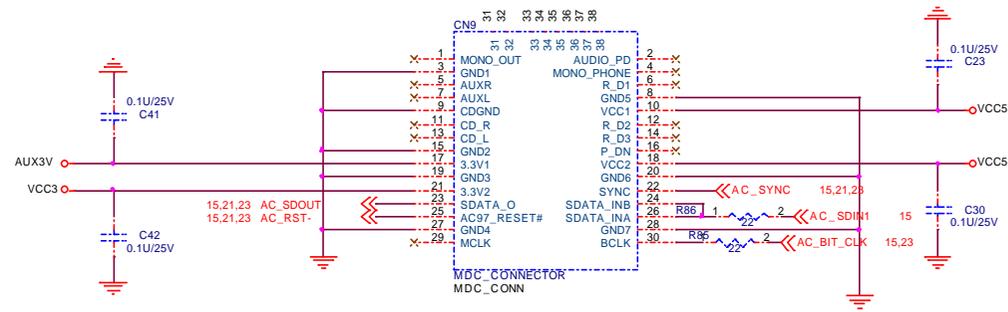
**Note :
Add Capacitor on AUX5V trace**



POWER trace above 80 mils



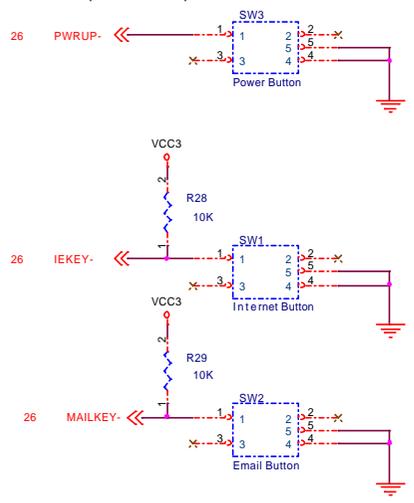
Modem Daughter Card Connector



For two brand MDC, Pin 24 and Pin 26 must be shoot 10/27

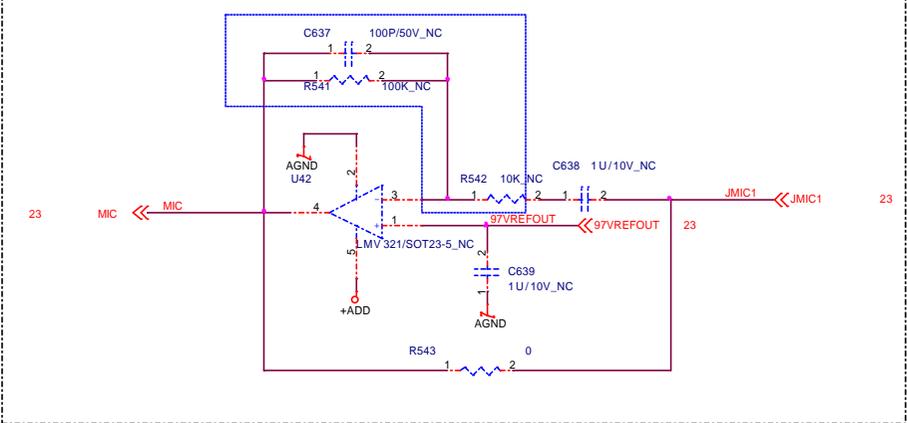
Modify MDC Footprint 12/08

pin 1 & 4 to press

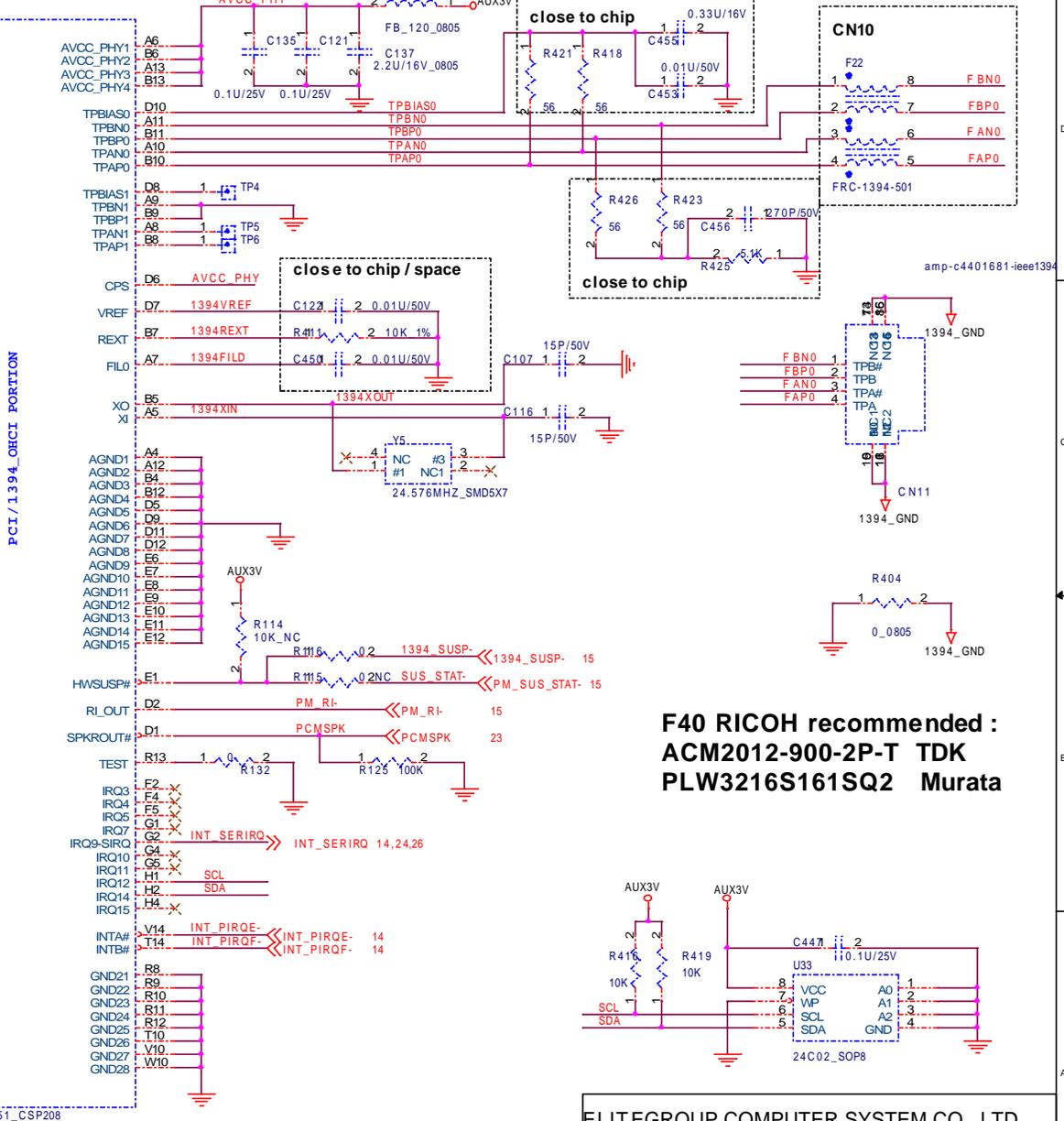
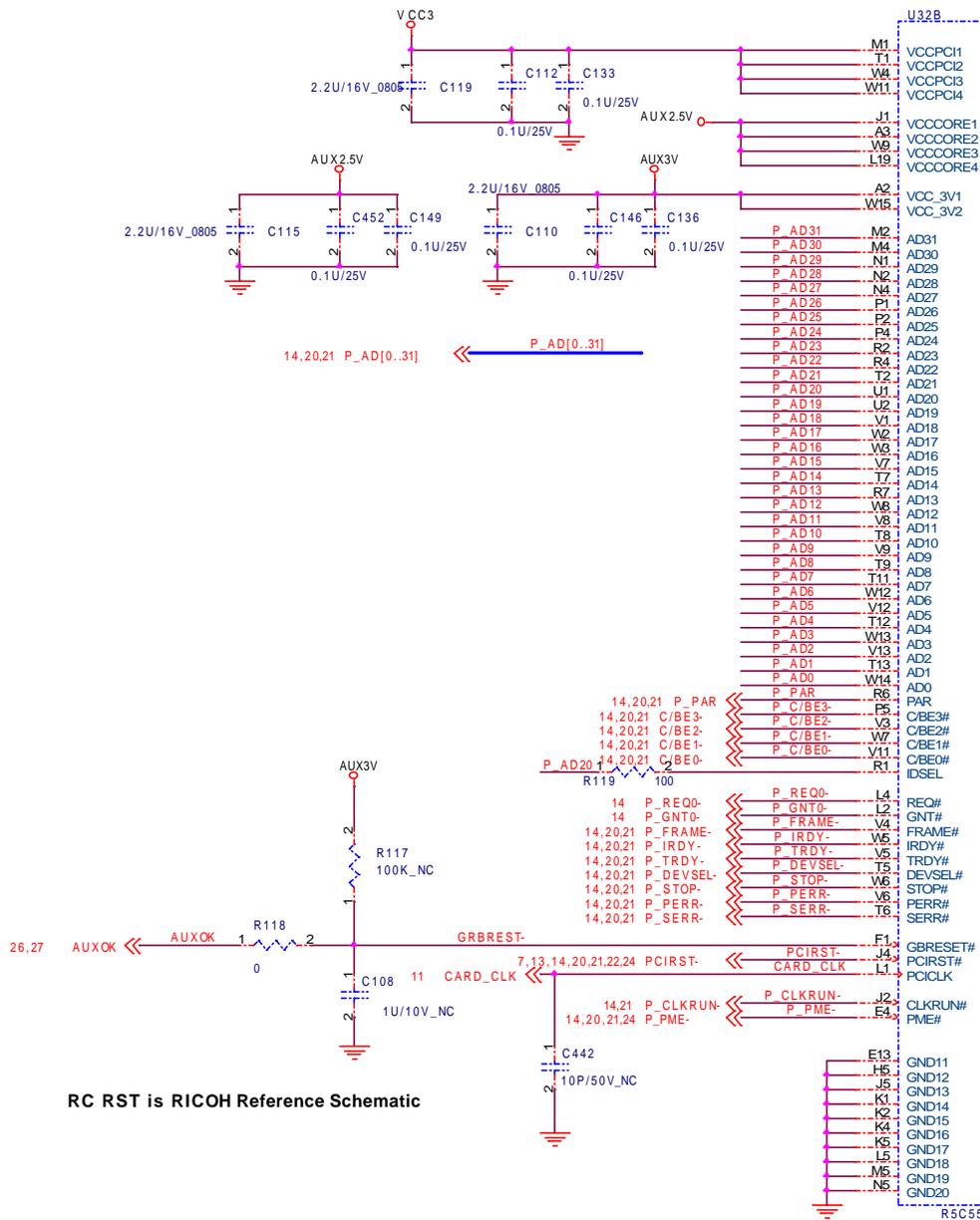


Add MIC Pre-AMP circuit for special customer request 10/27

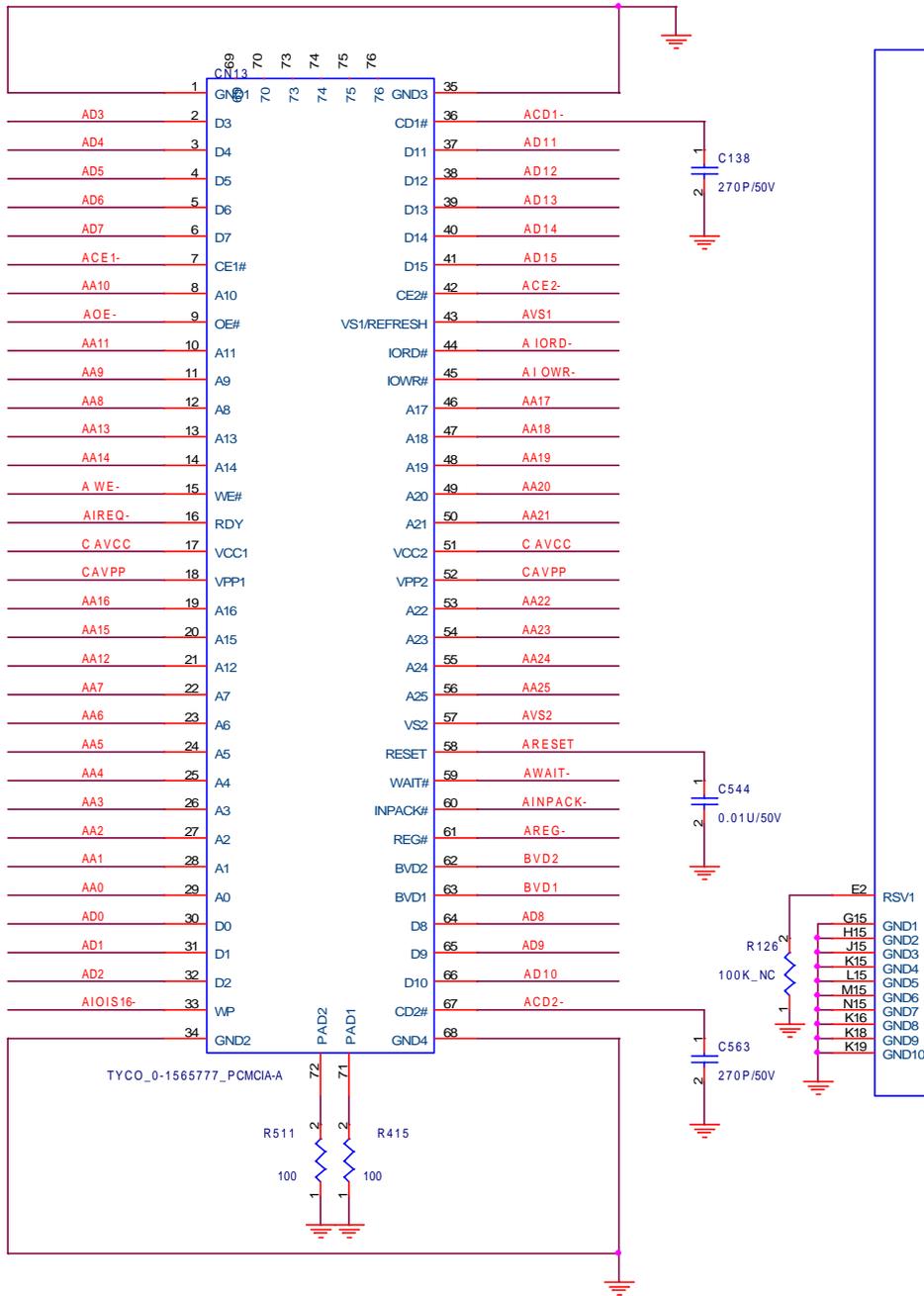
+20dB Gain



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Title G553 1394 OHCI		
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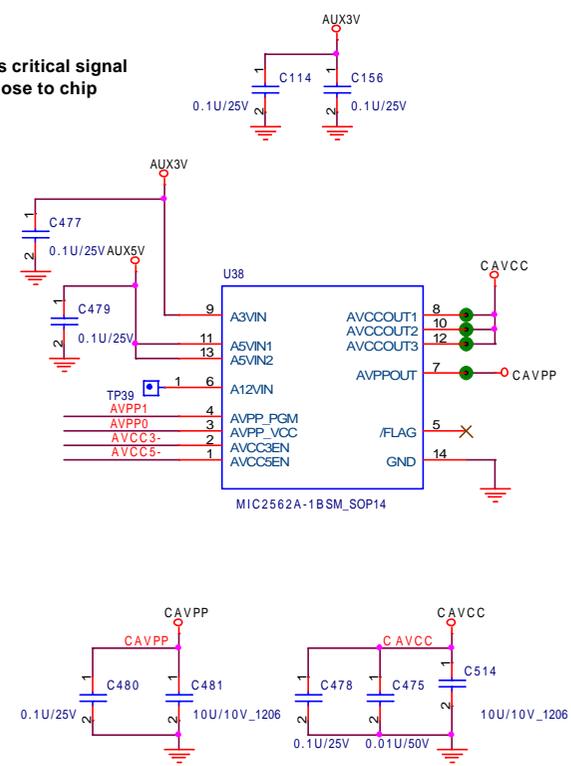


U32A

VCCSLOT1	D19	AA25
VCCSLOT2	N19	AA24
CADR25	F15	AA23
CADR24	F18	AA22
CADR23	G16	AA21
CADR22	H16	AA20
CADR21	H18	AA19
CADR20	J16	AA18
CADR19	J18	AA17
CADR18	M18	AA15
CADR17	G19	AA14
CADR16	G18	AA13
CADR15	L16	AA12
CADR14	M16	AA11
CADR13	F19	AA10
CADR12	P16	AA9
CADR11	R16	AA8
CADR10	N18	AA7
CADR9	M19	AA6
CADR8	F16	AA5
CADR7	E19	AA4
CADR6	E18	AA3
CADR5	D18	AA2
CADR4	C18	AA1
CADR3	A18	AA0
CADR2	B17	AD15
CADR1	B16	AD14
CDATA15	R18	AD13
CDATA14	T18	AD12
CDATA13	U18	AD11
CDATA12	V19	AD10
CDATA11	V17	AD9
CDATA10	E14	AD8
CDATA9	B14	AD7
CDATA8	D15	AD6
CDATA7	T19	AD5
CDATA6	U19	AD4
CDATA5	W17	AD3
CDATA4	V16	AD2
CDATA3	D14	AD1
CDATA2	A14	AD0
CDATA1	B15	A IORD-
CDATA0	P15	A IOWR-
IORD#	N16	A OE-
IOWR#	P18	A WE-
OE#	J18	ACE2-
WE#	P19	ACE1-
CE2#	R19	AREG-
CE1#	A17	ARESET
REG#	E16	AWAIT-
RESET	C19	AIOIS16-
WAIT#	D13	AIREQ-
VP	H19	BVD2
RDY	H19	BVD1
BVD2	A16	AVS2
BVD1	A15	AVS1
VS2	T15	ACD2-
VS1	V15	ACD1-
CD2#	W16	AINPACK-
CD1#	R14	AVPP1
INPACK#	B19	AVPP0
GND6	C2	AVCC3-
GND7	C1	AVCC5-
GND8	B1	
GND9	B3	
GND10		

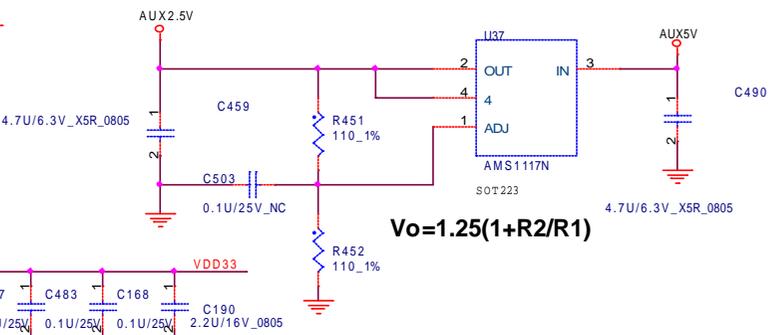
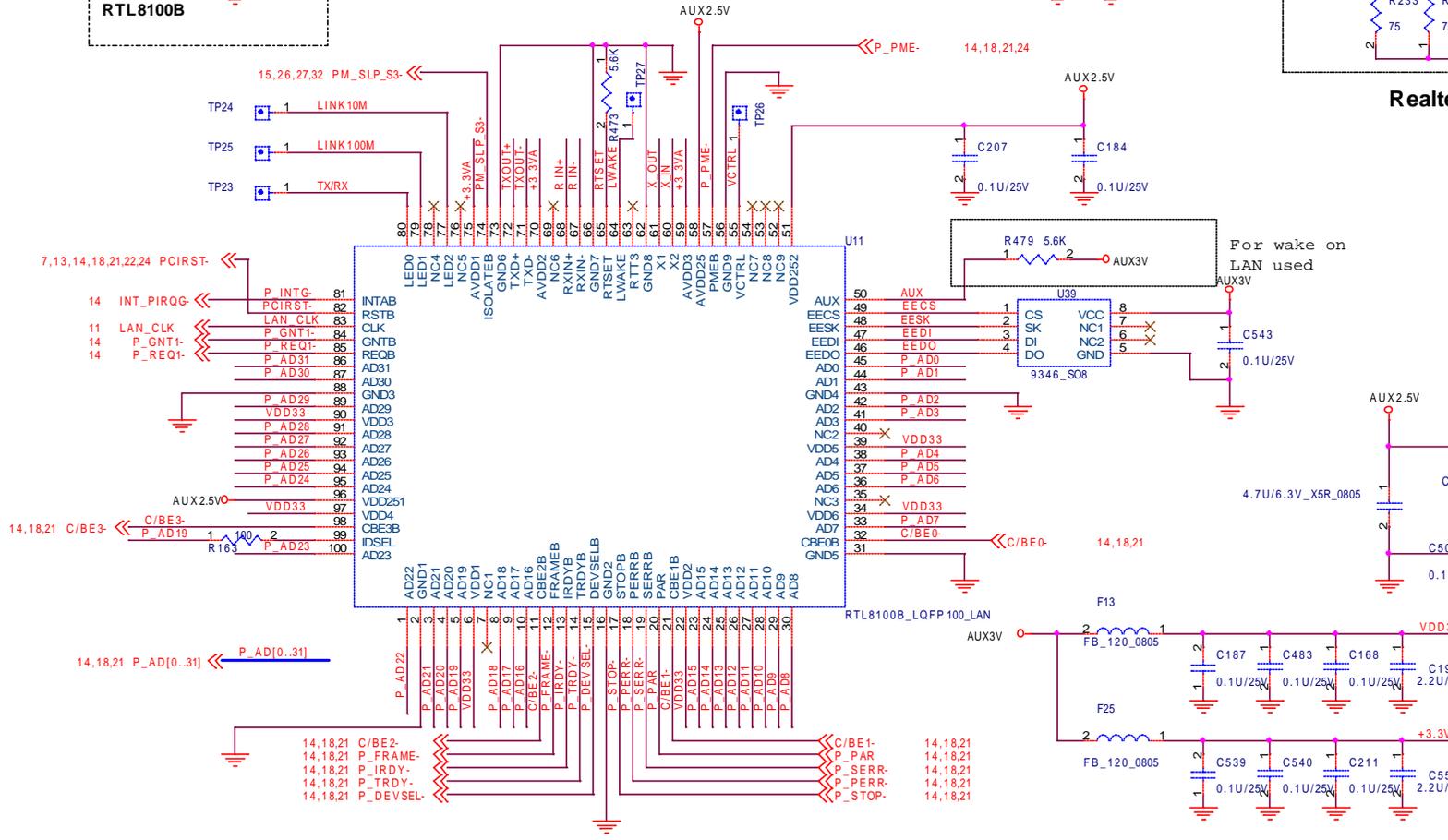
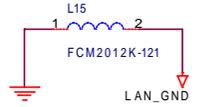
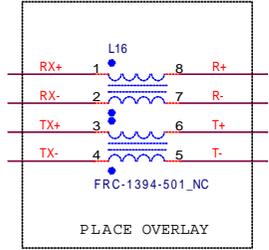
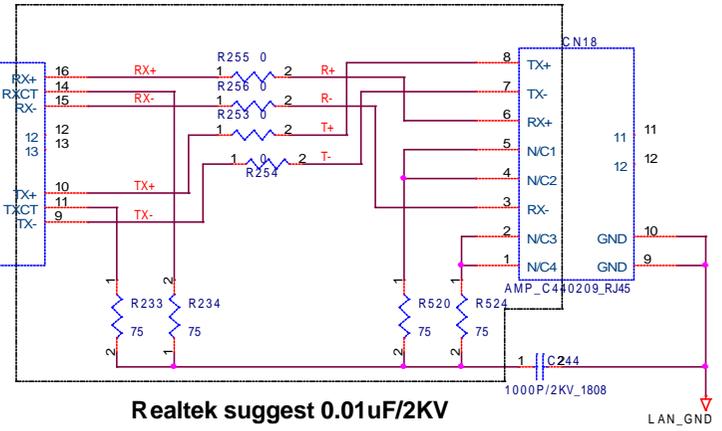
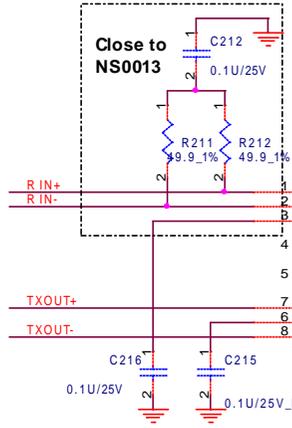
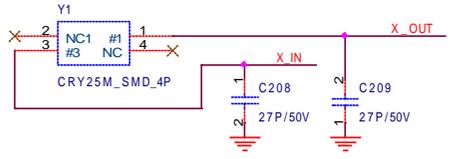
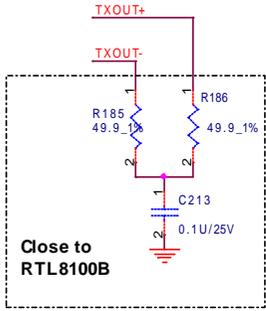
CARDBUS PORTION

AA16 is critical signal
R400 close to chip



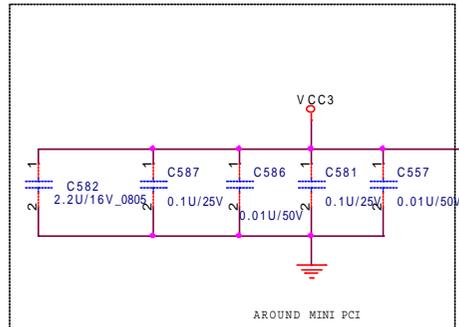
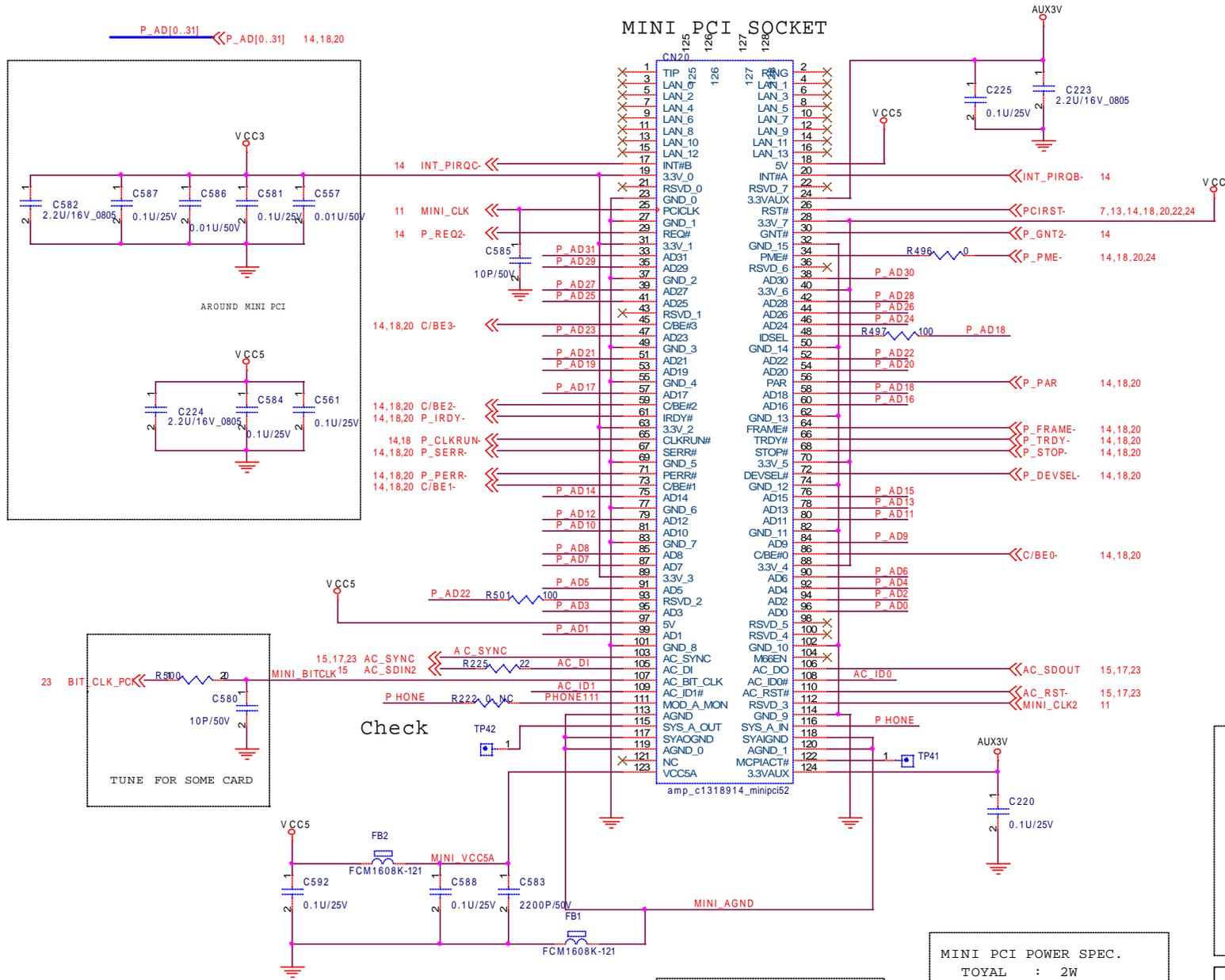
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Title G553 CARDBUS		
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- Layout Rule :
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 2. Through hole less .
 3. Tx+/- -----
GND -----
Rx+/- -----
Ground separates Tx/Rx .

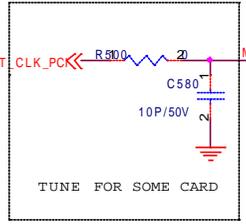
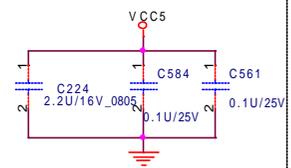


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Size	Document Number	Rev
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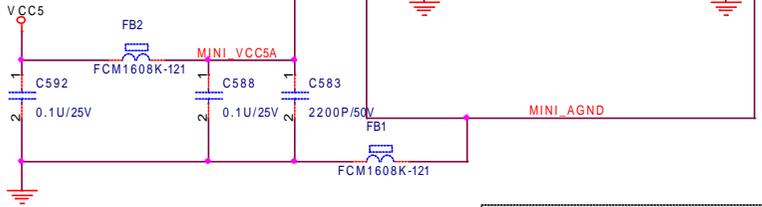
MINI PCI SOCKET



AROUND MINI PCI



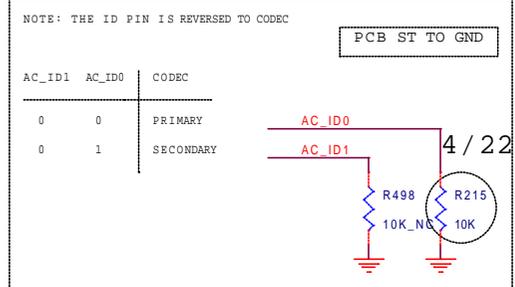
TUNE FOR SOME CARD

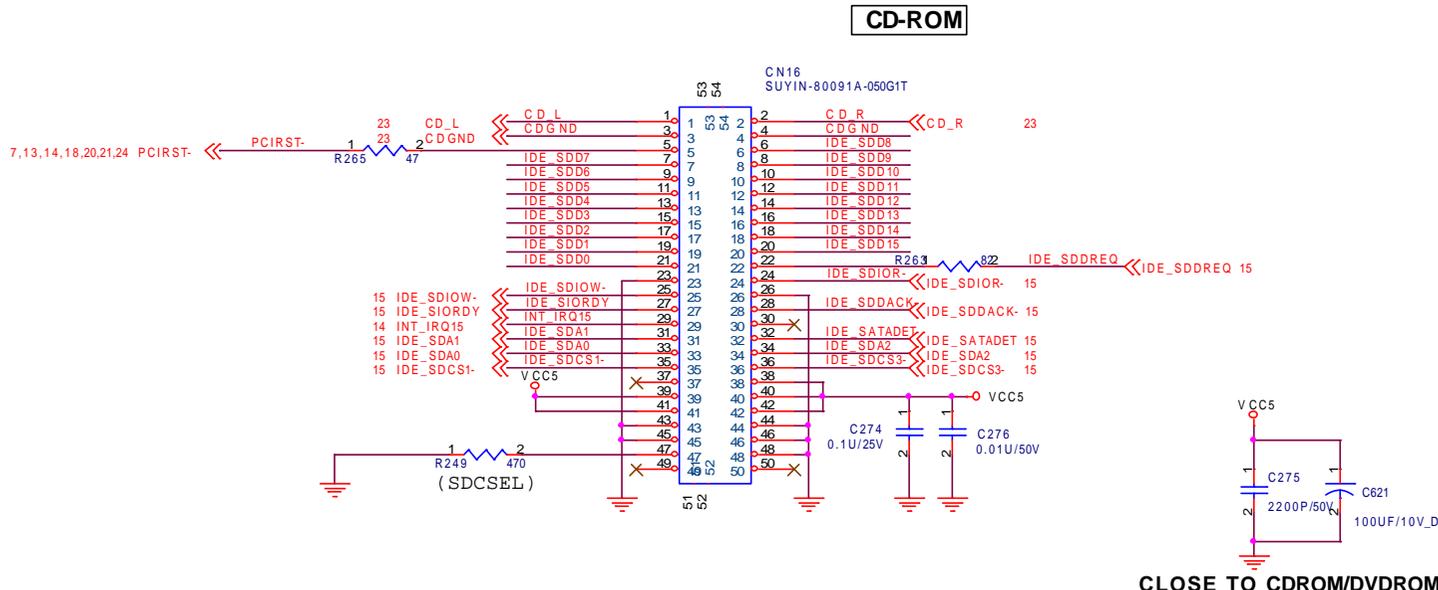
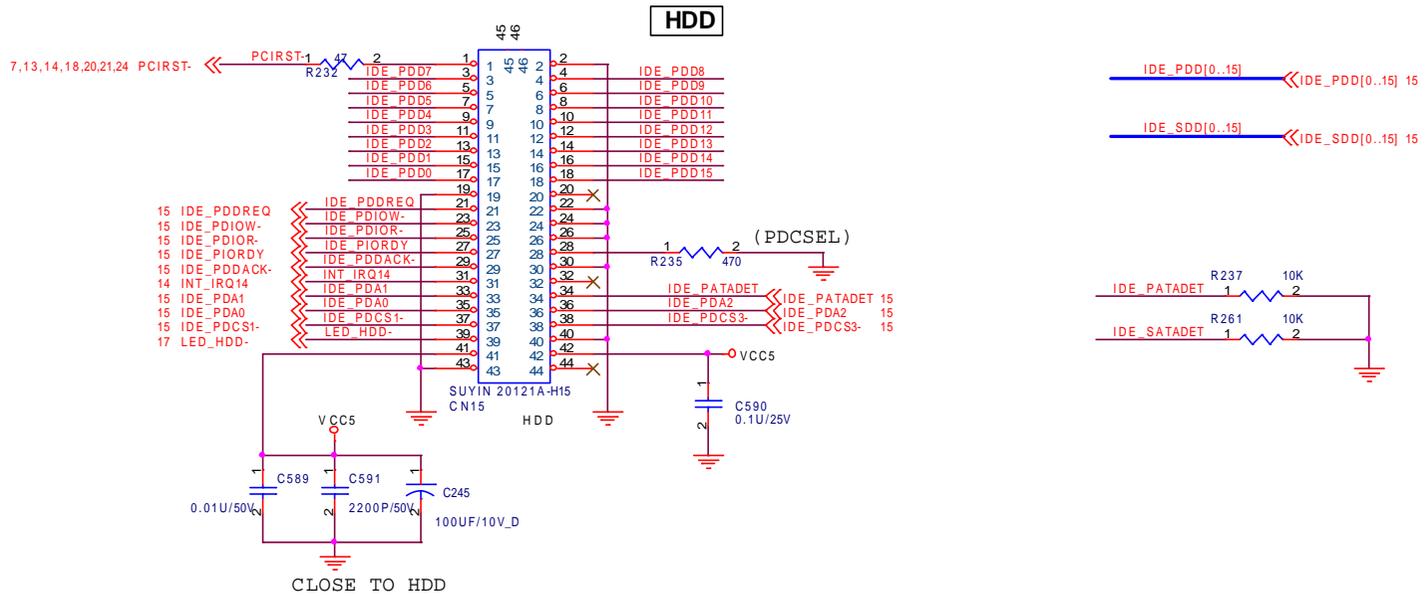


FOR G551 :
PHONE AND MONO_OUT NOT USE

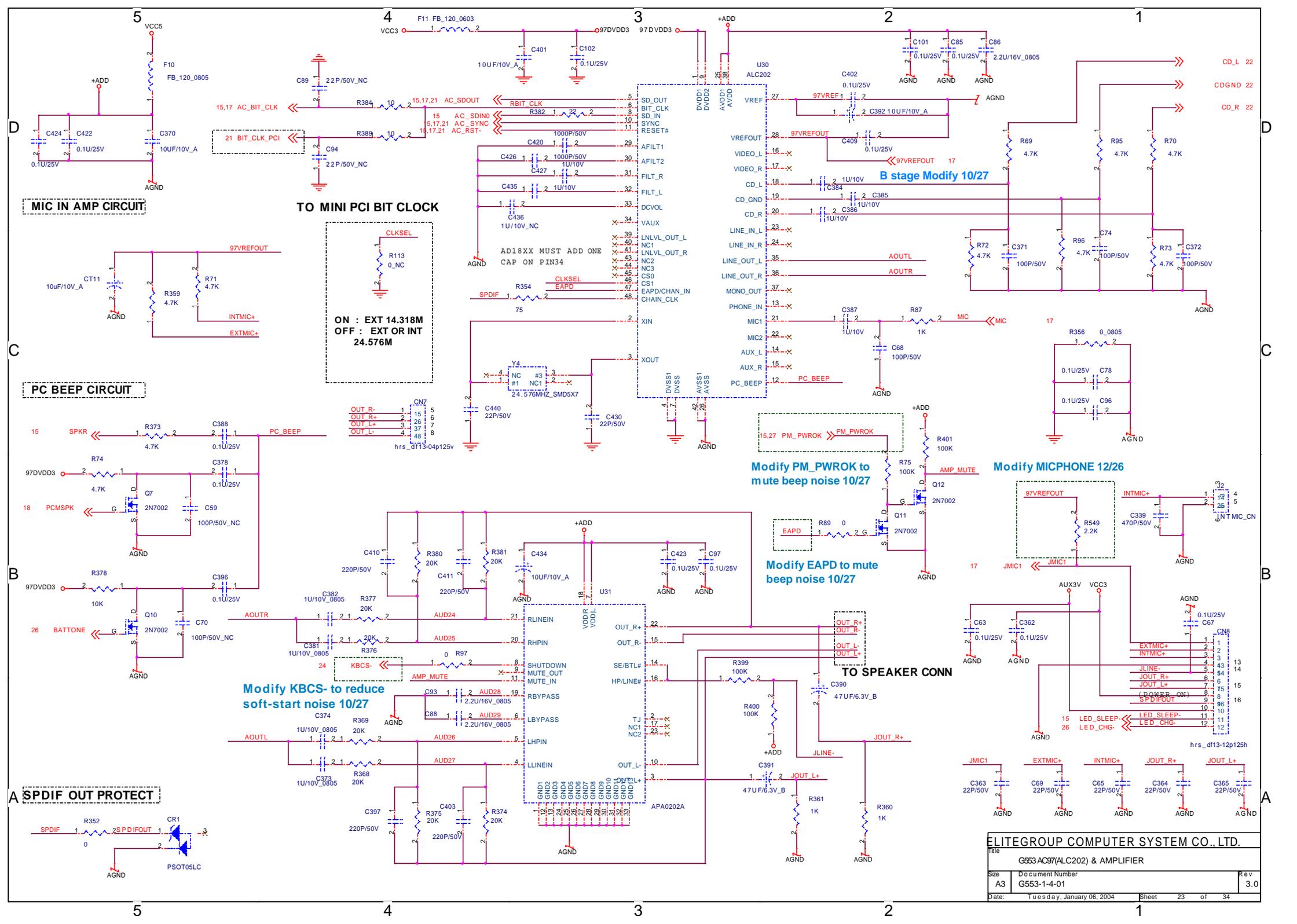
MINI PCI POWER SPEC.

TOYAL	: 2W
+5V	: 100mA
3.3VAUX	: 5/200/375mA
VCC5A	: 100mA
+3V	

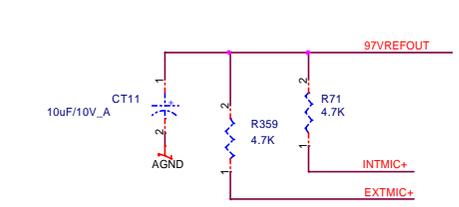




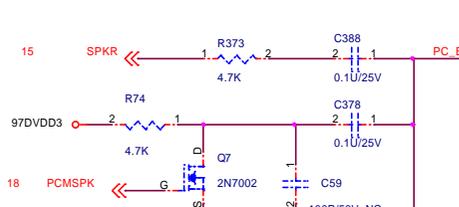
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Title: G553HDD & CDROM CONN.		
Size: B	Document Number: G553-1-4-01	Rev: 3.0
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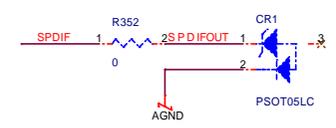
MIC IN AMP CIRCUIT



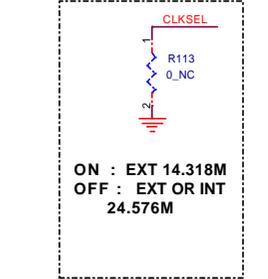
PC BEEP CIRCUIT



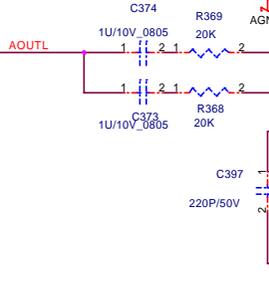
SPDIF OUT PROTECT



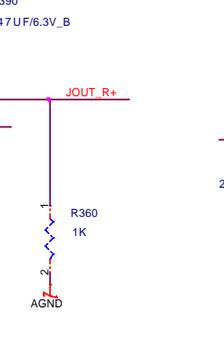
TO MINI PCI BIT CLOCK

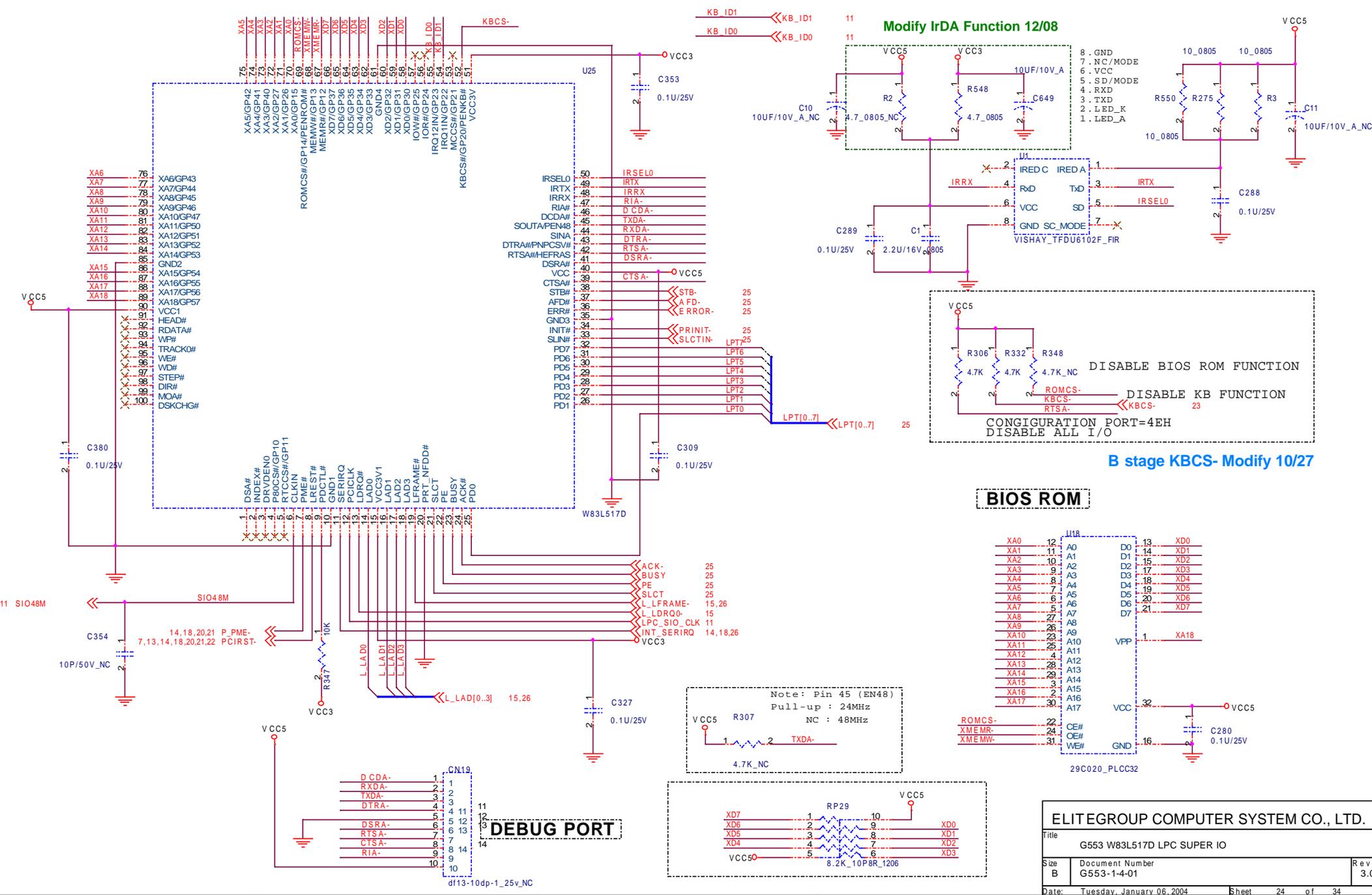


Modify KBCS- to reduce soft-start noise 10/27

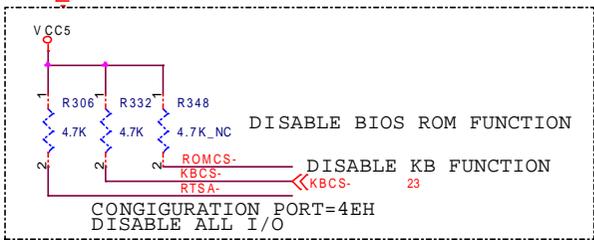
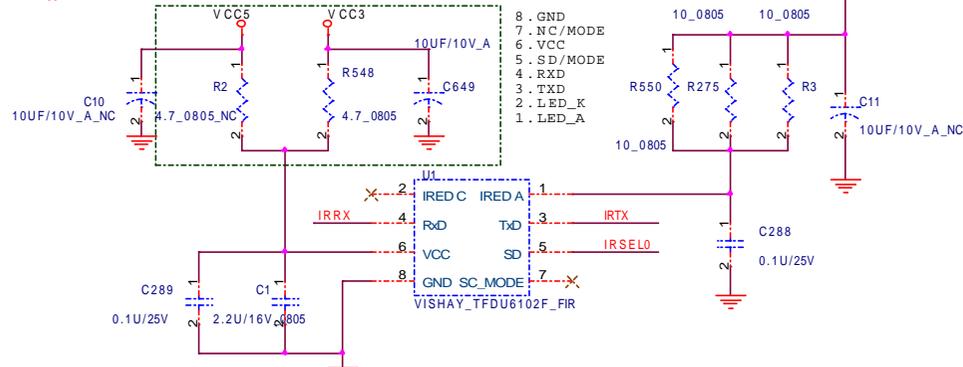


TO SPEAKER CONN



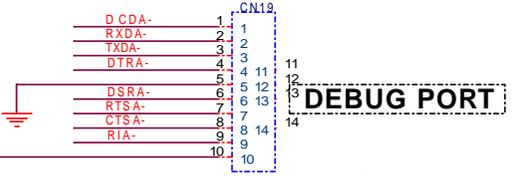
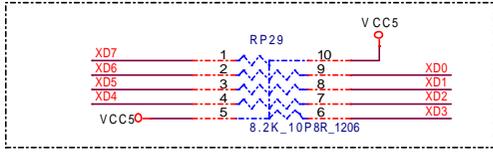
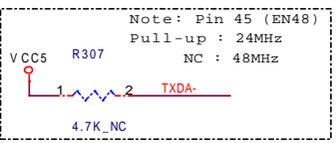
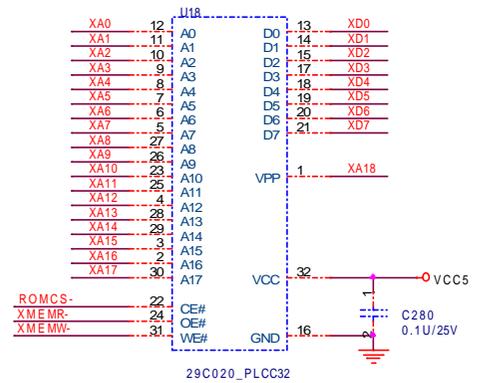


Modify IrDA Function 12/08



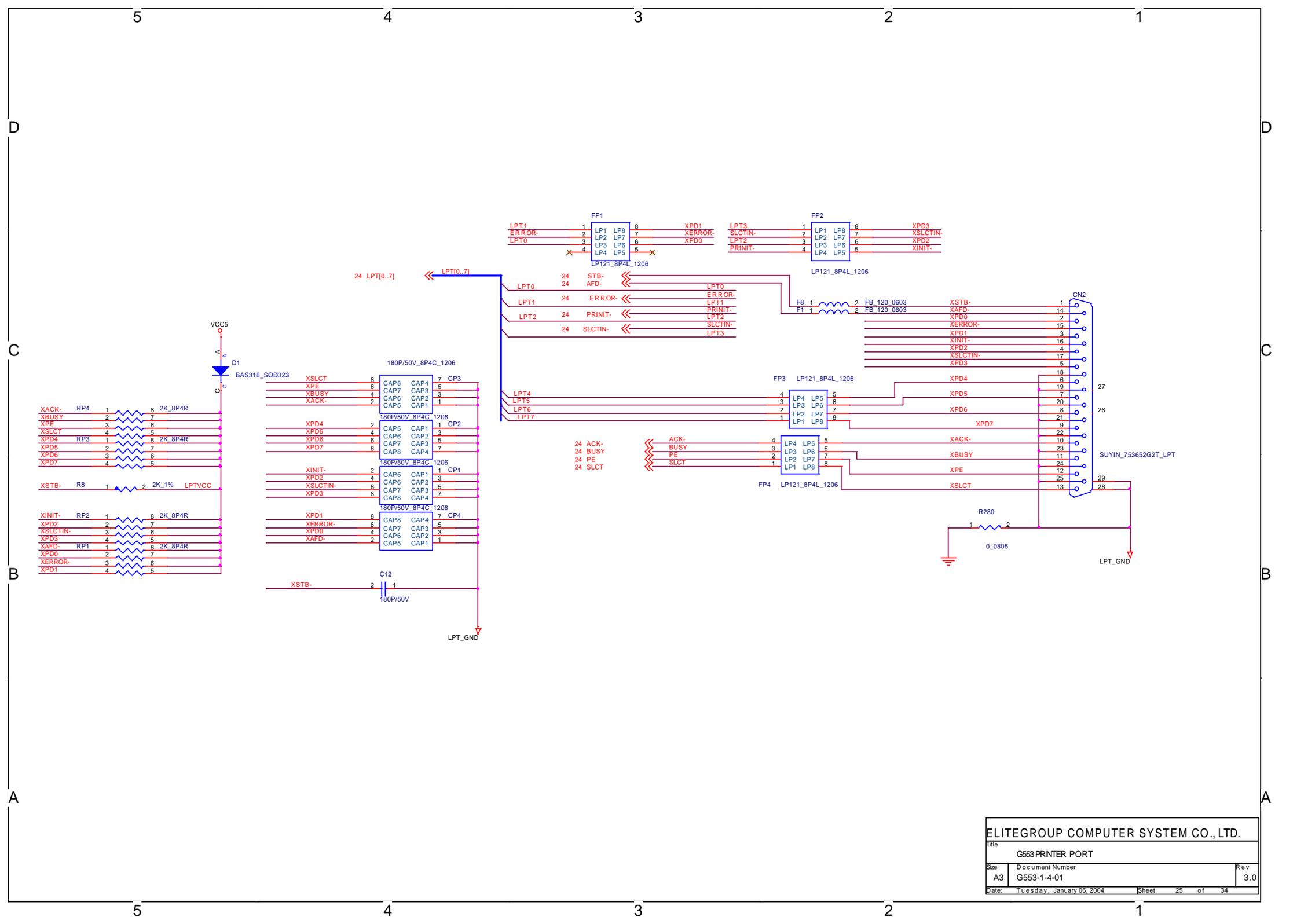
B stage KBCS- Modify 10/27

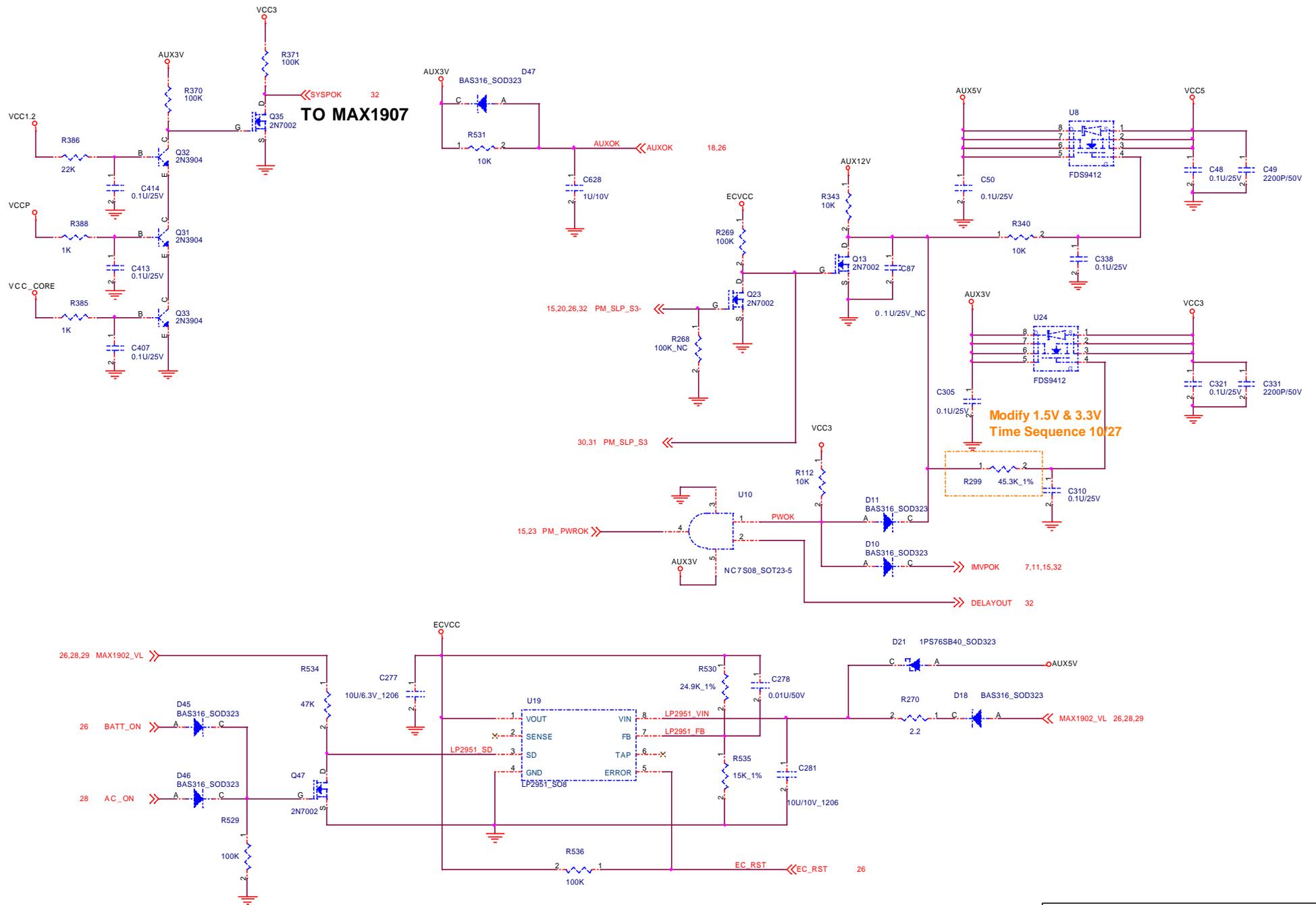
BIOS ROM



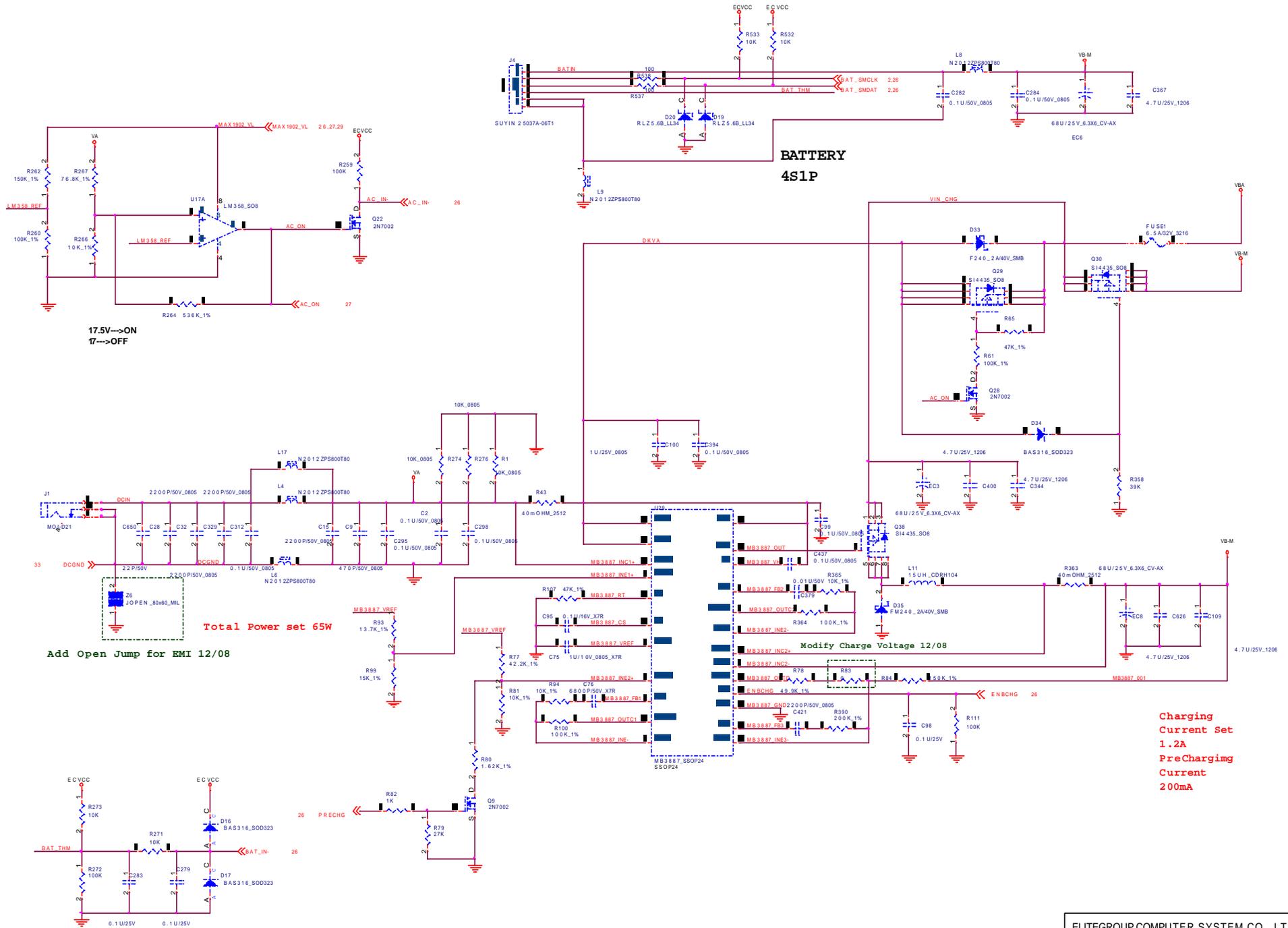
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Title	G553 W83L517D LPC SUPER IO	
Size	Document Number	Rev
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17.5V-->ON
17-->OFF

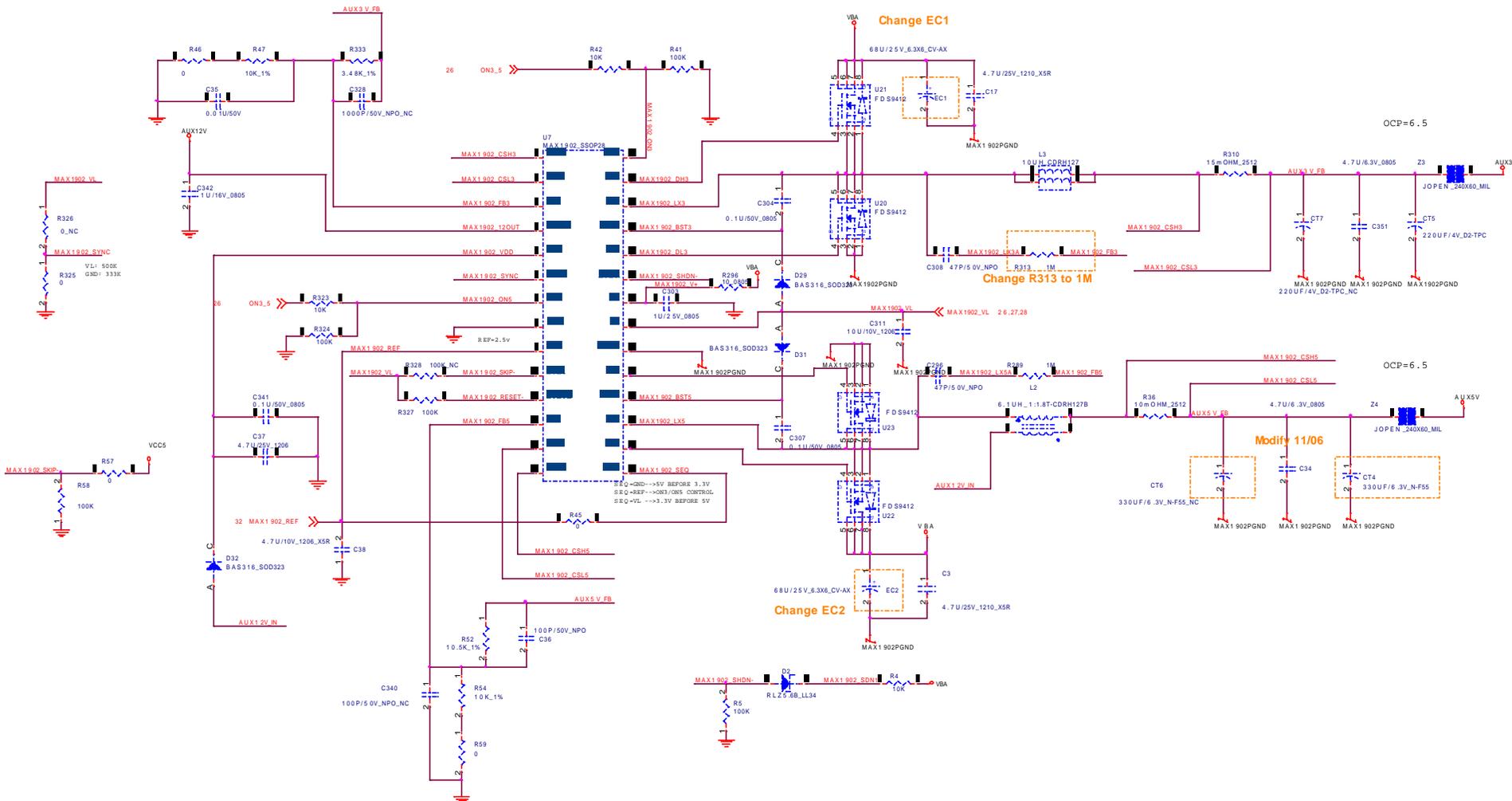
Total Power set 65W

Add Open Jump for EMI 12/08

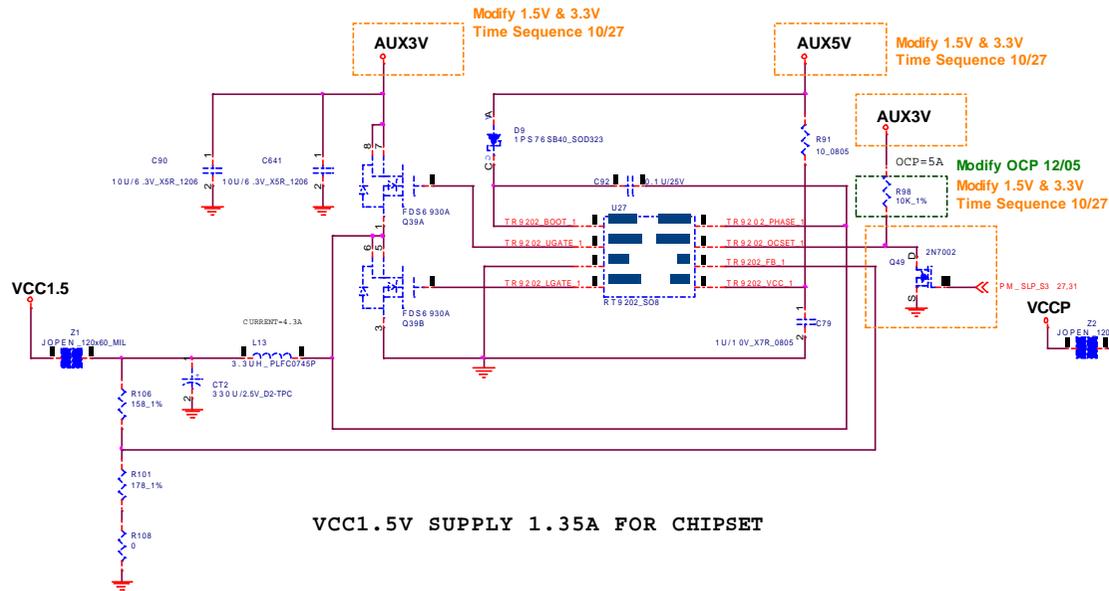
Modify Charge Voltage 12/08

Charging Current Set 1.2A
PreCharging Current 200mA

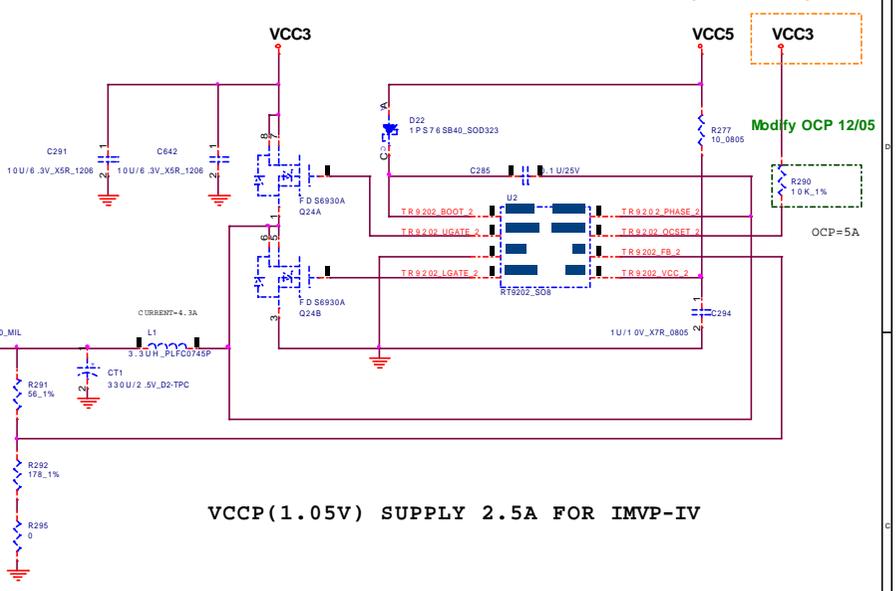
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Size	Document Number	Rev
C	G553-1-401	3.0
Date	Tue 05 Jan 06 2004	Sheet 28 of 34



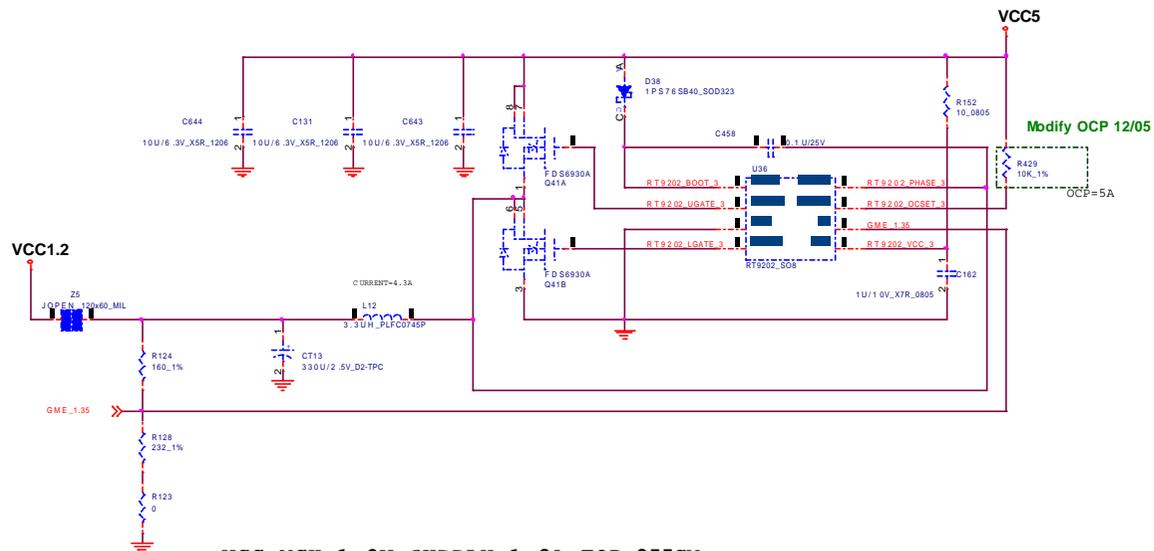
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Size	DocumentNumber	Rev
C	<Doc>	30
Date	Tue 5/5/2004	Sheet 29 of 34



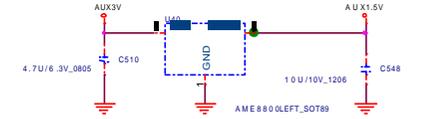
VCC1.5V SUPPLY 1.35A FOR CHIPSET



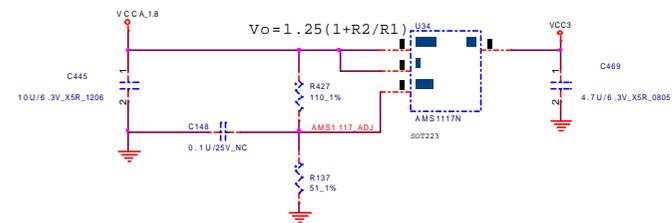
VCCP(1.05V) SUPPLY 2.5A FOR IMVP-IV



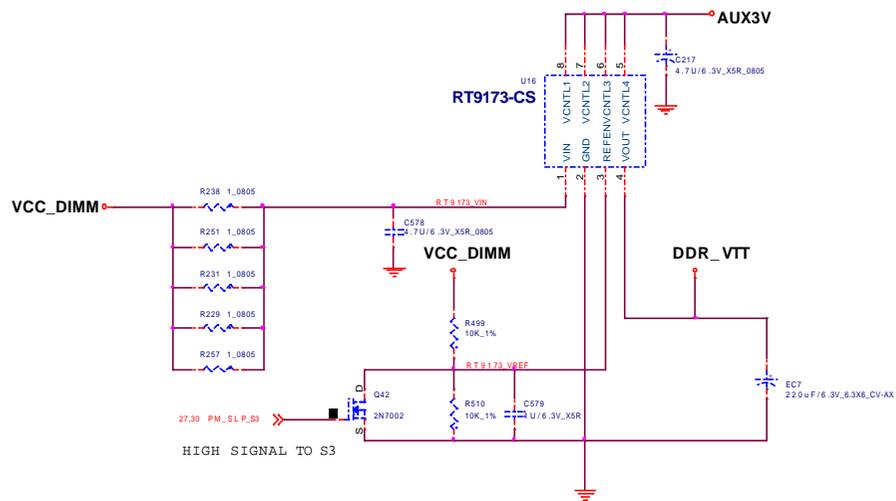
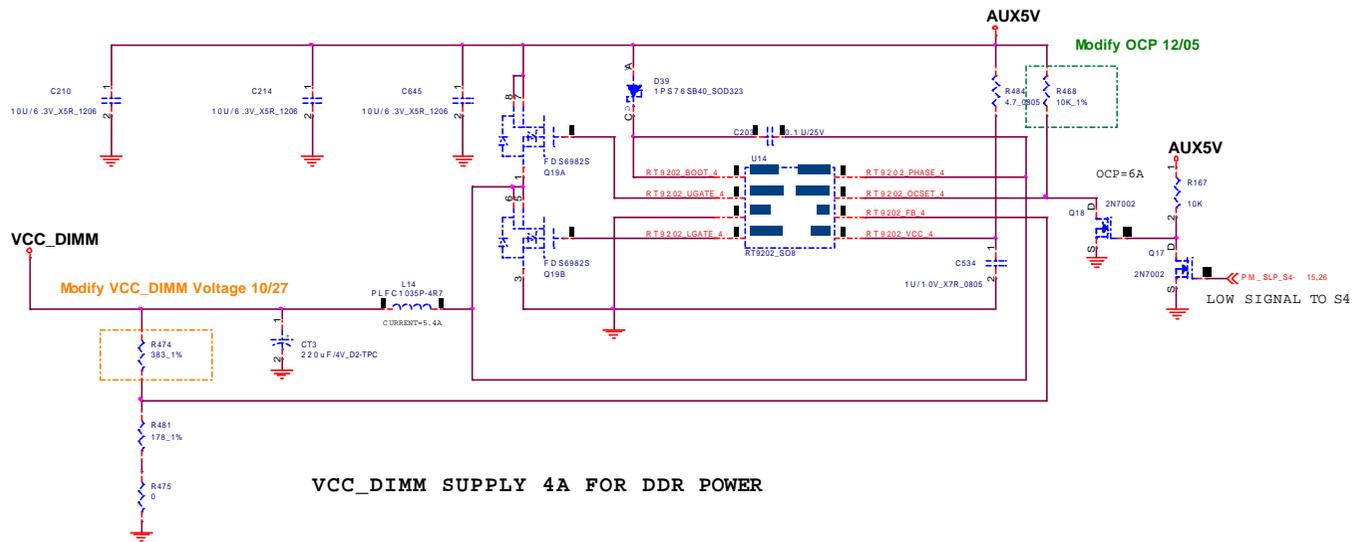
VCC_MCH 1.2V SUPPLY 1.8A FOR 855GM
VCC_MCH 1.35V SUPPLY 1.8A FOR 855GME



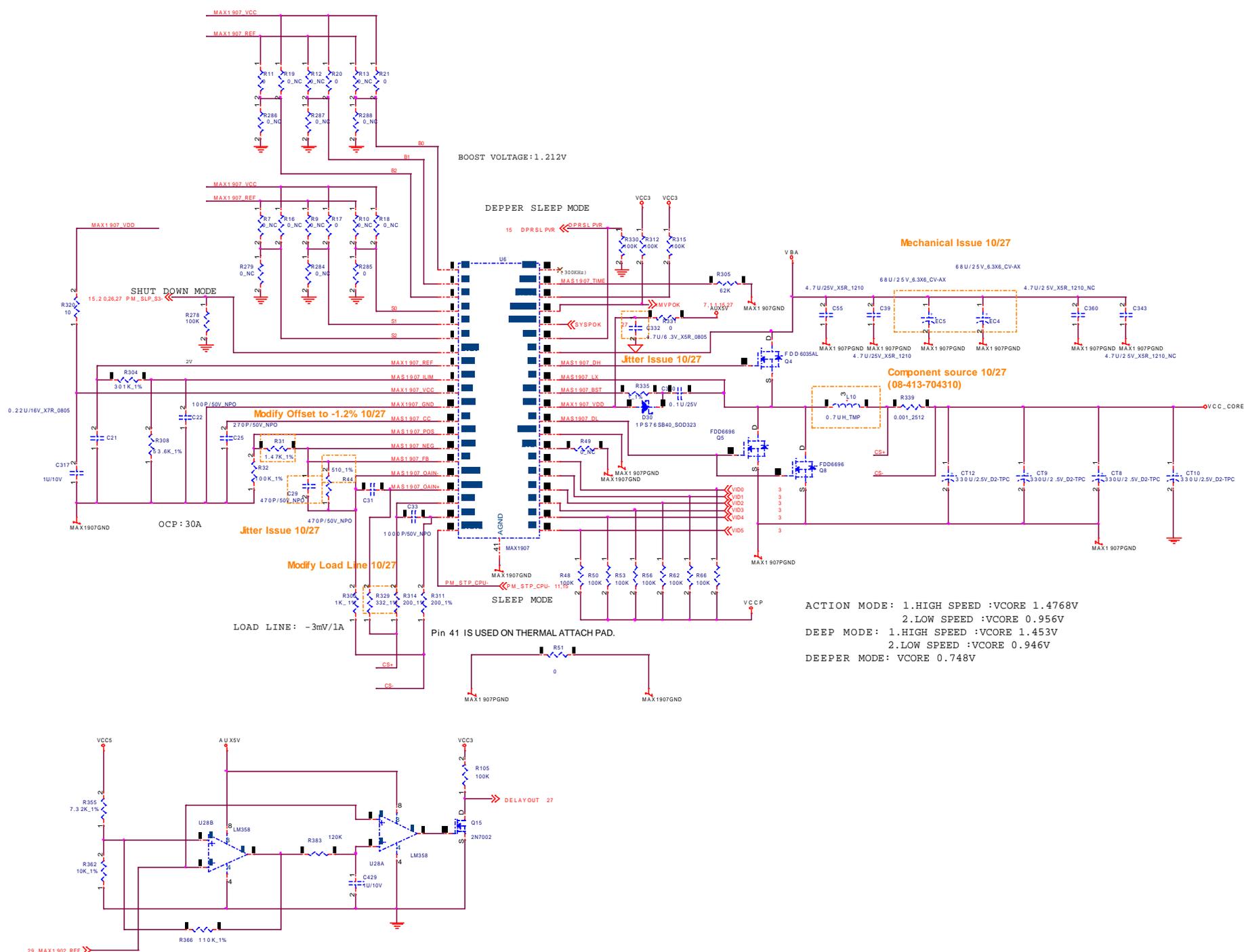
AUX1.5V SUPPLY 100mA for ICH4-M

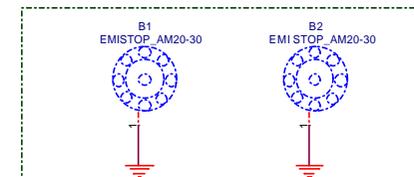
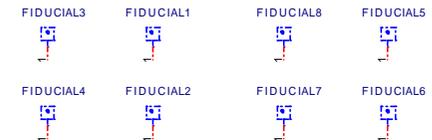
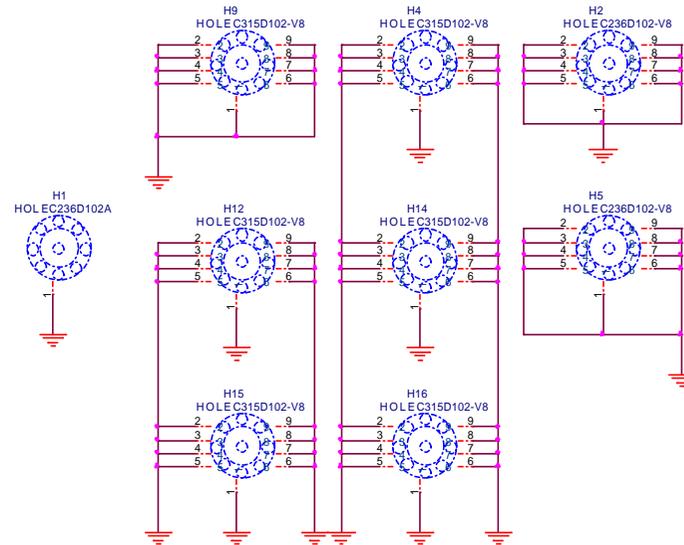
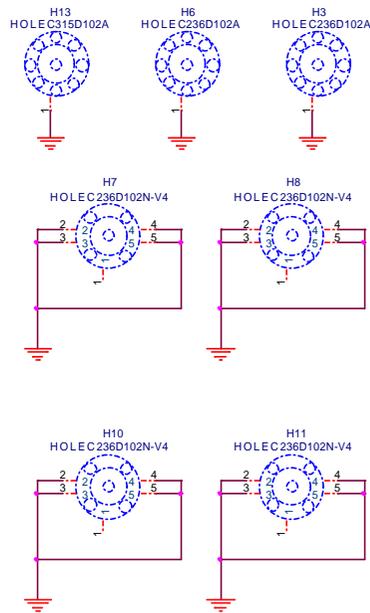


VCCA(1.8V) SUPPLY 600mA FOR Banias (PLL VOLTAGE)
VCCA(1.5V & 1.8V) SUPPLY FOR DOTHAN (PLL VOLTAGE)



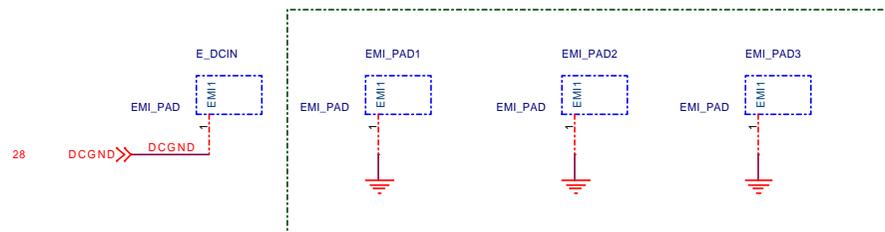
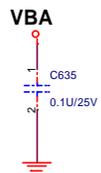
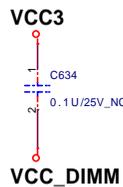
ELITEGROUP COMPUTER SYSTEM CO., LTD.		
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Add MDC Hold 12/08

EMI SOLUTION



Modify EMI Spring 12/05

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0.0 : Initial Release. 2003/9/18 15-F44-010010
1.0 : Initial Release. 2003/11/05 15-F44-011000
2.0 : By pass for waiting EMI solution
3.0 : Initial Release. 2003/12/31 15-F44-013000

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Title	G553 HISTORY	
Size	Document Number	Rev
A3	G553-1-4-01	3.0
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