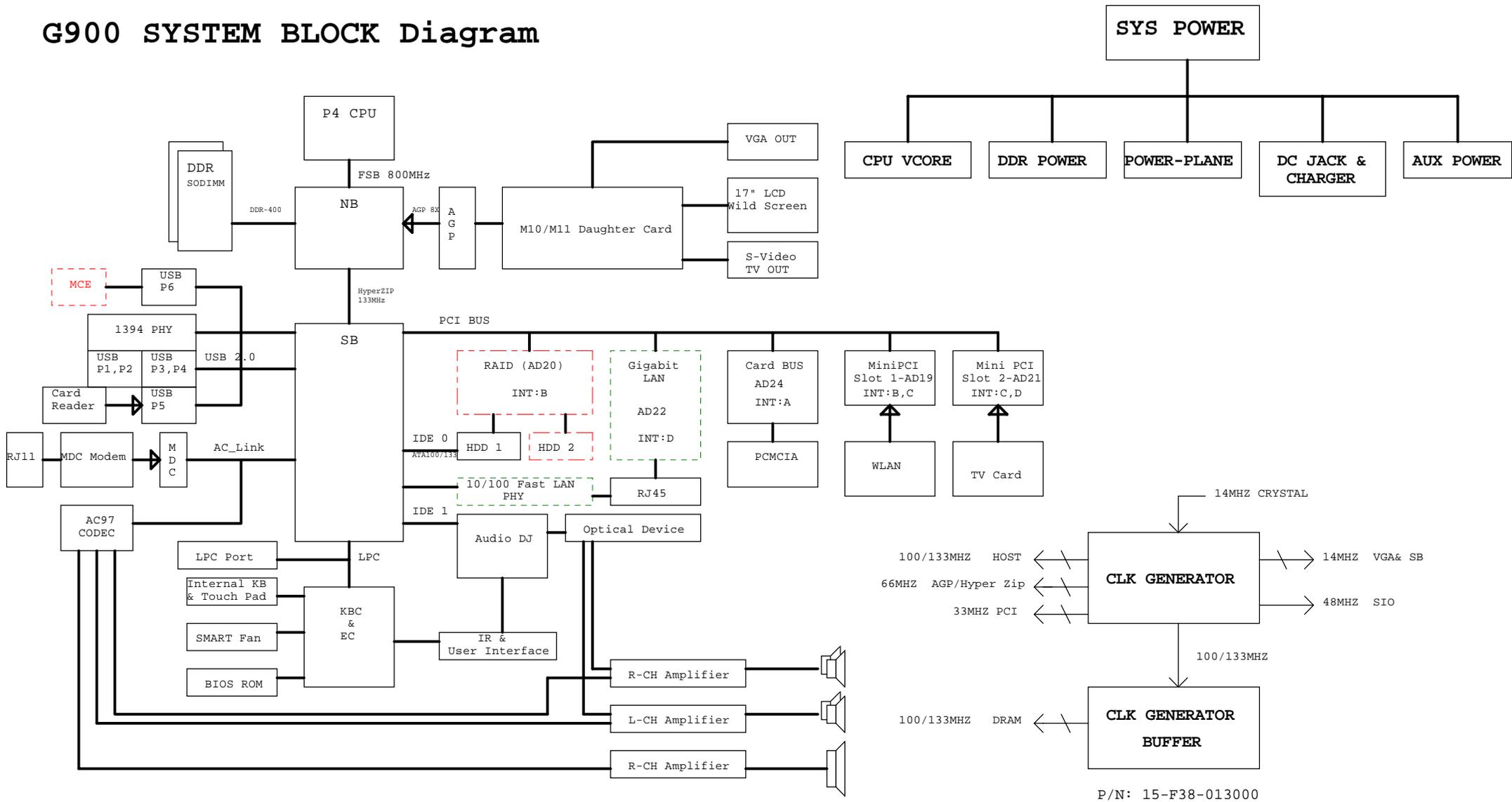


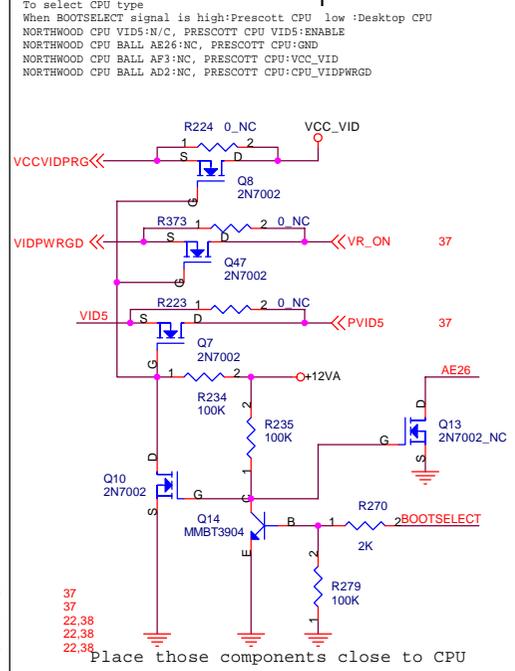
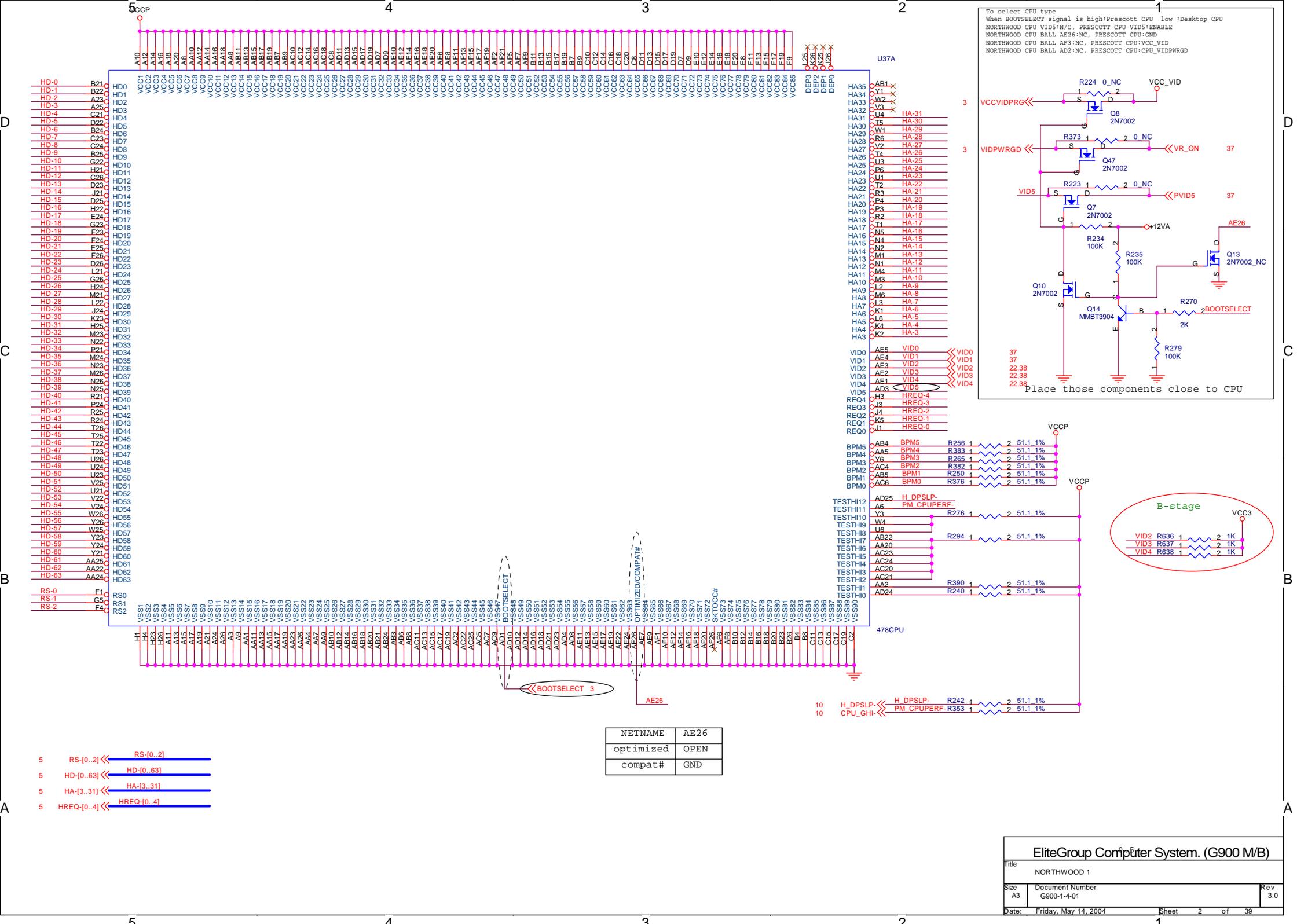
# G900 SYSTEM BLOCK Diagram



PAGE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
REV	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
DATE	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04

PAGE	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39
REV	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
DATE	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04	04/14/04

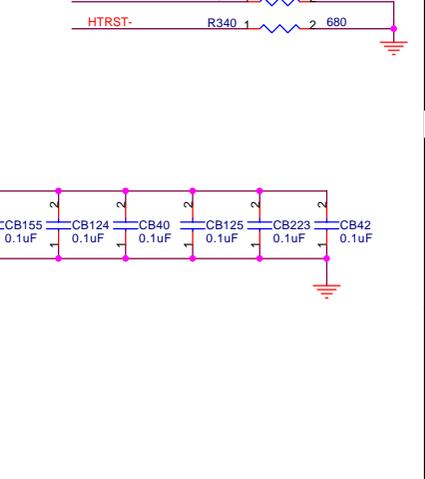
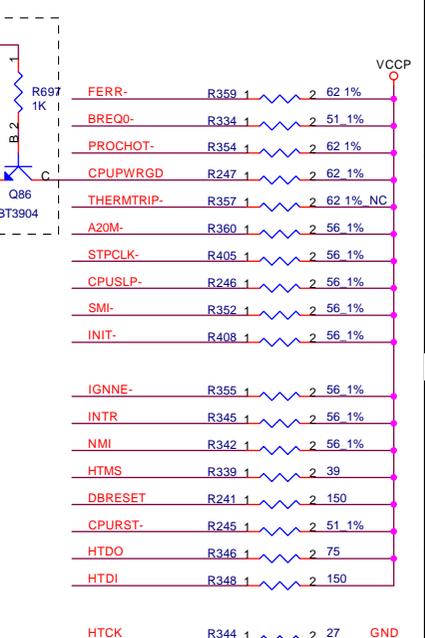
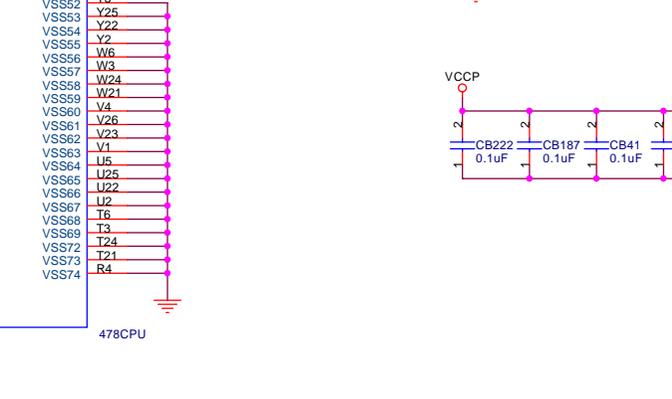
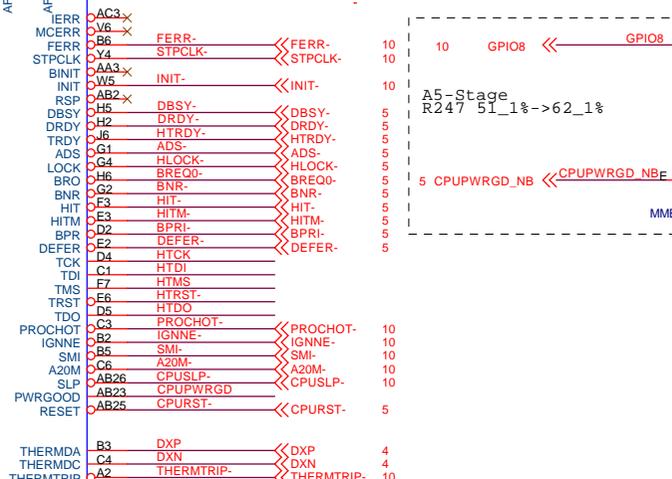
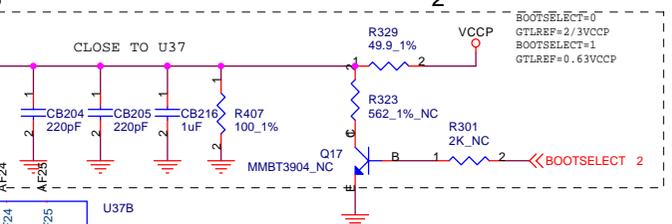
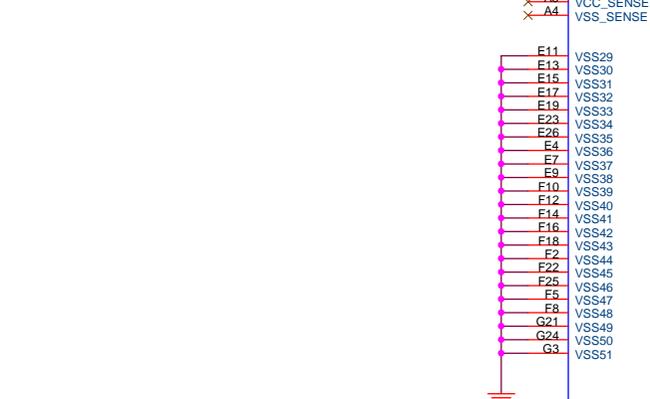
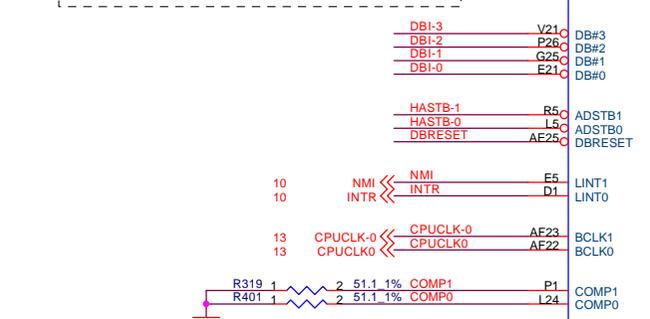
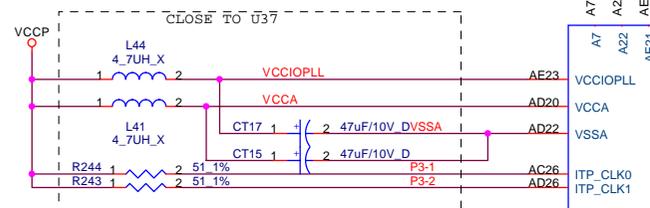
Approved by	Checked by	Designed by
EliteGroup Computer System. (G900 MB)		
P. Leader	Title	G900 BLOCK DIAGRAM
	Size	Custom
	Document Number	G900-1-4-01
	Date:	Friday, May 14, 2004
	Sheet	1 of 39
	Rev	3.0



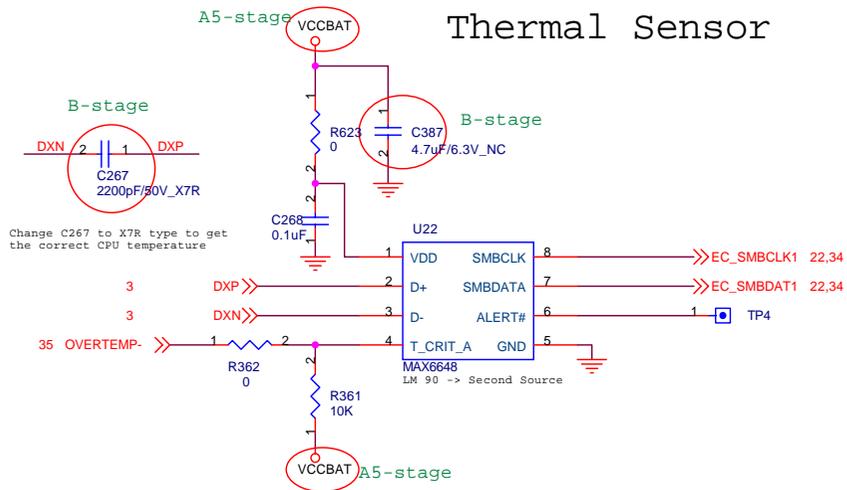
NETNAME	AE26
optimized	OPEN
compat#	GND

- 5 RS-[0..2] << RS-[0..2]
- 5 HD-[0..63] << HD-[0..63]
- 5 HA-[3..31] << HA-[3..31]
- 5 HREQ-[0..4] << HREQ-[0..4]

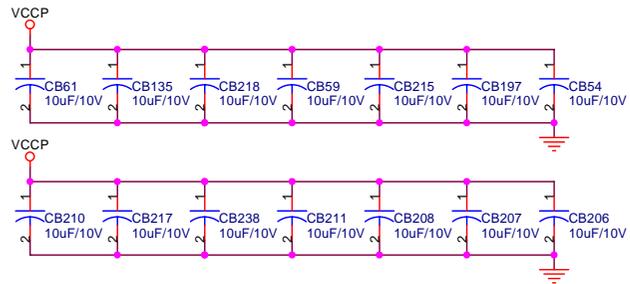
5 HDSTBP-[0..3] << HDSTBP-[0..3]  
 5 HDSTBN-[0..3] << HDSTBN-[0..3]  
 5 DBI-[0..3] << DBI-[0..3]  
 5 HASTB-[0..1] << HASTB-[0..1]



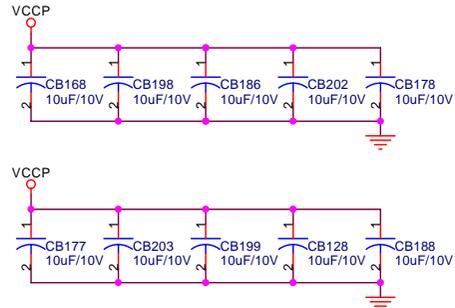
# Thermal Sensor



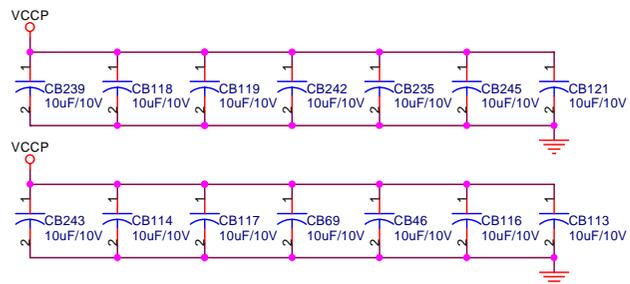
Put these capacitors at processor NORTH SIDE



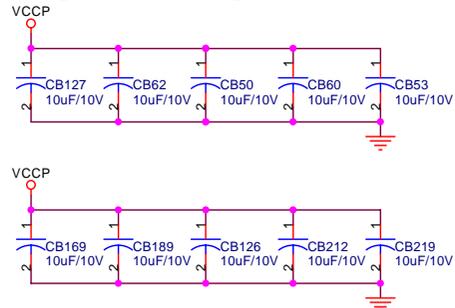
Put these capacitors INSIDE PROCESSOR CAVITY



Put these capacitors at processor SOUTH SIDE



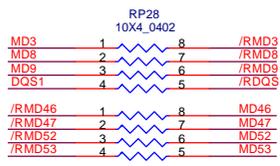
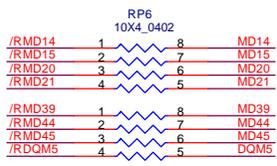
Put these capacitors at processor SOLDER SIDE



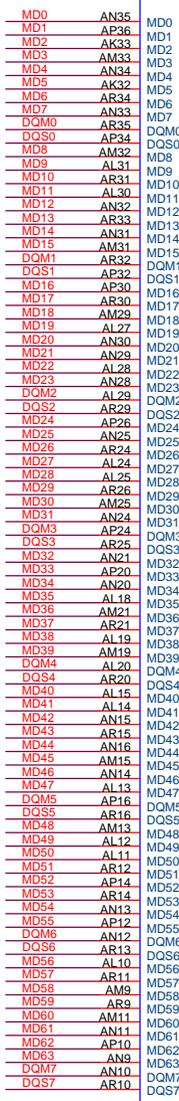
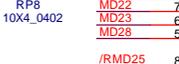
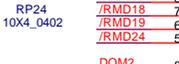
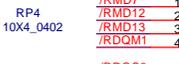
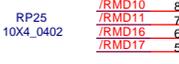
EliteGroup Computer System. (G900 M/B)

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NORTHWOOD 3		
Size	Document Number	Rev
B	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 4 of 39

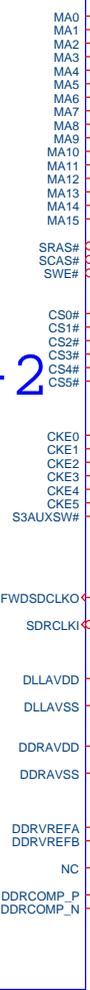




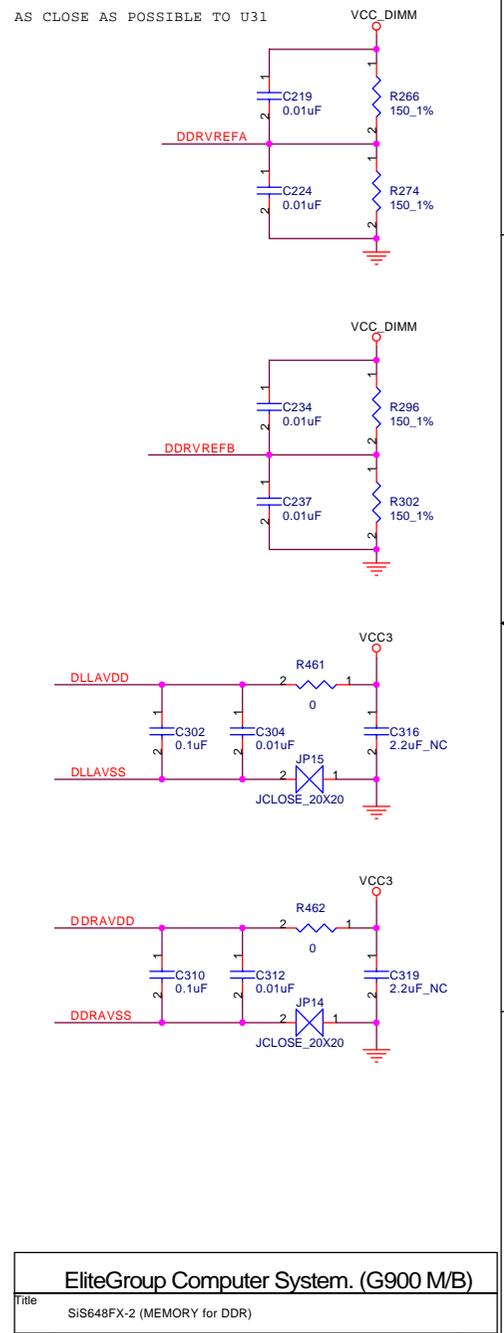
Resistors place close to DIMM1



648FX-2



Es place close to DIMM1

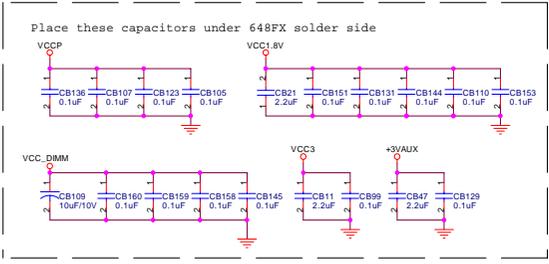
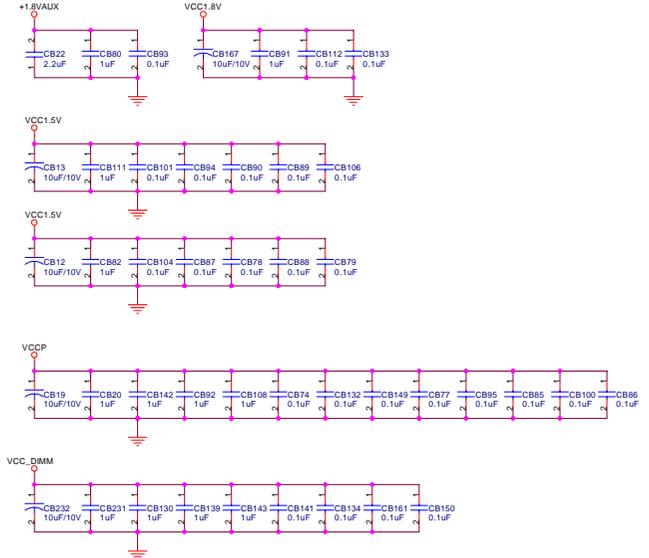
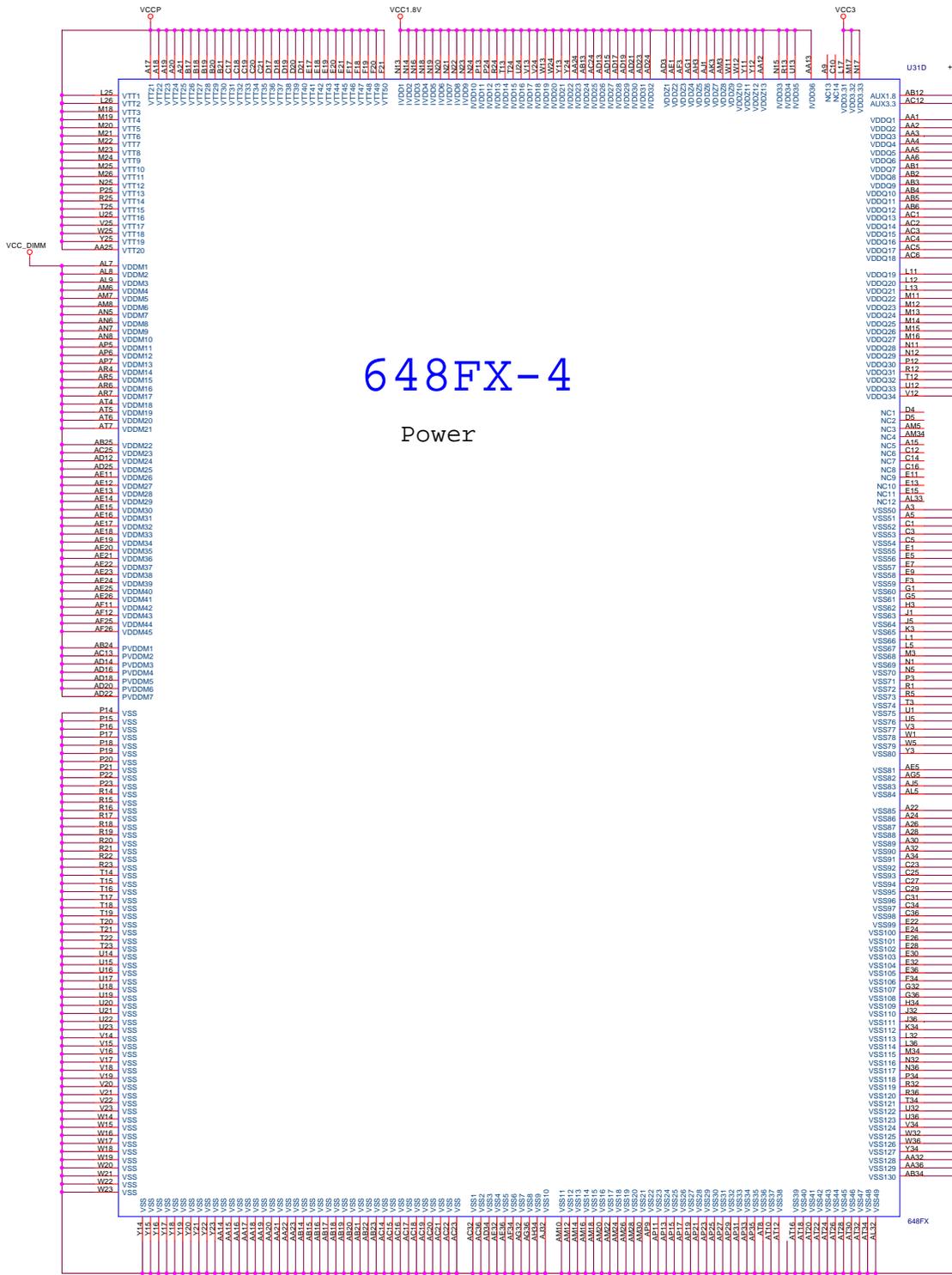


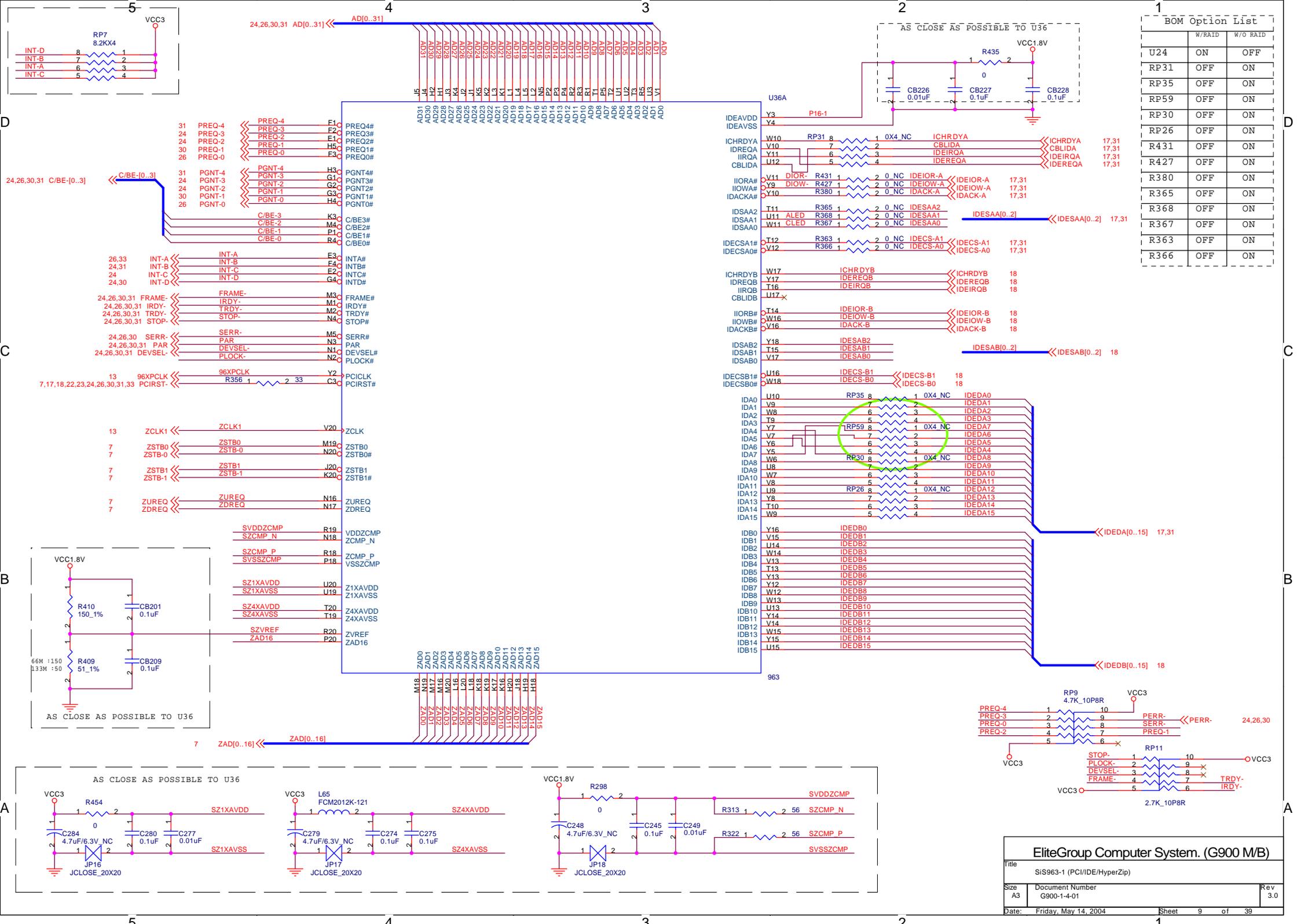
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Size	Document Number	Rev	3.0
Custom	G900-1-4-01		
Date:	Friday, May 14, 2004	Sheet	6 of 39



# 648FX-4

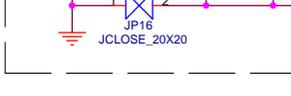
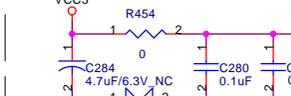
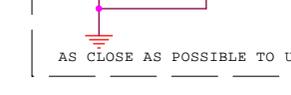
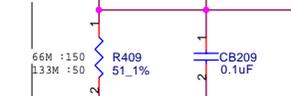
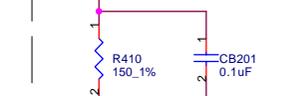
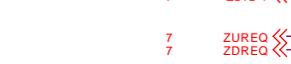
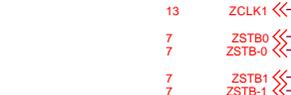
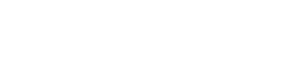
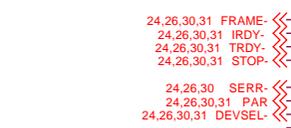
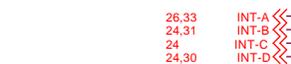
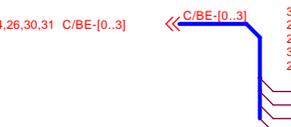
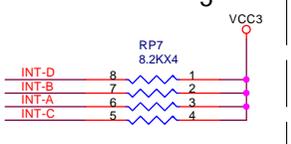
## Power



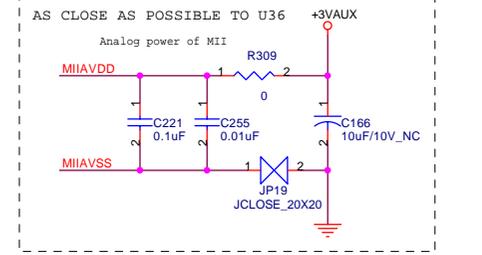
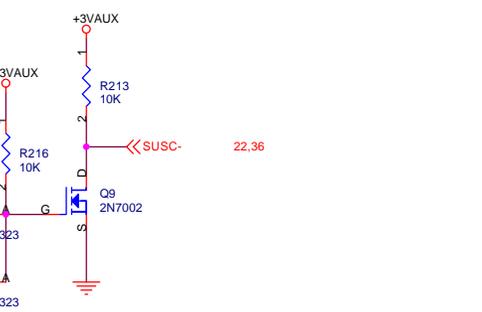
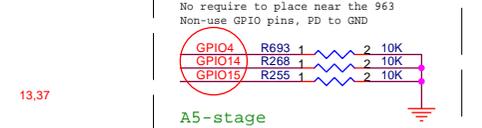
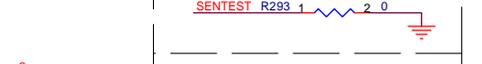
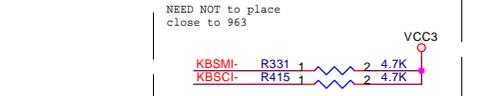
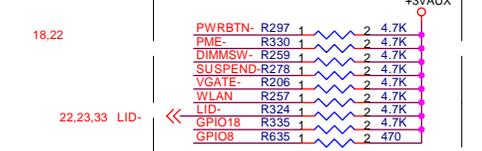
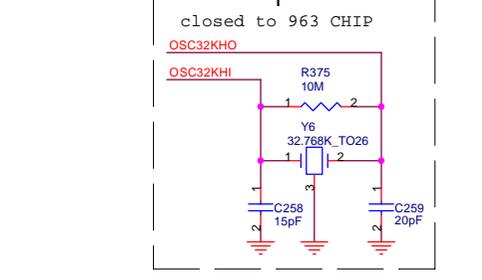
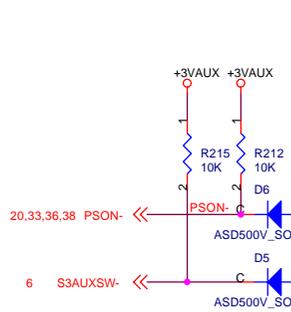
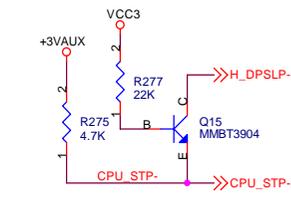
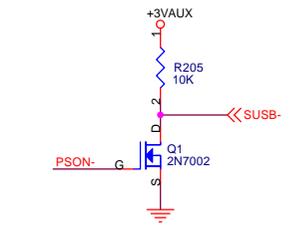
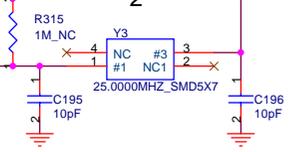
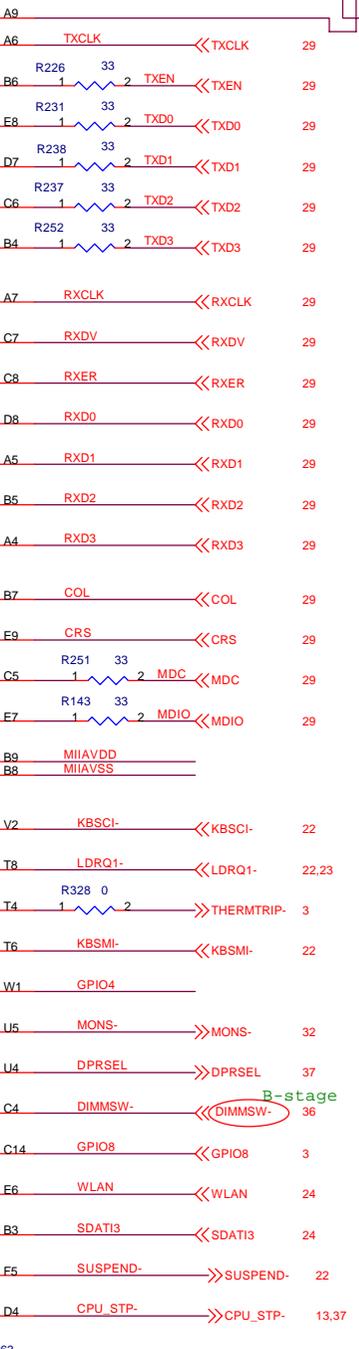
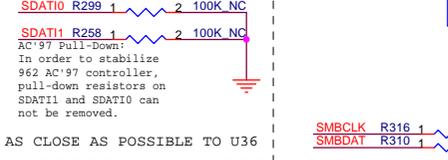
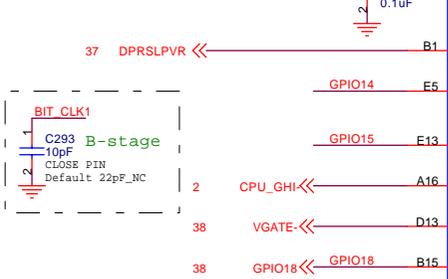
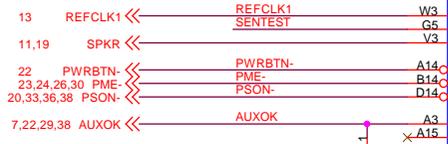
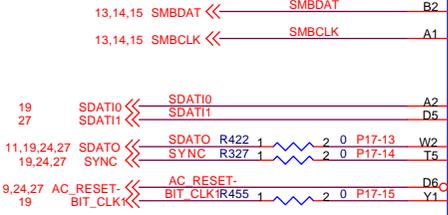
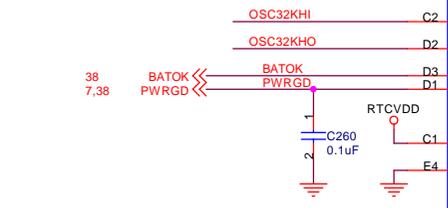
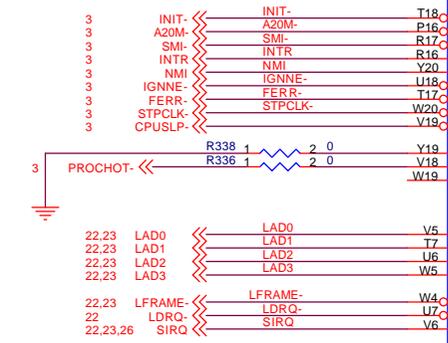


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U24	ON	OFF
RP31	OFF	ON
RP35	OFF	ON
RP59	OFF	ON
RP30	OFF	ON
RP26	OFF	ON
R431	OFF	ON
R427	OFF	ON
R380	OFF	ON
R365	OFF	ON
R368	OFF	ON
R367	OFF	ON
R363	OFF	ON
R366	OFF	ON

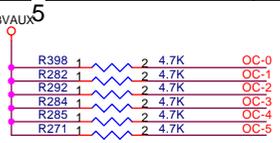
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Title		
SiS963-1 (PCI/IDE/HyperZip)		
Size	Document Number	Rev
A3	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 9 of 39



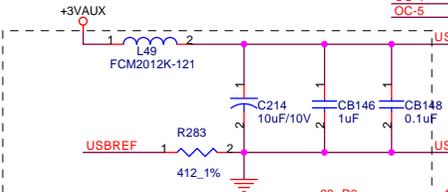
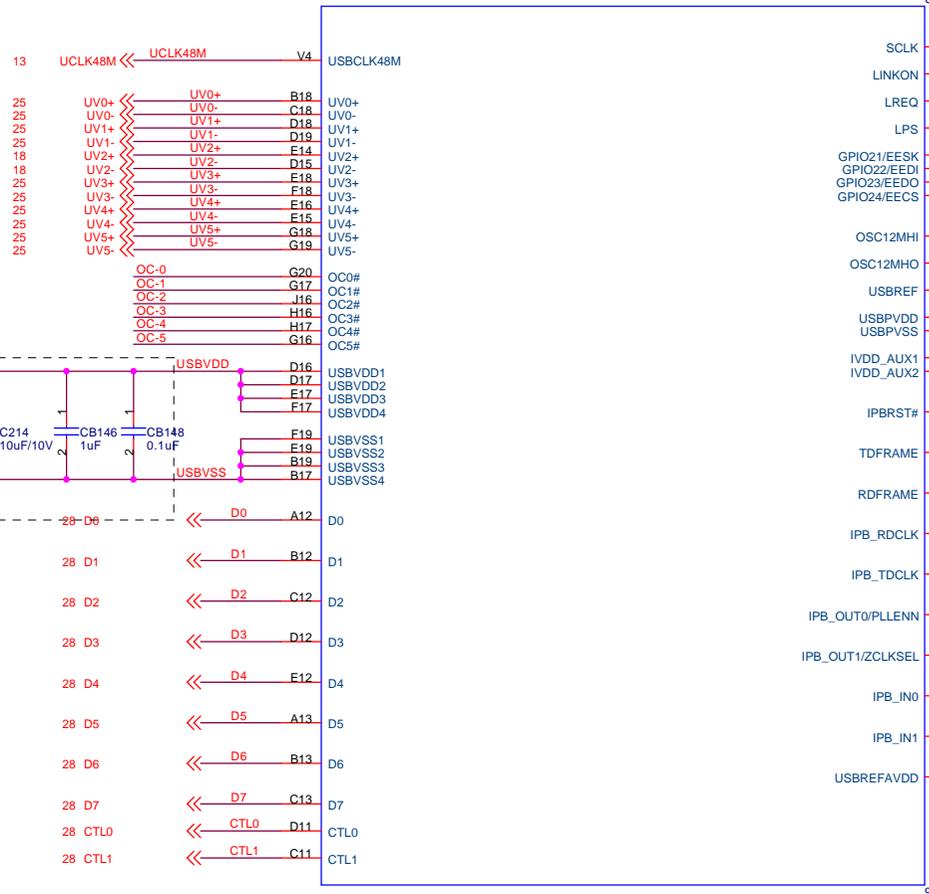
Programmable on-die high-strength for CPU\_S:  
(Infinite, 150, 110, 56 Ohm)



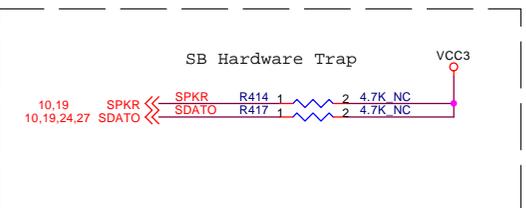
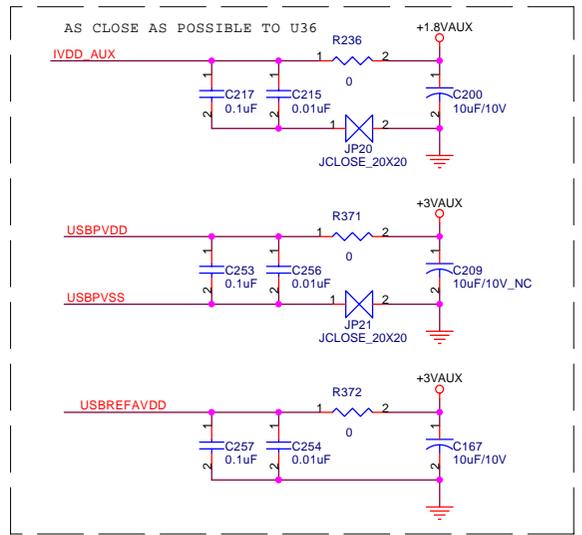
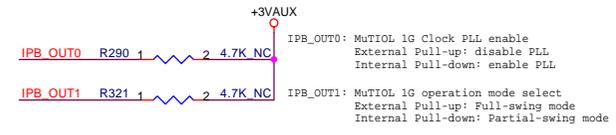
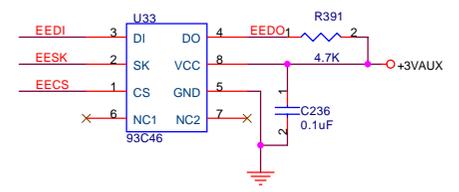
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Size	Document Number	Rev	
A3	G900-1-4-01	3.0	
Date:	Friday, May 14, 2004	Sheet	10 of 39



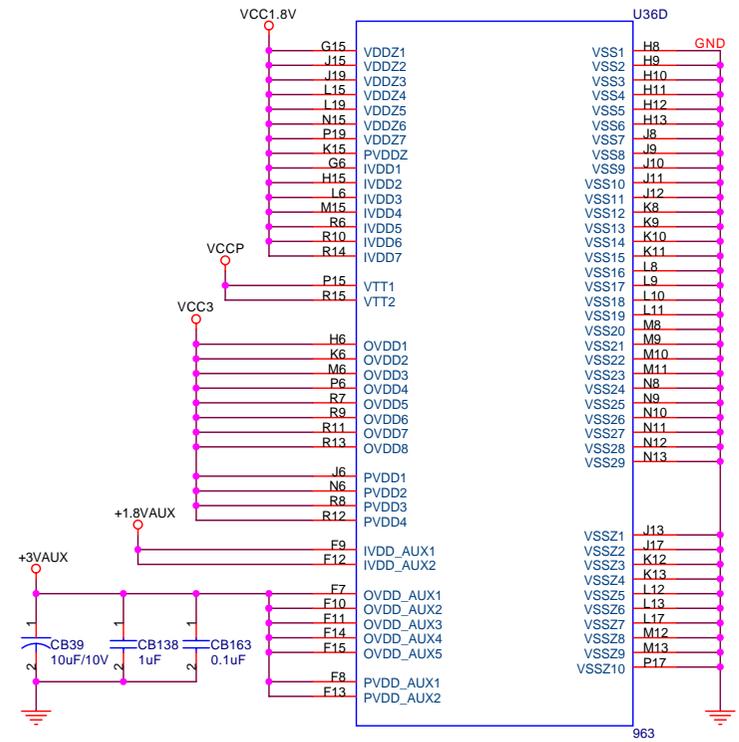
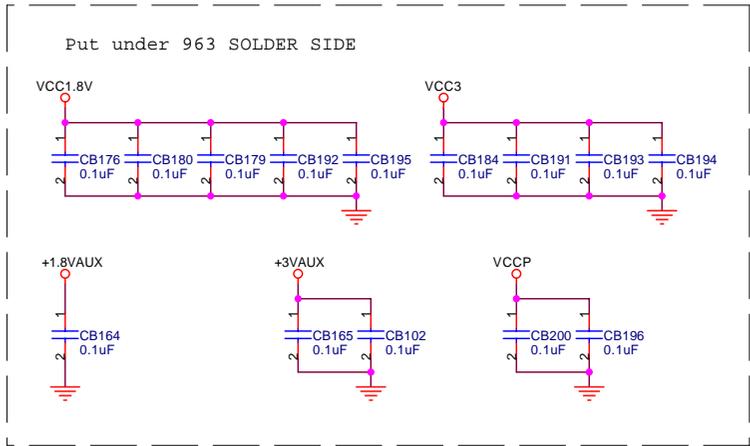
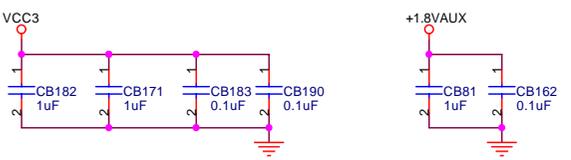
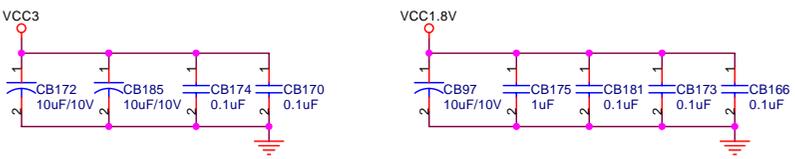
They don't need to be placed near the SiS963



AS CLOSE AS POSSIBLE TO U36

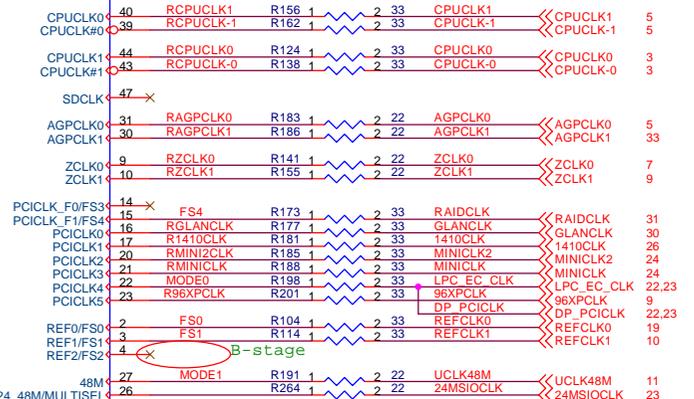
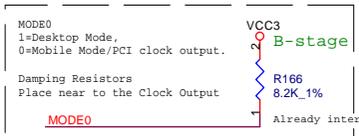
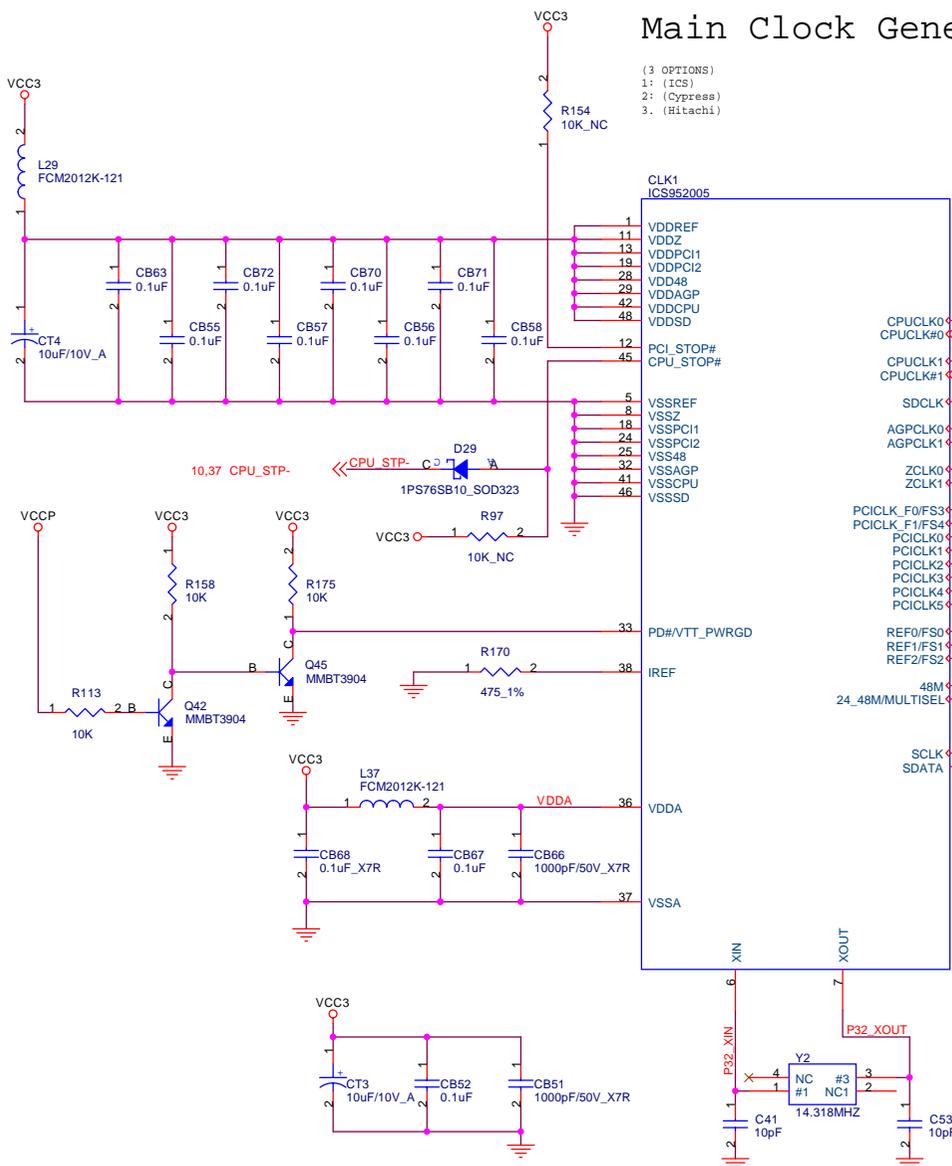


	0	1	Default	
SPKR( LPC address mapping)	disable	enable	R414 un-stuff	yes
SDATO( Trap from)	ROM	PCI AD	R417 un-stuff	yes
SB debug mode	enable	disable		

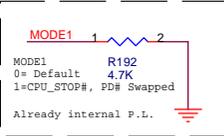
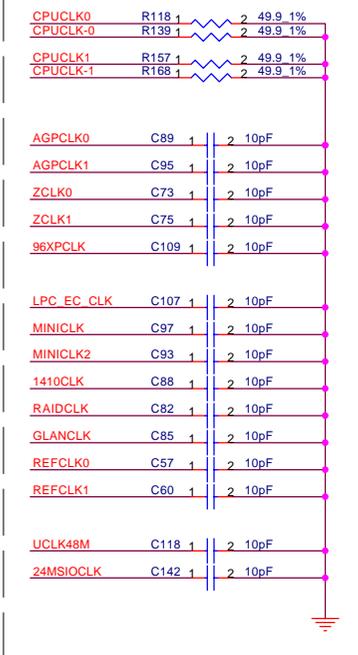


<b>EliteGroup Computer System. (G900 M/B)</b>		
Title		
SIS963-4 (POWER)		
Size	Document Number	Rev
B	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 12 of 39

# Main Clock Generator

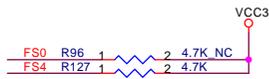


## By-Pass Capacitors Place near to the Clock Outputs



PIN12 ,26,45 INTERNAL PULL-UP

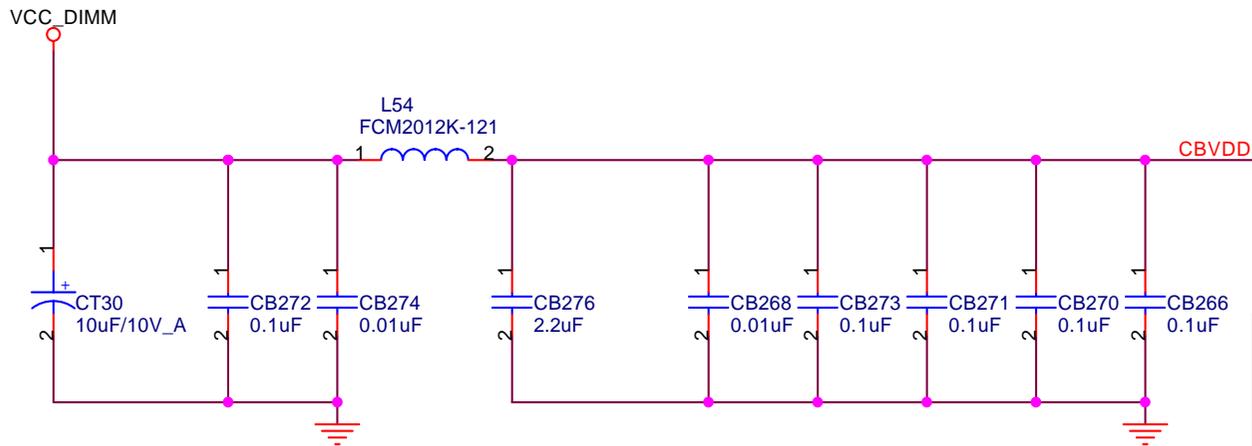
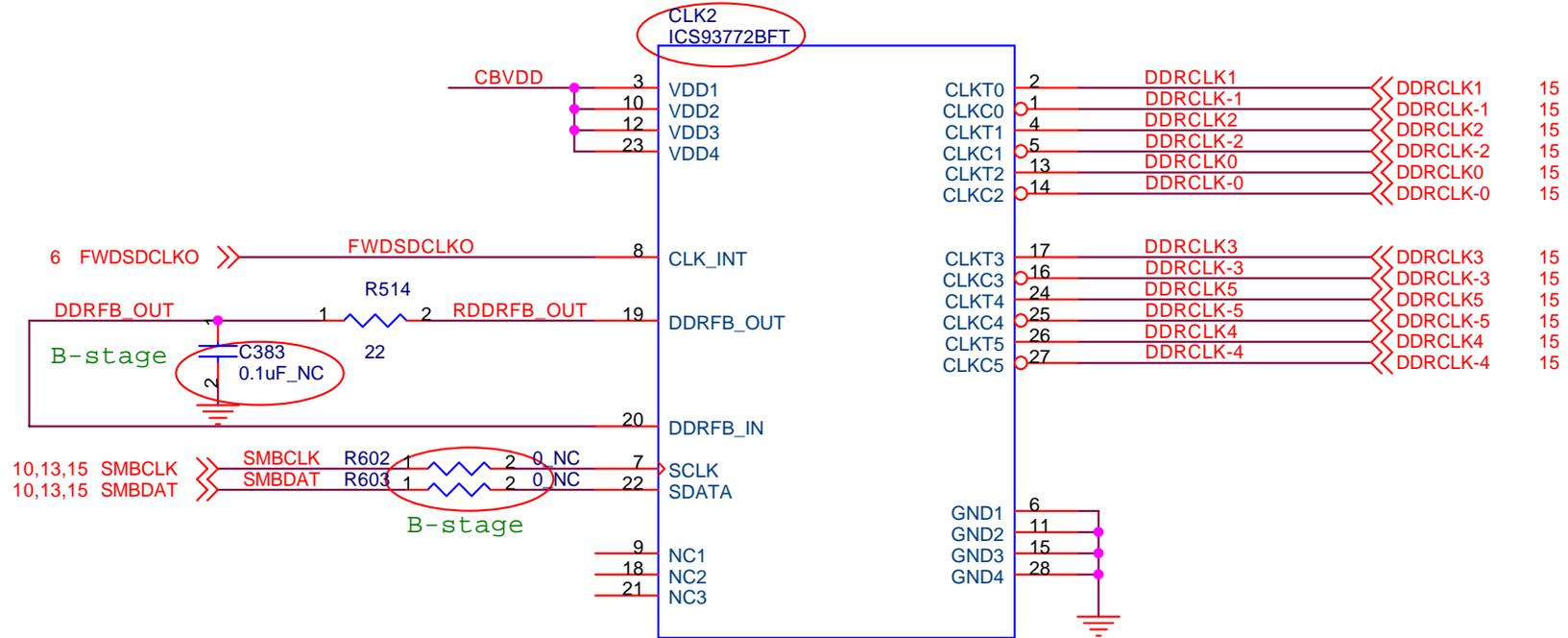
FS4	FS3	FS2	FS1	FS0	CPU	DDR	AGP	PCI	ZCLK
1	0	0	0	0	100	100	66	33	133
1	0	0	0	1	100	133	66	33	133
1	0	1	0	1	133	133	66	33	133
1	0	1	1	1	133	166	66	33	133
1	0	1	1	0	133	200	66	33	133
1	1	1	0	1	166	133	66	33	133
1	1	1	1	1	166	166	66	33	133
1	1	1	1	0	166	200	66	33	133
1	1	0	0	1	200	133	66	33	133
1	1	0	1	1	200	166	66	33	133
1	1	0	1	0	200	200	66	33	133



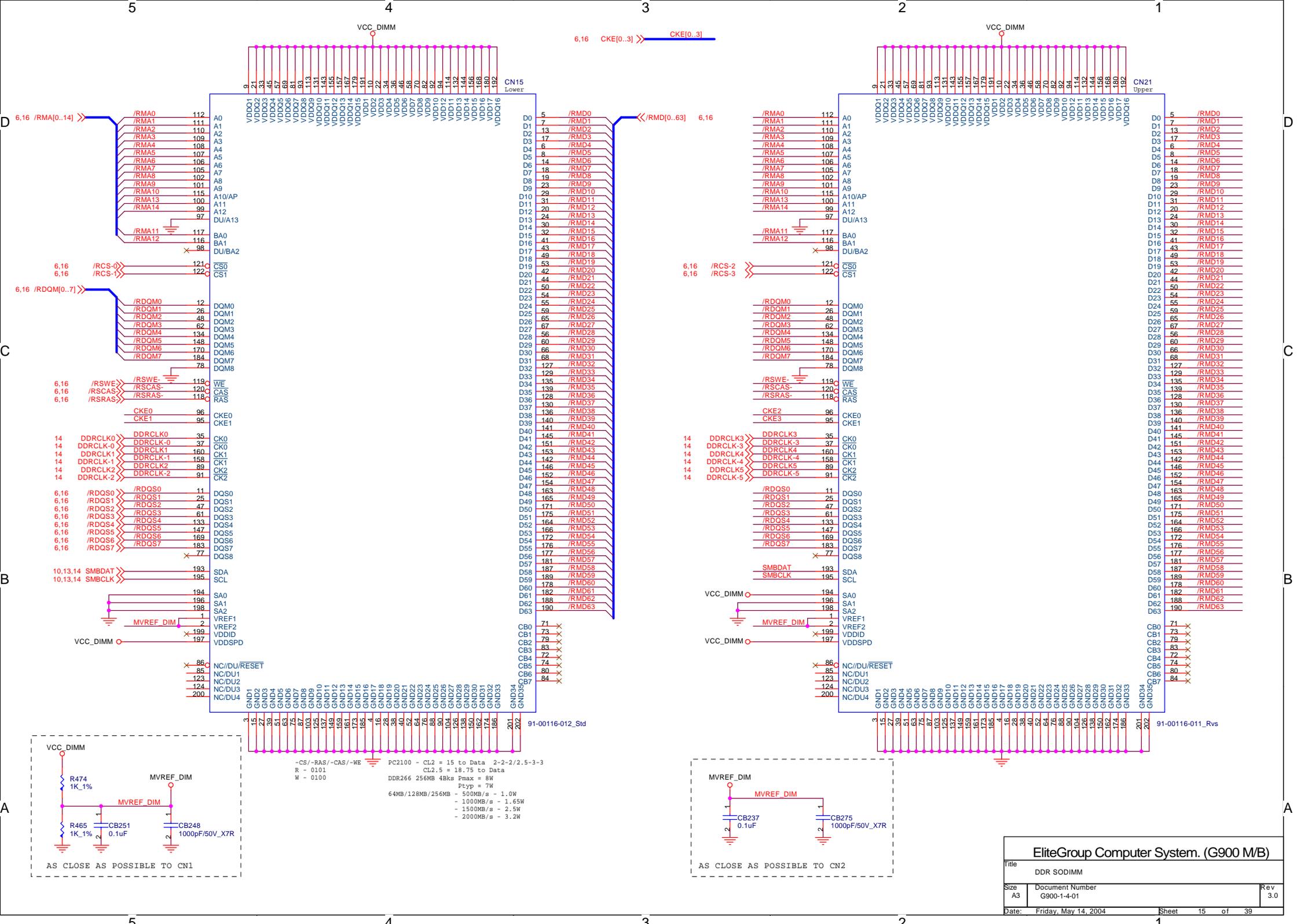
# Clock Buffer (DDR)

B-stage

Change from 93722 to 93772

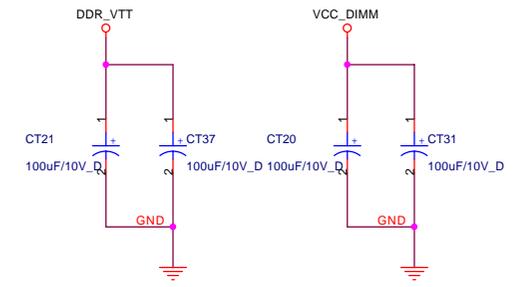
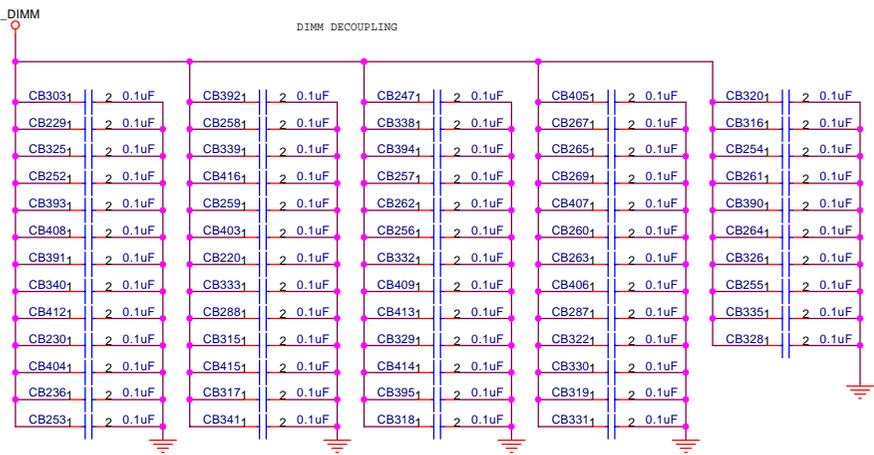
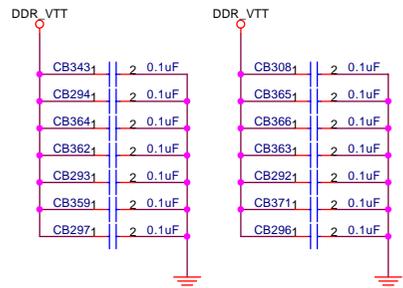
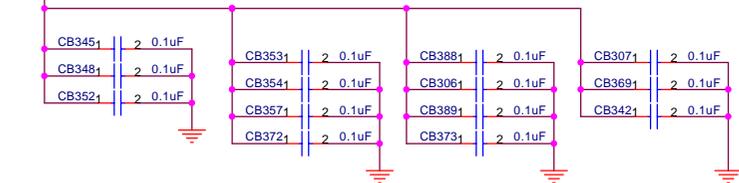
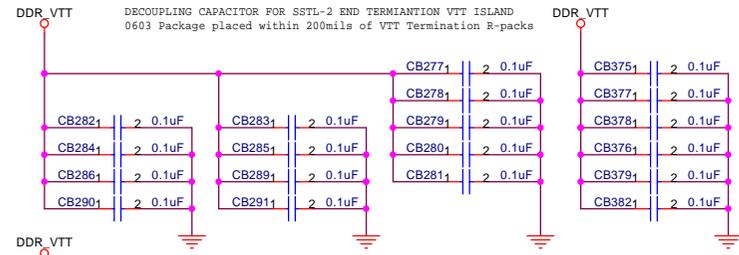
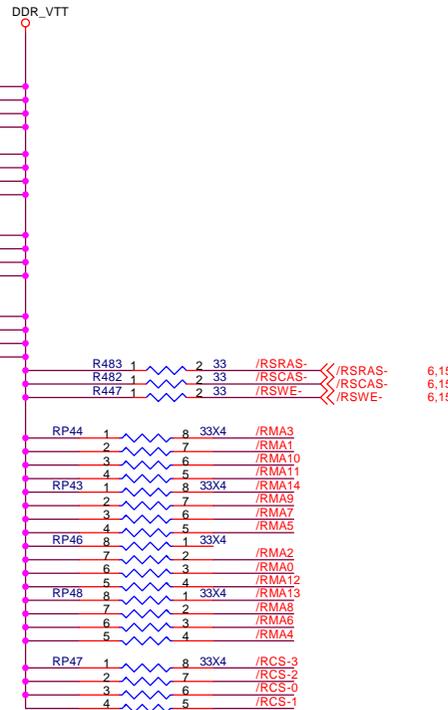
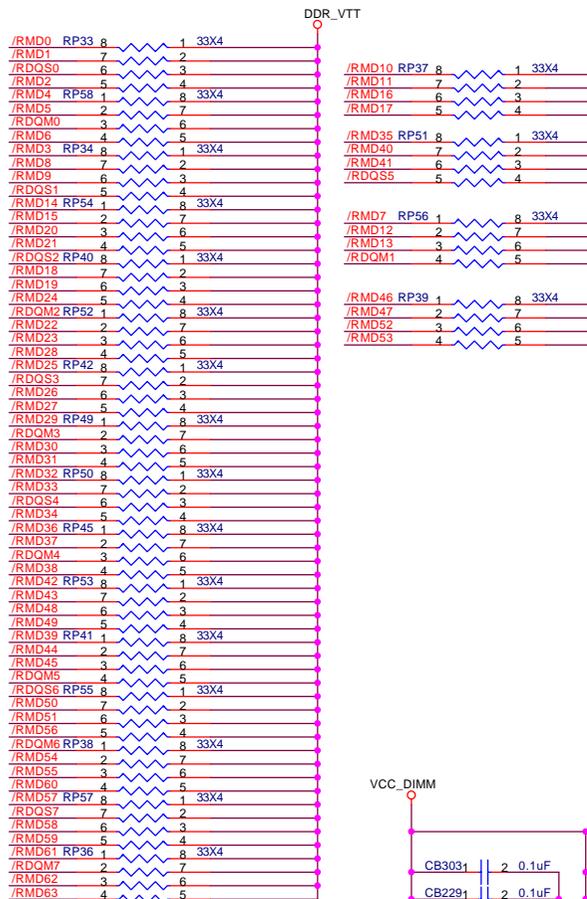
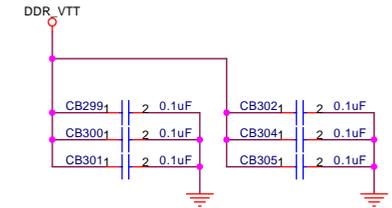
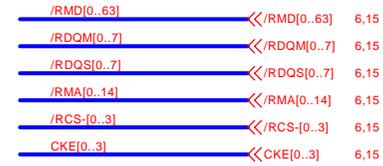


EliteGroup Computer System. (G900 M/B)		
Title CLOCK BUFFER (DDR)		
Size A	Document Number G900-1-4-01	Rev 3.0
Date: Friday, May 14, 2004	Sheet 14	of 39

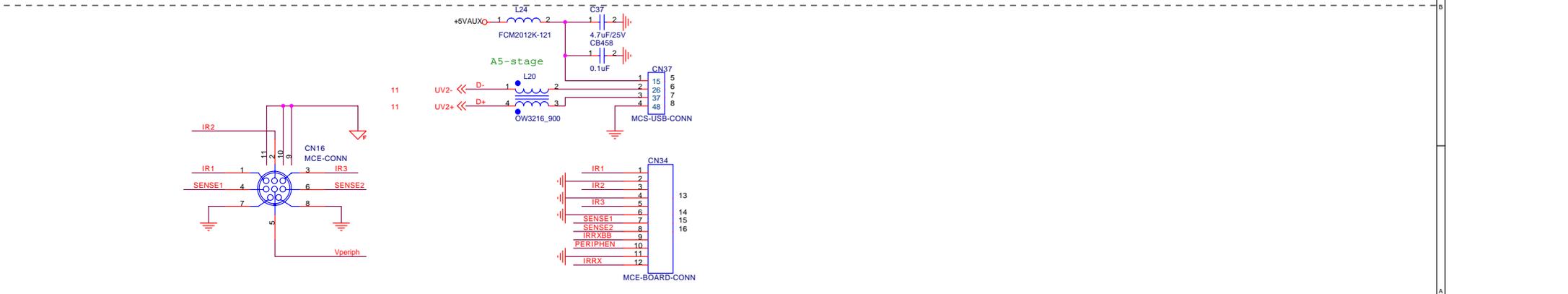
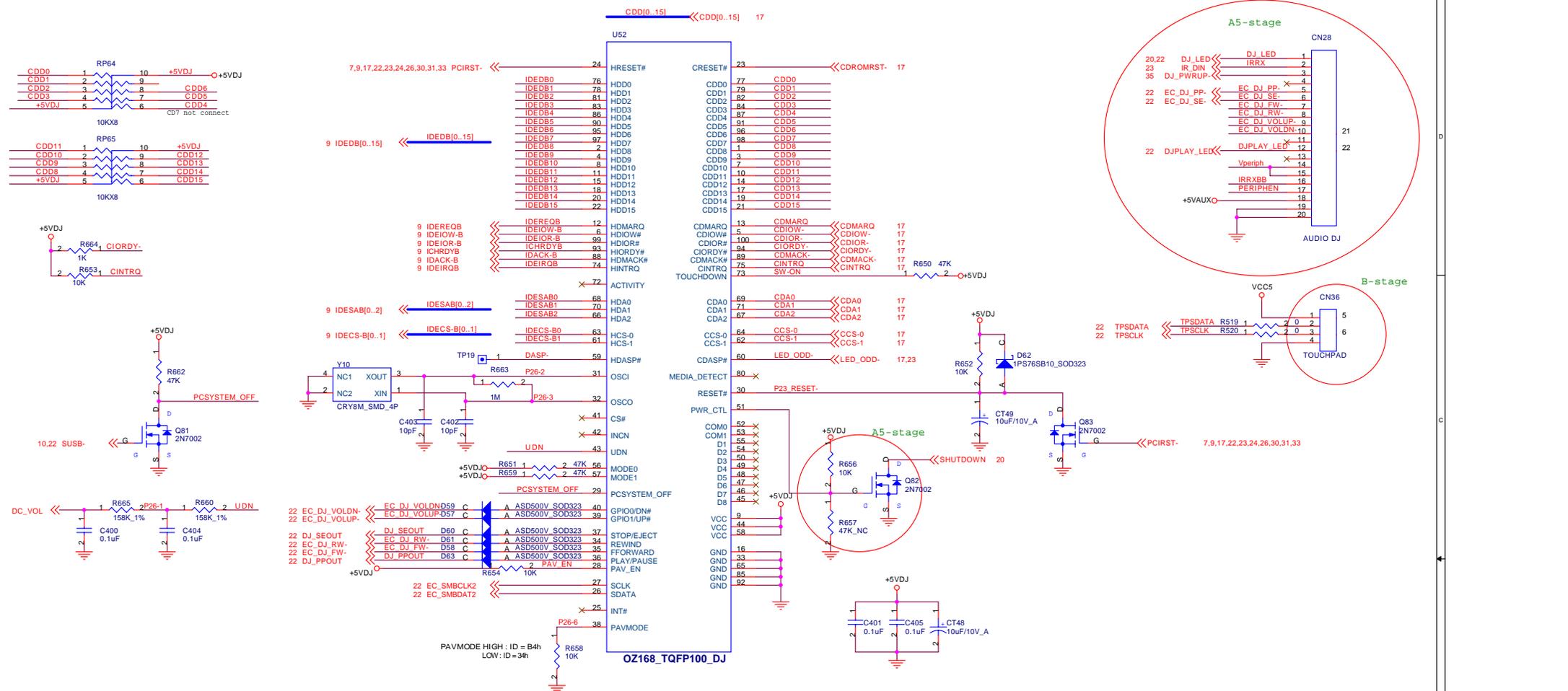


# SSTL-2 Termination Resistors

	SDR	Ra	DDR	Ra	RtL
MD/DQM (/DQS)	LV-CMOS	0/10/-	SSTL-2		47
MA/Control	LV-CMOS	0	SSTL-2		47
CS	LV-CMOS	0	SSTL-2		47
CKE	DD 3.3V		DD 2.5V		



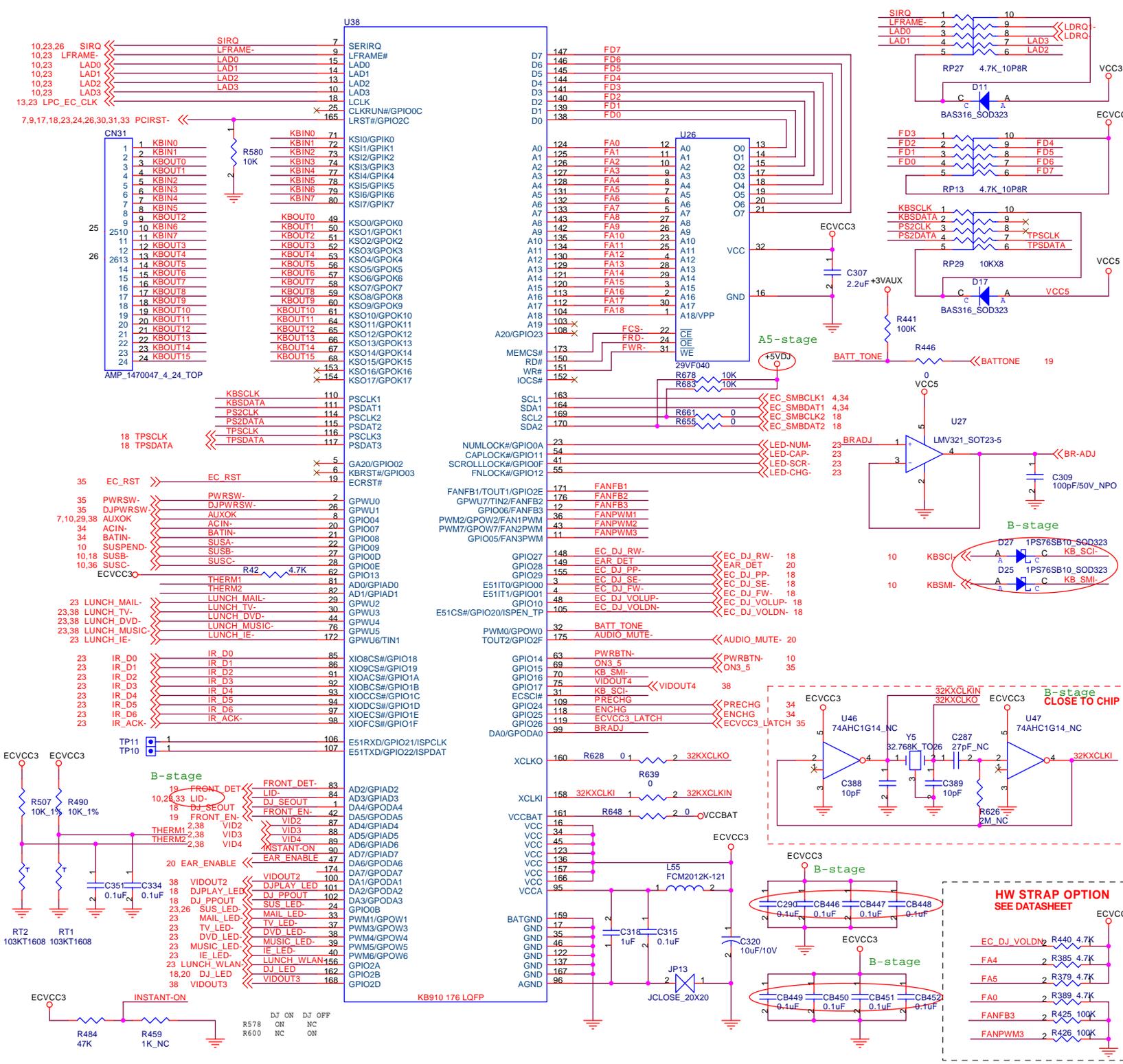






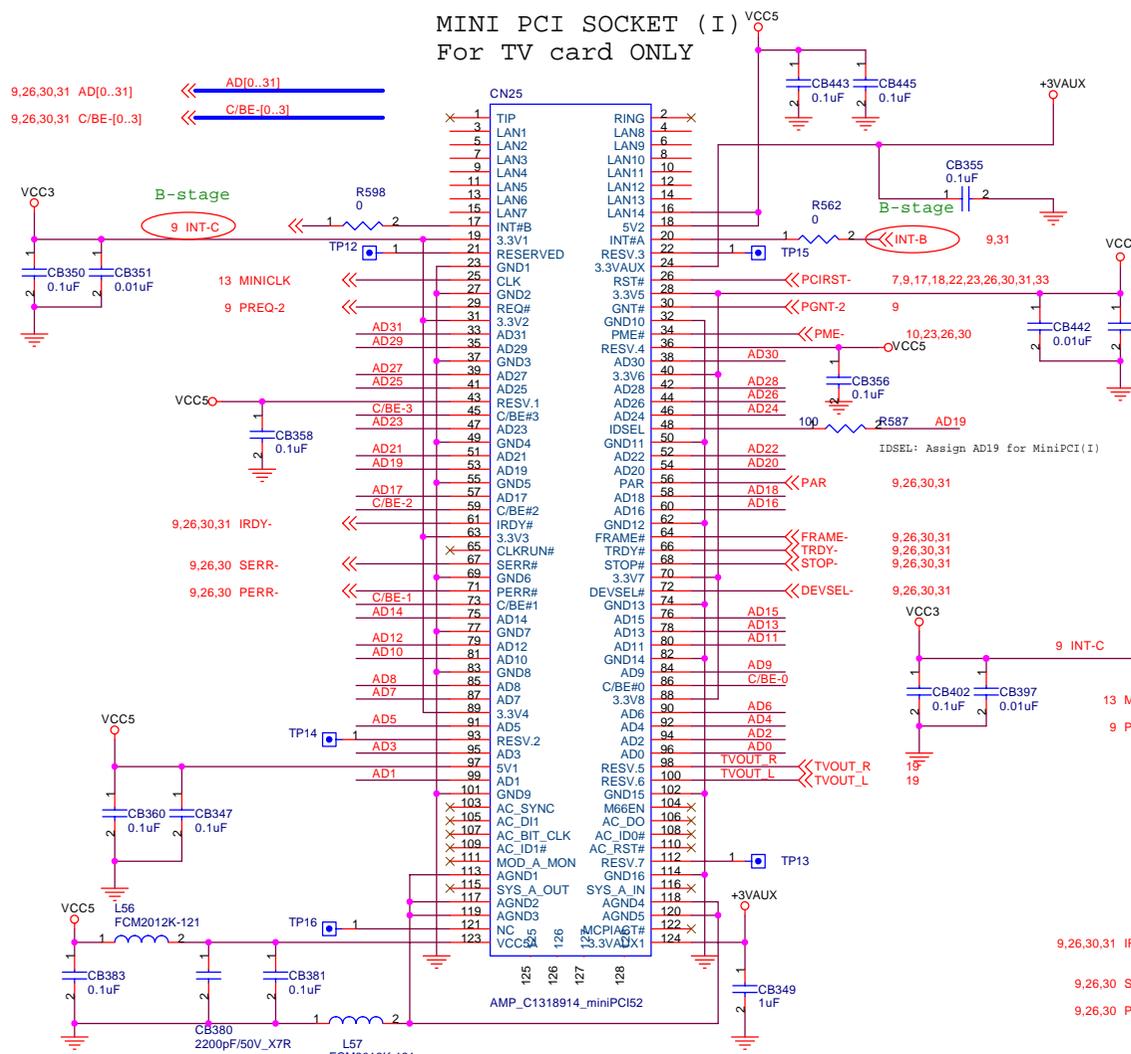




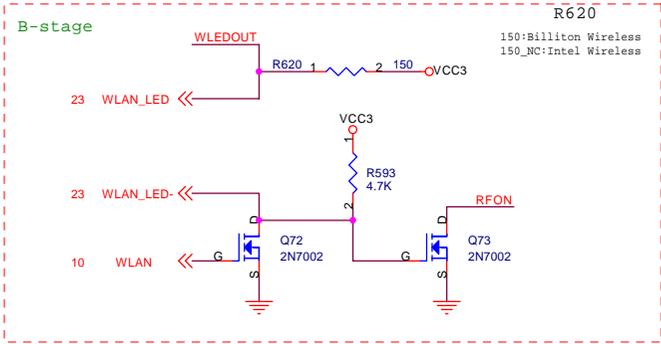




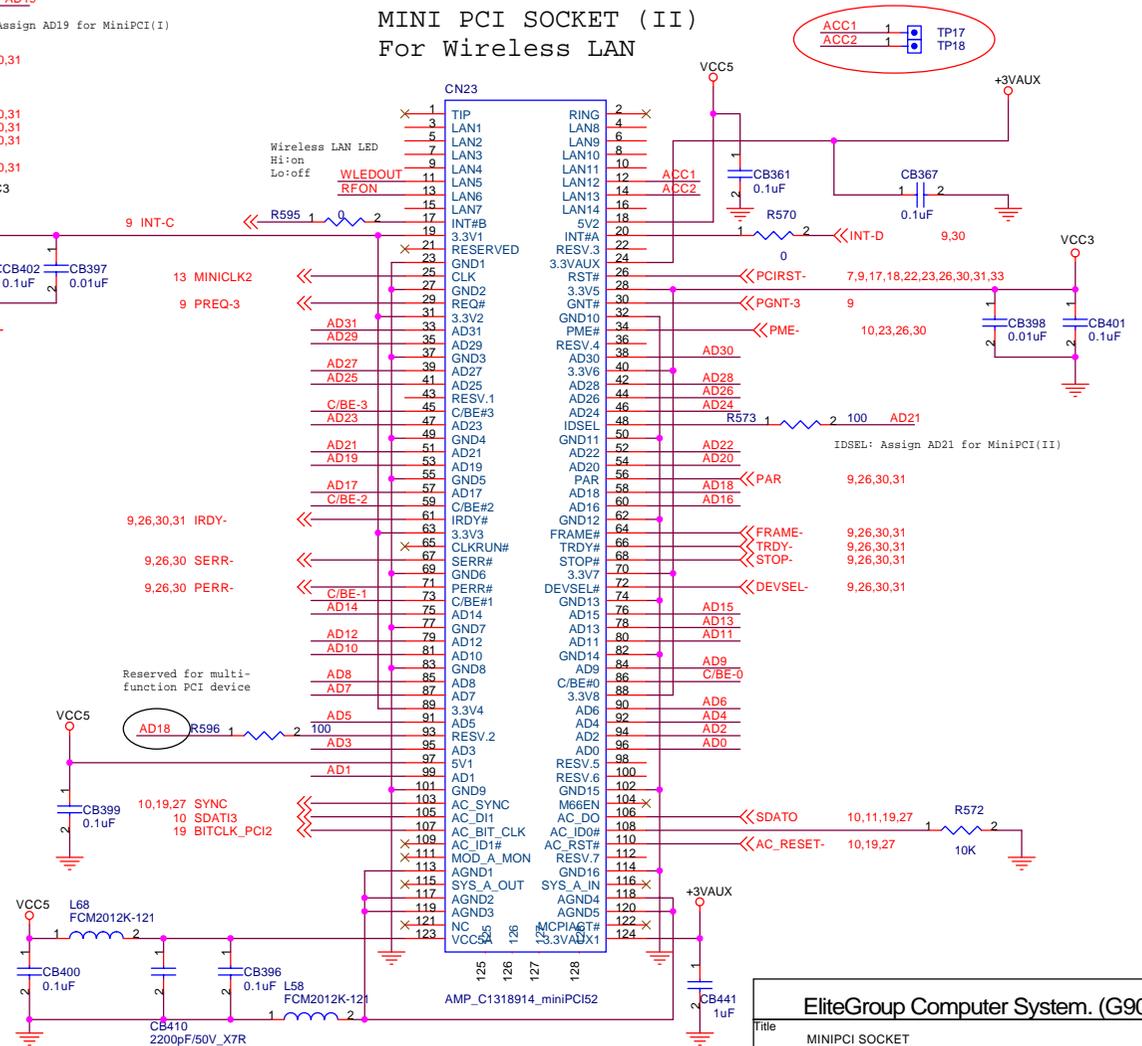
# MINI PCI SOCKET (I) For TV card ONLY



FootNote:  
 TP12:SVHS\_Y  
 TP13:SVHS\_C  
 TP14:CVBS  
 TP15:Audio-In\_R  
 TP16:Audio-In\_L

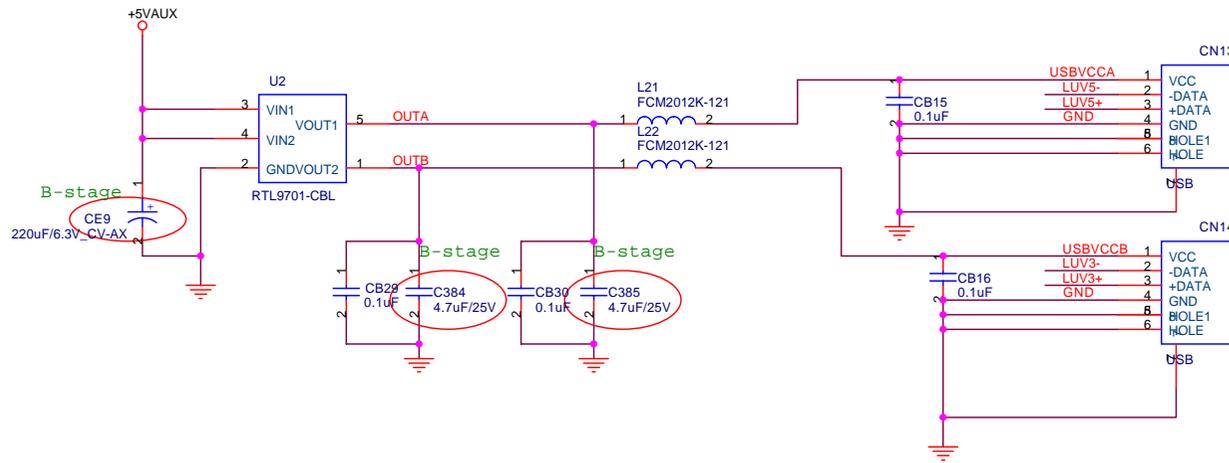
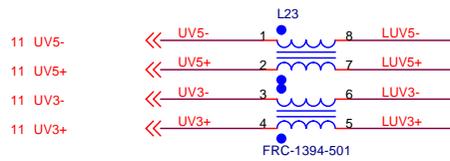


# MINI PCI SOCKET (II) For Wireless LAN

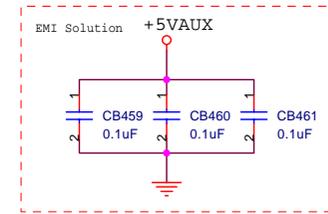
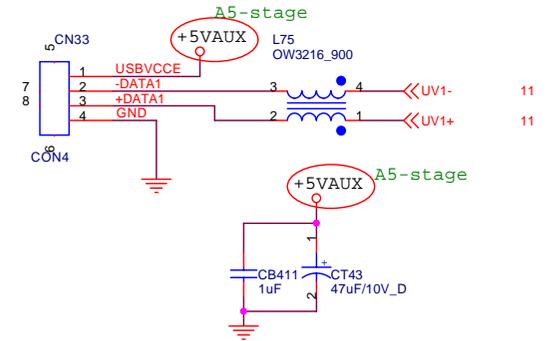


## EliteGroup Computer System. (G900 M/B)

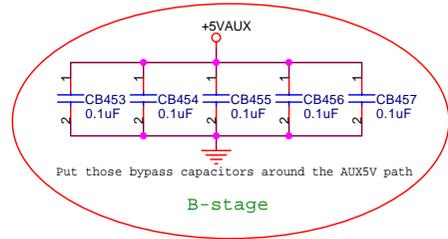
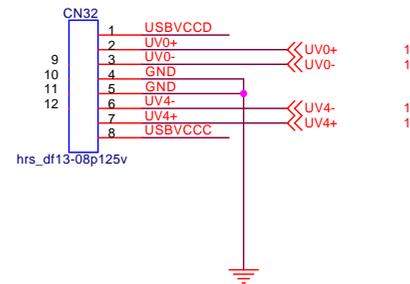
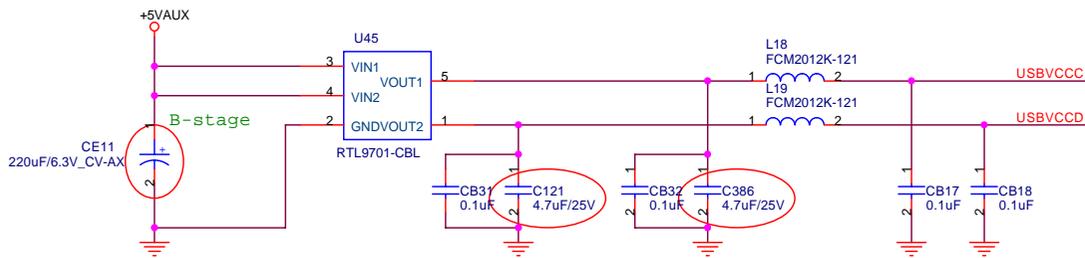
Title MINIPCI SOCKET		
Size A3	Document Number G900-1-4-01	Rev 3.0
Date: Friday, May 14, 2004	Sheet 24	of 39



### USB CardReader



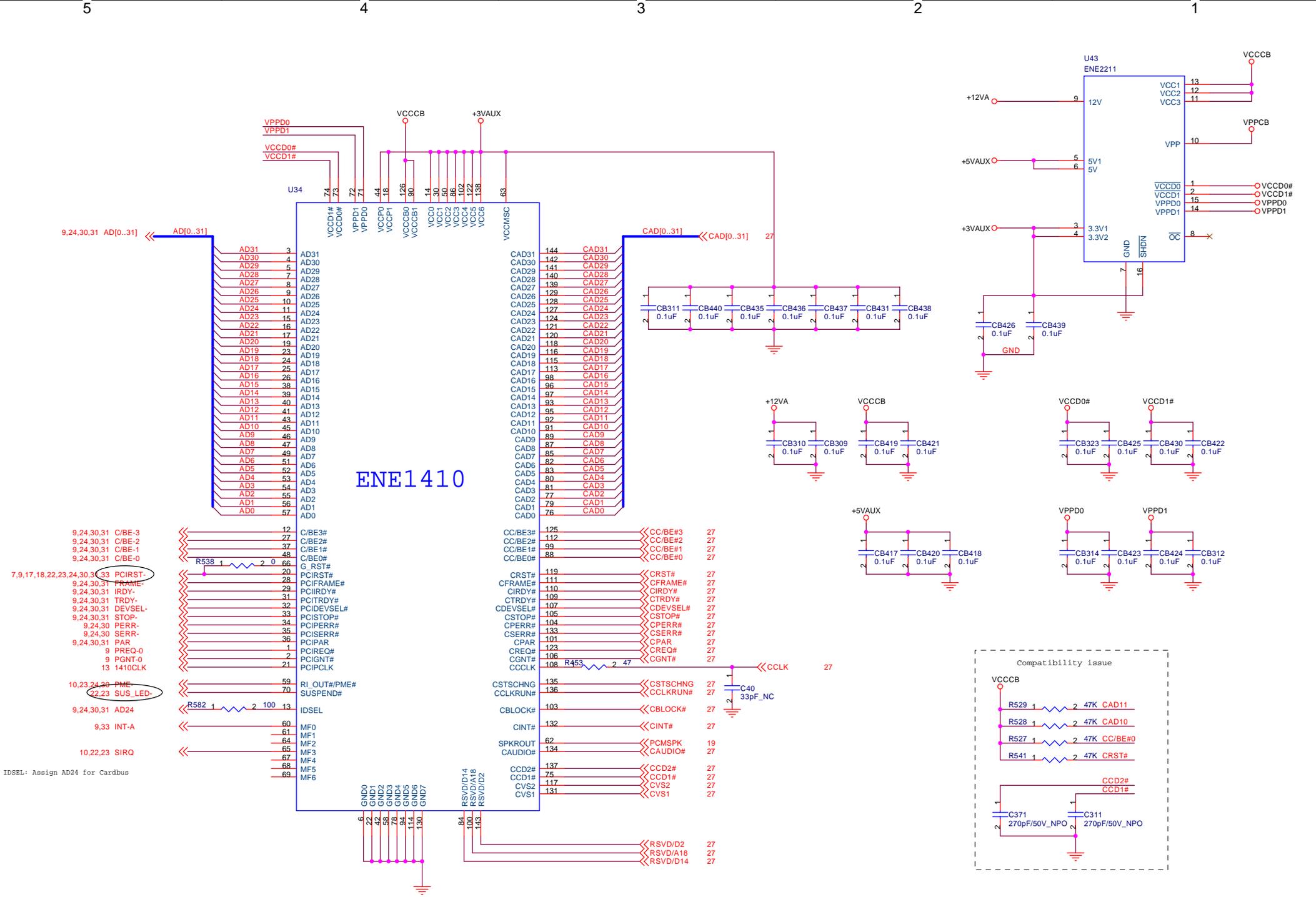
A5-stage  
To Double USB Connector

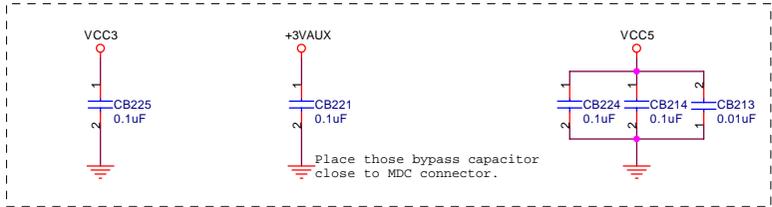
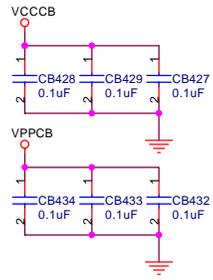


Put those bypass capacitors around the AUX5V path

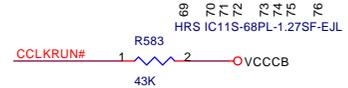
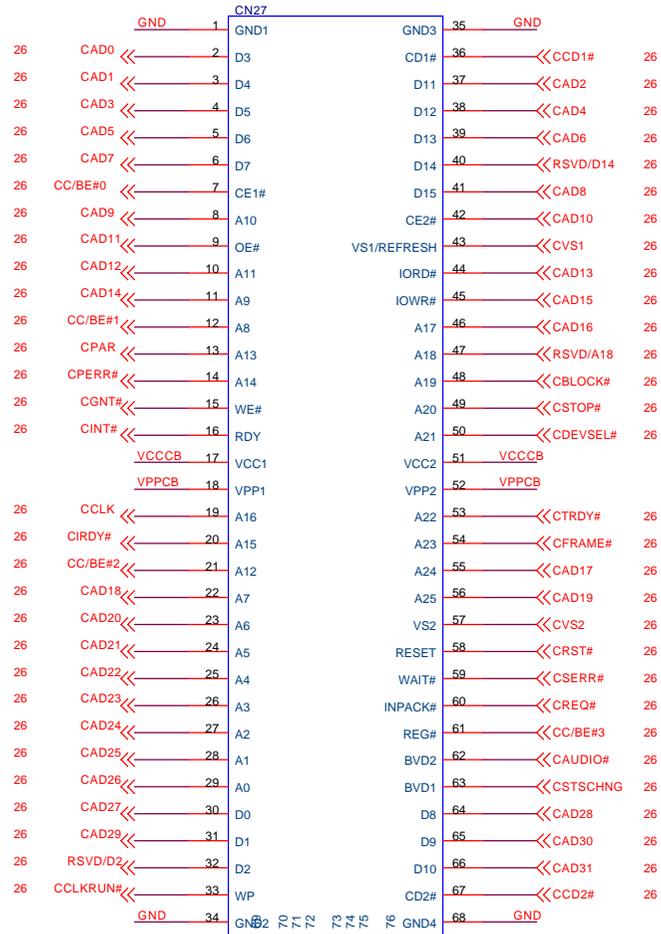
### EliteGroup Computer System. (G900 M/B)

Title		USB CONNECTOR
Size	Document Number	Rev
B	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 25 of 39

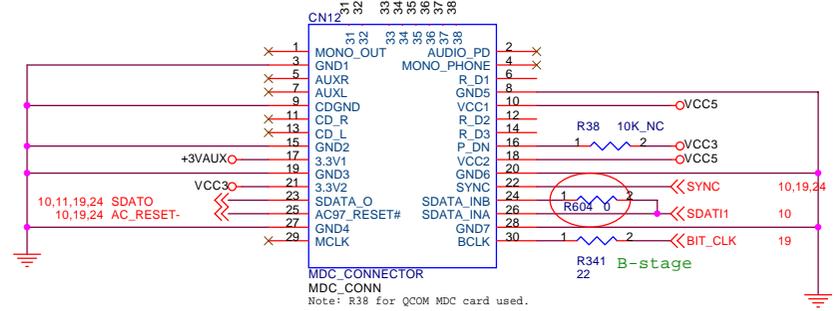




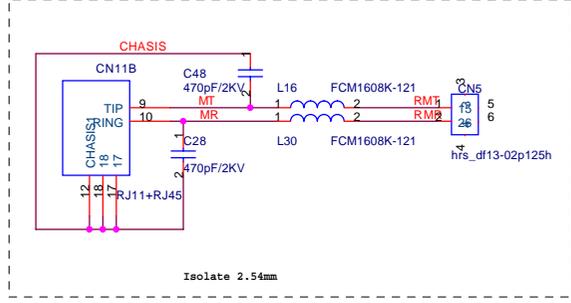
PCMCIA SLOT

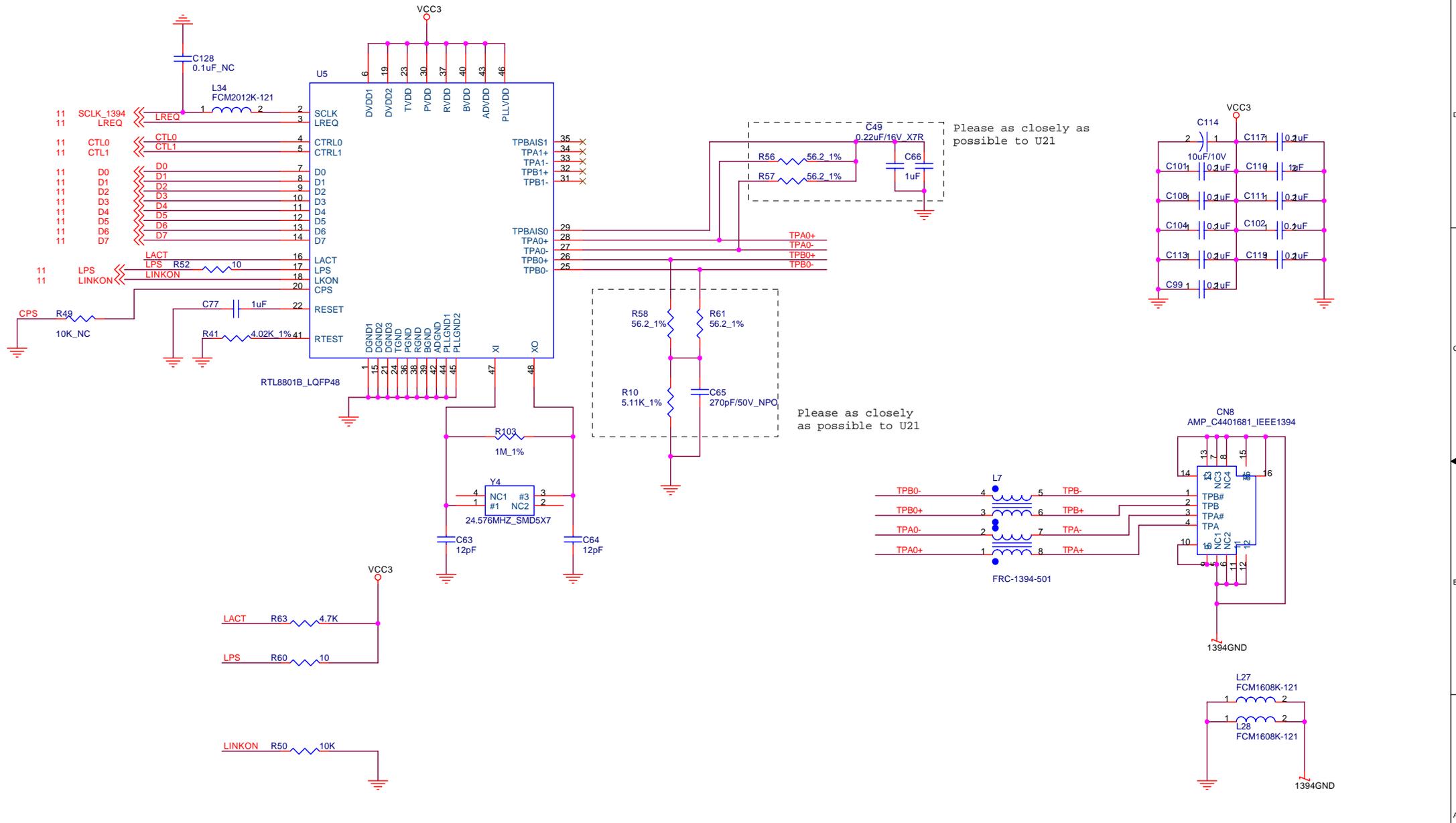


Modem Daughter Card Connector



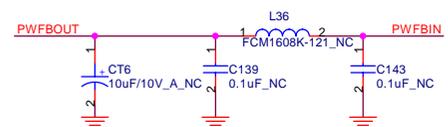
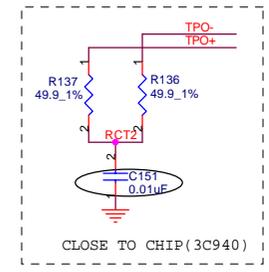
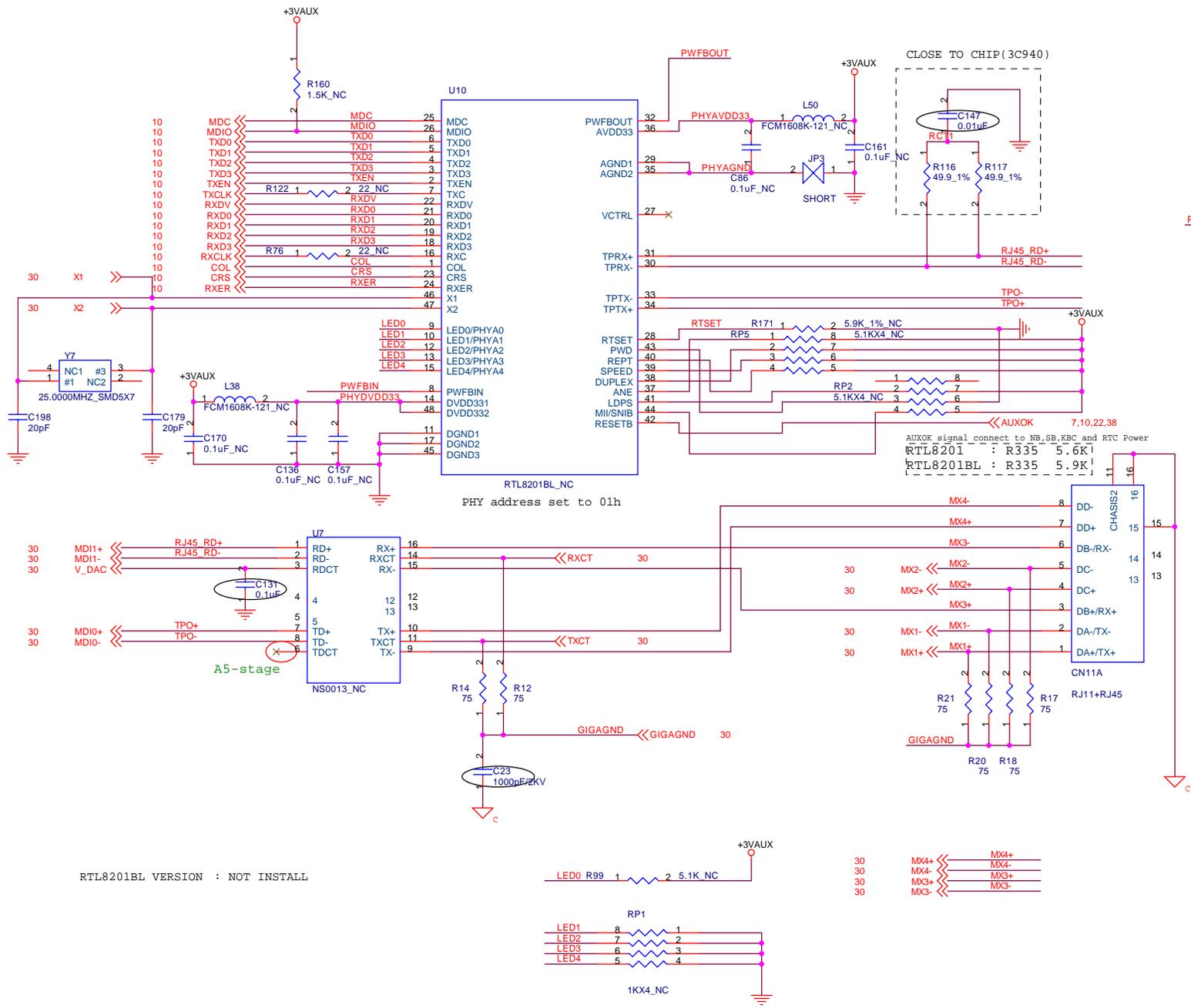
Note: R38 for QCOM MDC card used.





<b>EliteGroup Computer System. (G900 M/B)</b>		
Title REALTEK IEEE 1394 PHY		
Size B	Document Number G900-1-4-01	Rev 3.0
Date: Friday, May 14, 2004	Sheet 28	of 39

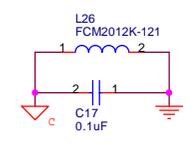
# RTL8201BL



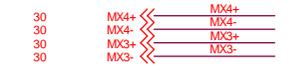
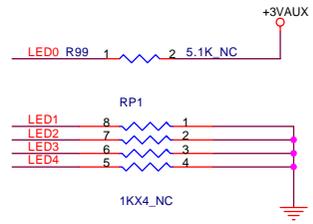
Place CT25, C557, L51 close to PWFBOU and place C558 close to PWFBIN

## Config:

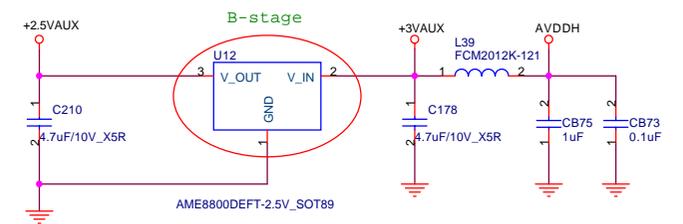
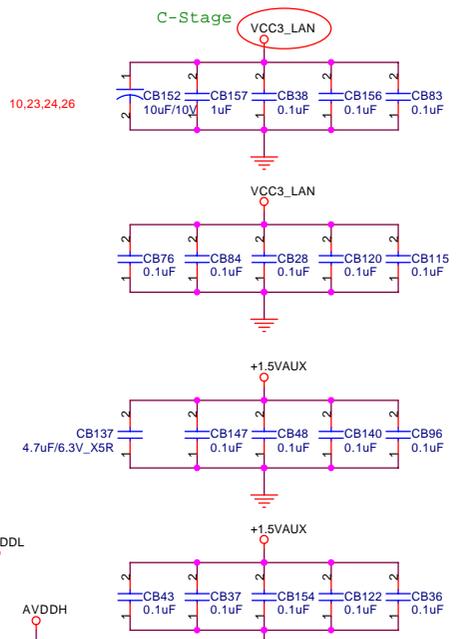
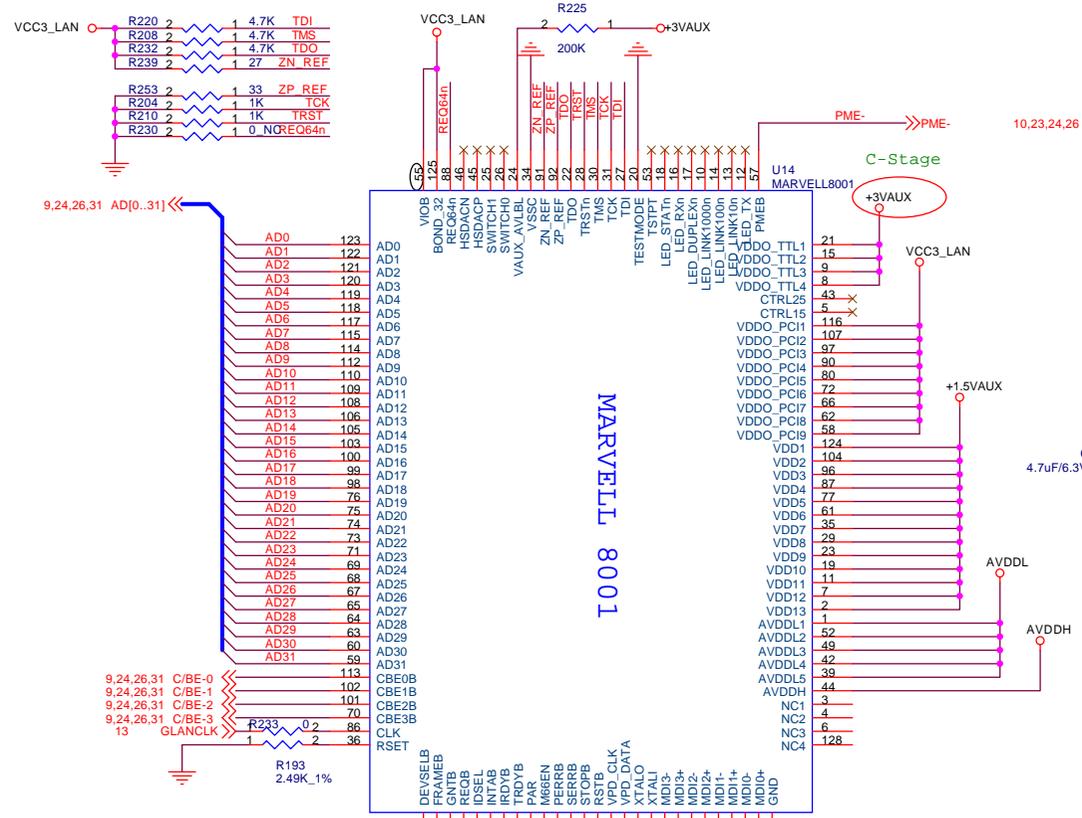
Enable: N-way, Full duplex, 100Mbps, Link Down Power Saving  
 Disable: Isolate, Repeater Mode



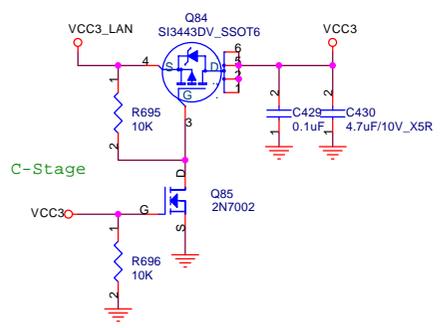
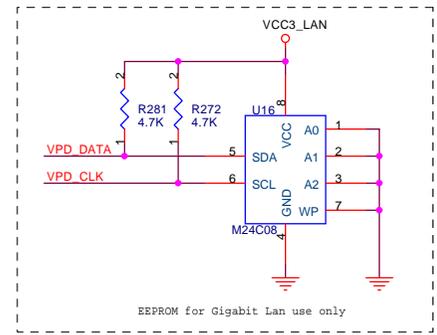
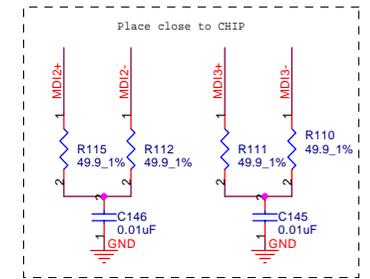
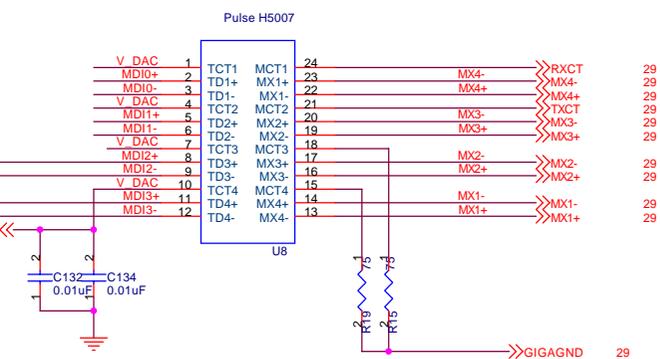
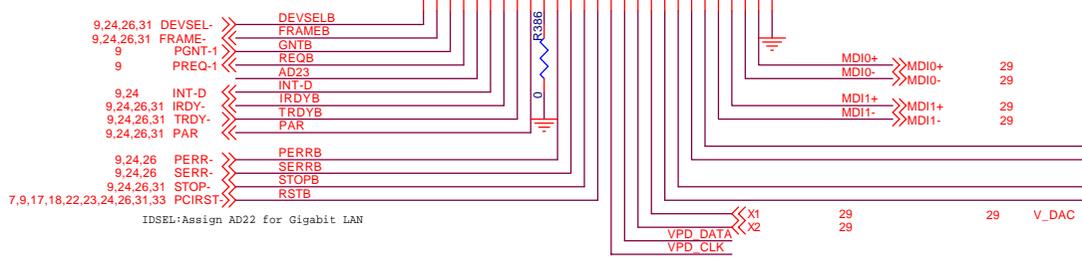
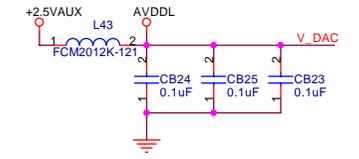
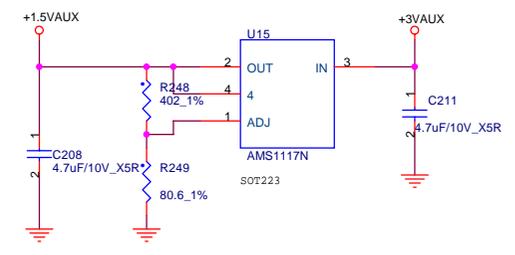
RTL8201BL VERSION : NOT INSTALL

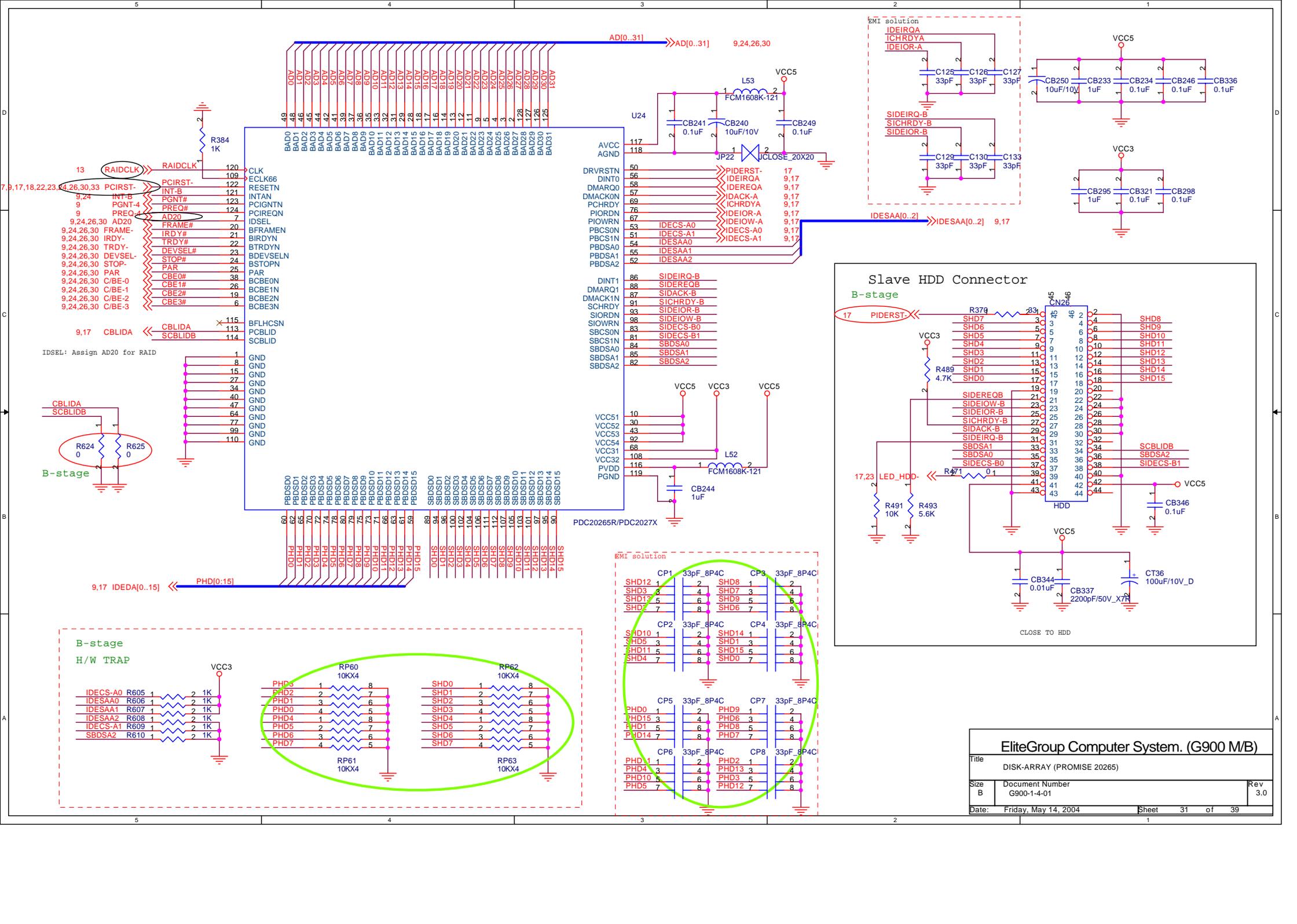


EliteGroup Computer System. (G900 M/B)		
Title	REALTEK 8201BL LAN PHY	
Size	Document Number	Rev
A3	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 29 of 39



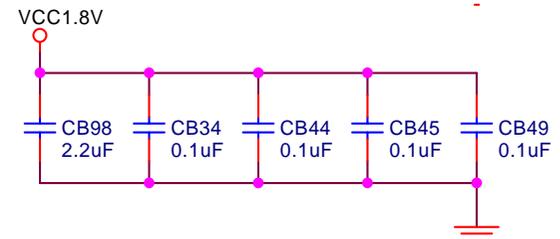
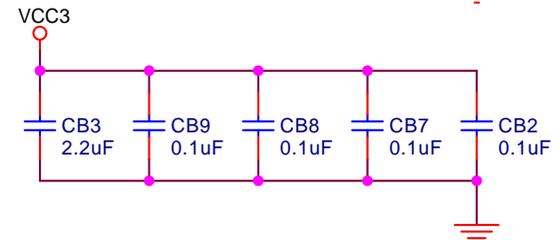
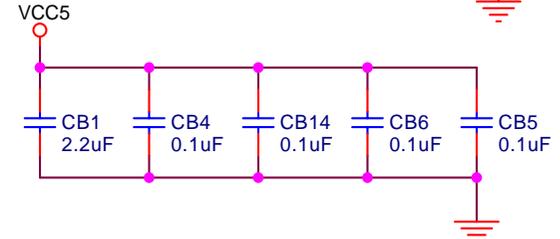
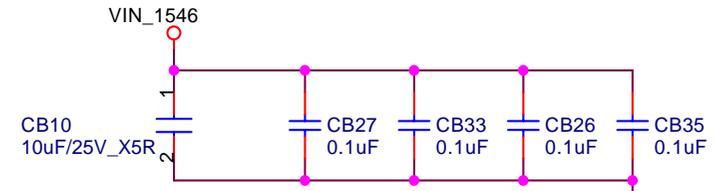
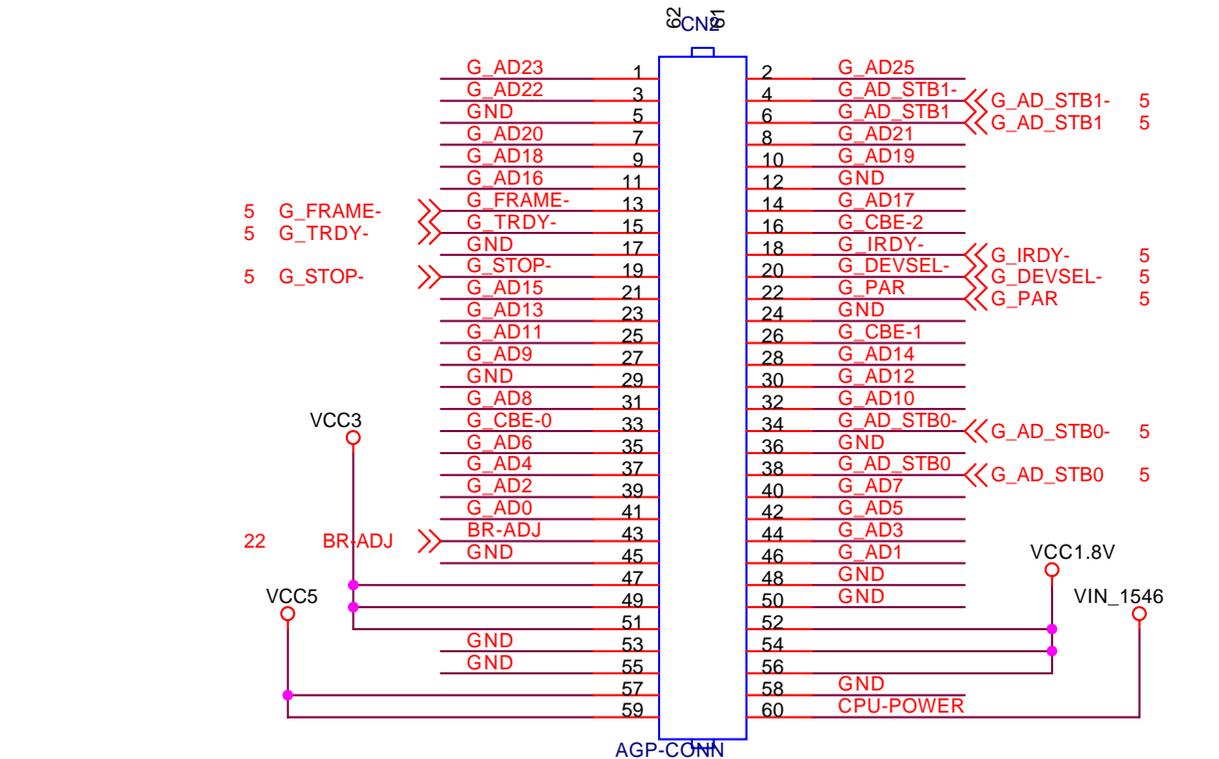
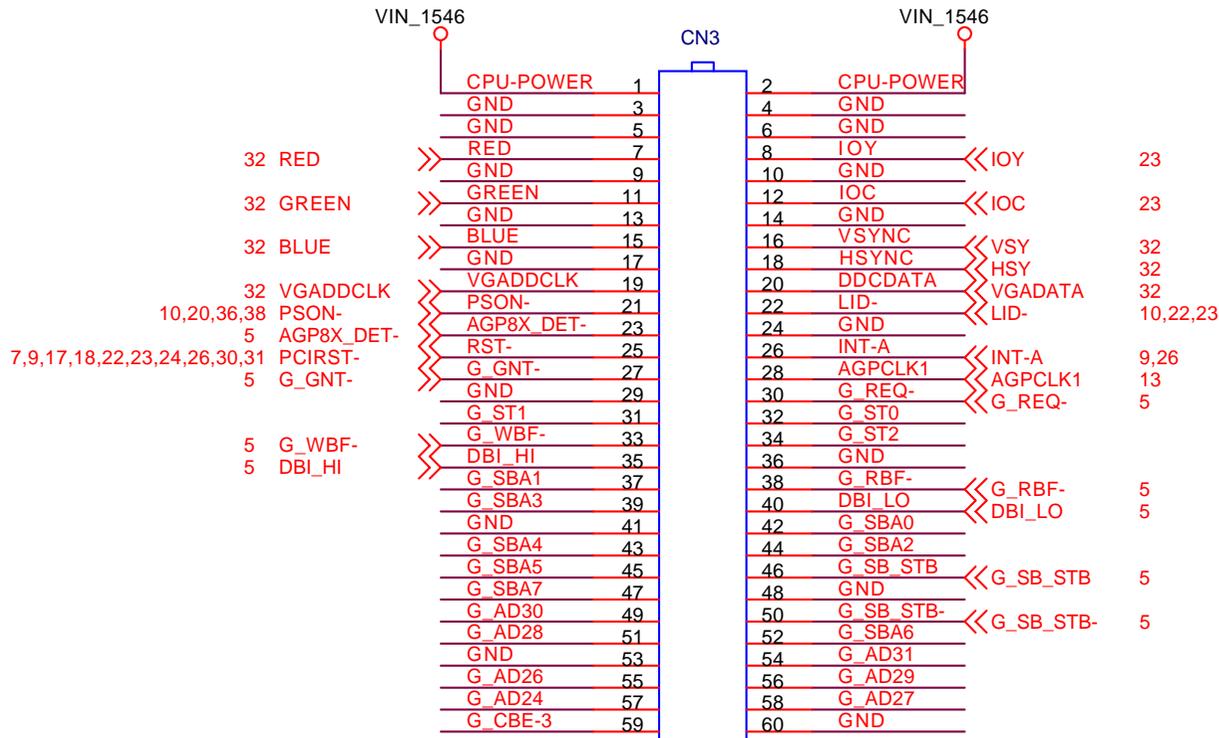
**+2.5VAUX SUPPLY 40mA for GIGA LAN**





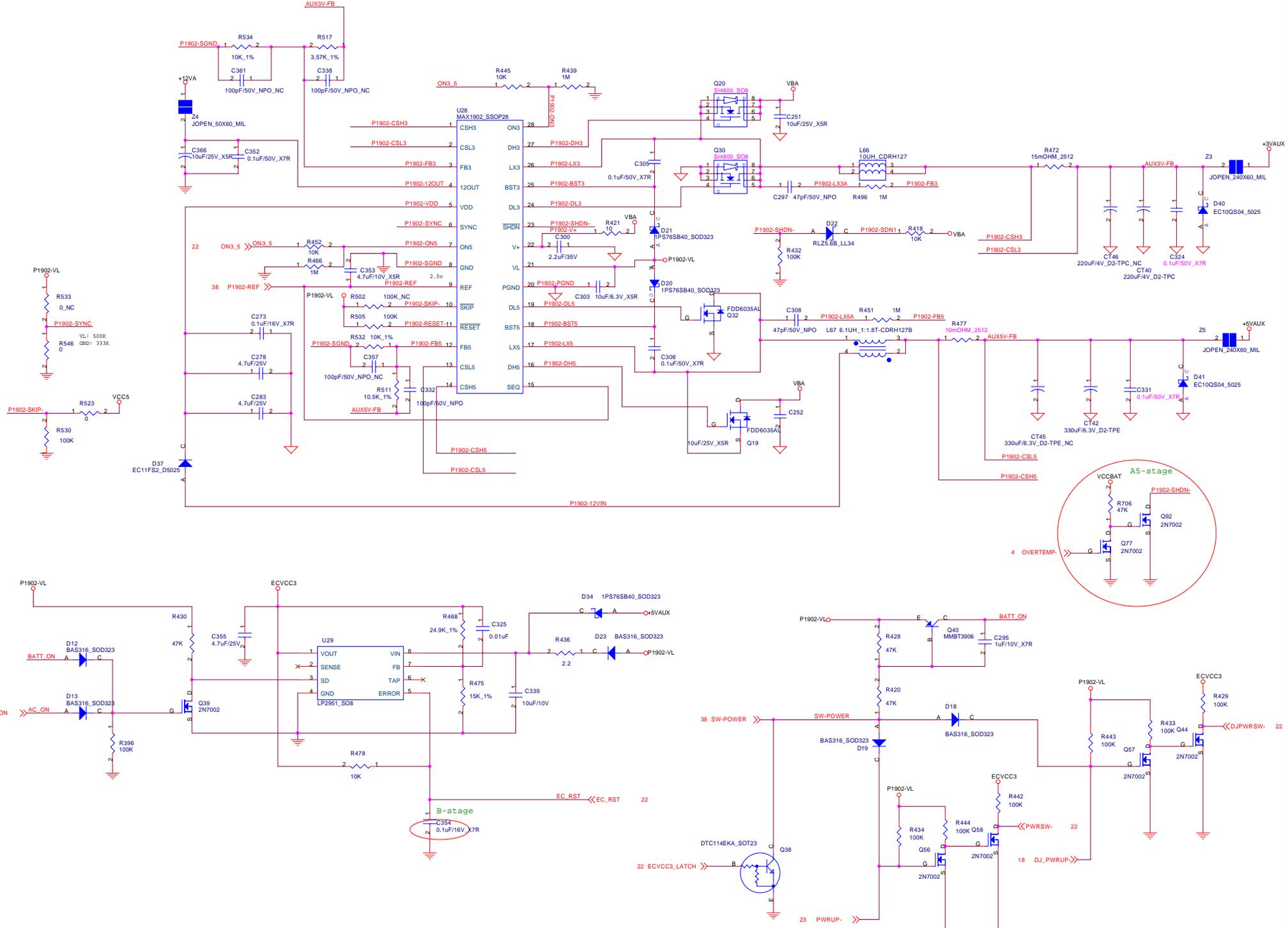
<b>EliteGroup Computer System. (G900 M/B)</b>		
Title	DISK-ARRAY (PROMISE 20265)	
Size	Document Number	Rev
B	G900-1-4-01	3.0
Date:	Friday, May 14, 2004	Sheet 31 of 39





EliteGroup Computer System. (G900 M/B)		
Title VGA M10 CONN.		
Size A	Document Number G900-1-4-01	Rev 3.0
Date: Friday, May 14, 2004	Sheet 33	of 39



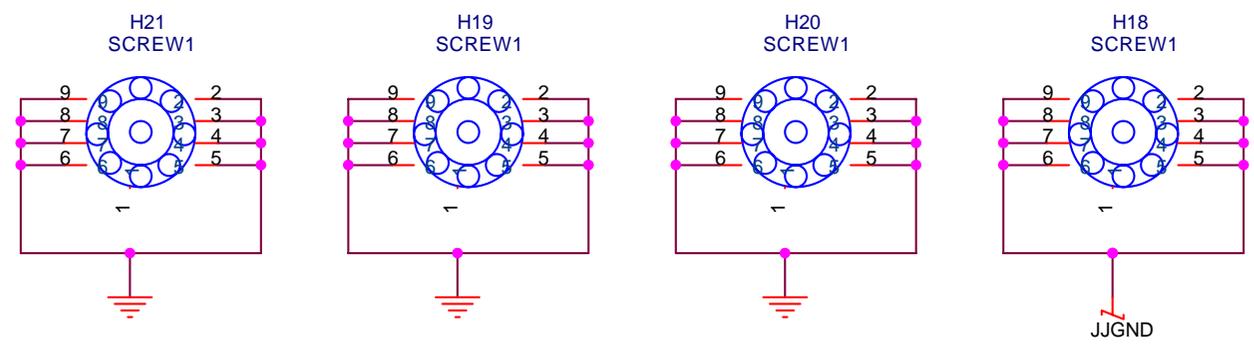
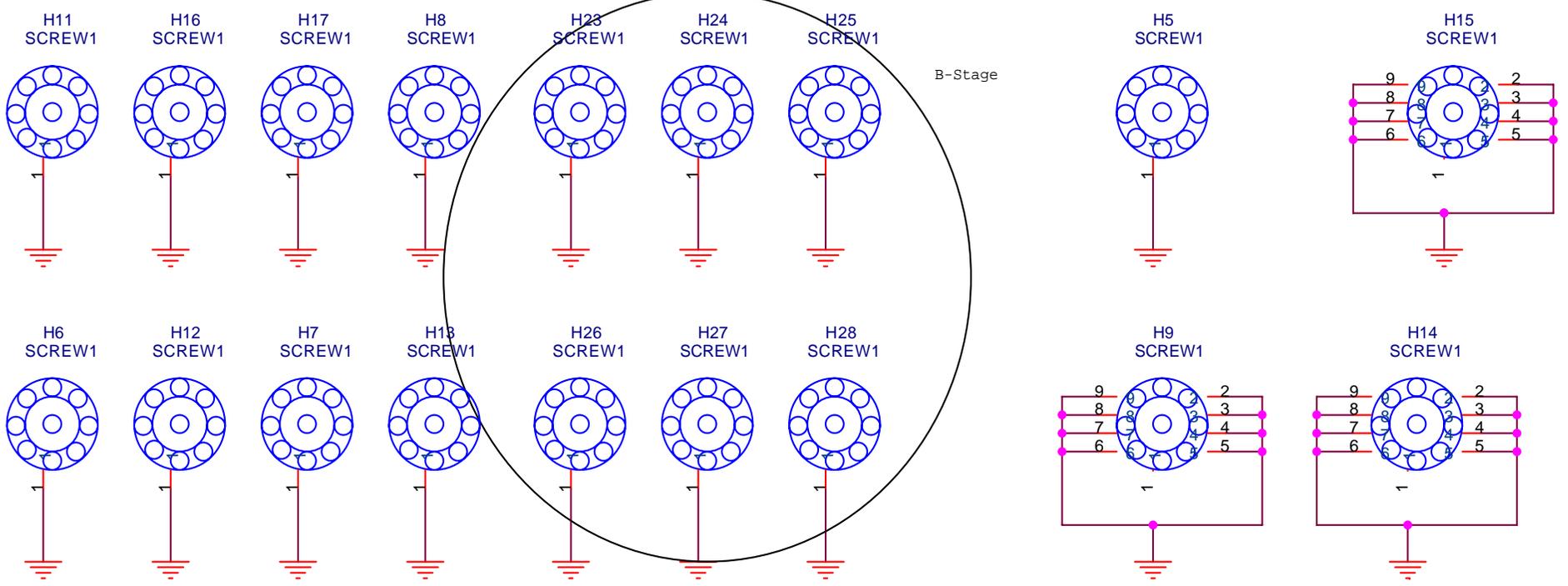
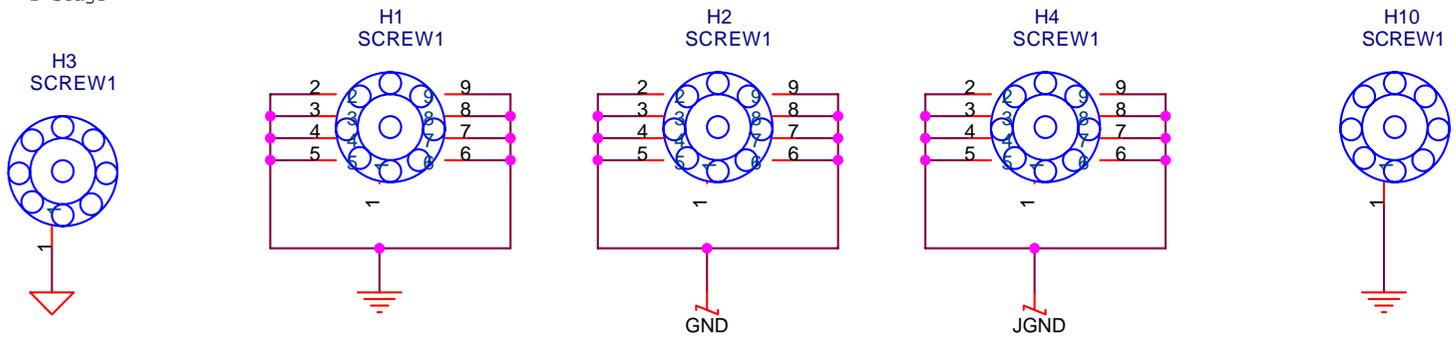








B-Stage



EliteGroup Computer System. (G900 M/B)		
Title HOLES		
Size A	Document Number G900-1-4-01	Rev 3.0
Date: Friday, May 14, 2004	Sheet 39	of 39