

**SOCKET 479**  
Dothan  
479 uFCPGA  
PAGE 2,3,4

HOST BUS

**NORTH BRIDGE**  
RC410MB  
PAGE 5,6,7,8,9

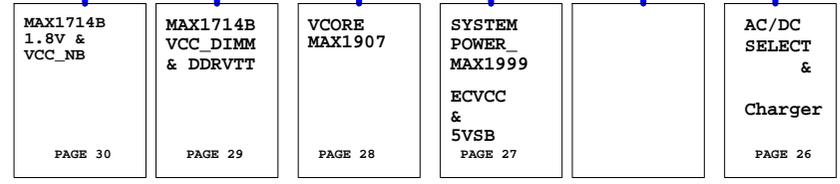
A-LINK

**SOUTH BRIDGE**  
SB450  
PAGE 15,16,17

LPC BUS

**K/B CONTROLLER**  
KB3910  
PAGE 18

**SYS POWER**



**CLK GENERATOR**  
ICS 951413  
PAGE 14

**POWER SEQUENCE**  
PAGE 31

**LCD DISPLAY**  
PAGE 13

**CRT OUT TV OUT**  
PAGE 12

**DDR2 DRAM**  
PAGE 10,11

**DDR2 DRAM**  
PAGE 10,11

**MDC**  
PAGE 25

**HDD**  
ATA66/100/133  
PAGE 24

**ODD**  
DVD/Combo/DVD Duel  
PAGE 24

**AC97 CODEC & AMPLIFIER**  
PAGE 23

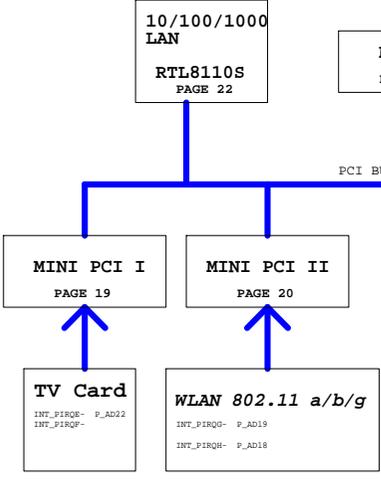
**USB2.0 x4**  
PAGE 22

**CARDREADER**  
PAGE 22

**SCREWHOLE HISTORY**  
PAGE 32

**SMBUS Table**

	SOURCE	PULL HIGH	NAME	WHERE TO USE
1	SB	3.3V	SMB_DAT2 SMB_CLK2	Clock , DDR
2	KBC P.16	ECVCC	SMB_DAT3 SMB_CLK3	For Battery
3	KBC P.16	ECVCC	SMB_DAT1 SMB_CLK1	For CPU_LM90,NB_LM90
4	N/B	3.3V	LVD8_CLK LVD8_DATA	For Panel SSID
5	N/B	3.3V	DAC_SCL DAC_SDAT	For CRT
6				



**SELECTOR MOS & AUX POWER GENERATOR**  
PAGE 29

**SMART FAN**  
PAGE 18

**TOUCH PAD & INTERNAL K/B**  
PAGE 18

**SB450 IRQ Table**

ICH-6	P_IRQ0- P_GNT0-	RTL8100B
P.13	P_REQ1- P_GNT1-	For Mini PCI (TV)
	P_REQ2- P_GNT2-	For Mini PCI (W/L)

**SB450 Interrupt Table**

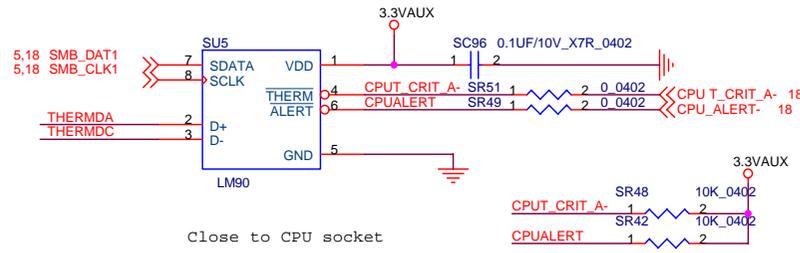
ICH-6	A	RTL8100B
P.13	B	MiniPCI (TV)
	C	
	D	MiniPCI (W/L)
	E	
	F	
	G	
	H	

<b>PAGE</b>	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
<b>REV</b>	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
<b>DATE</b>	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005
<b>PAGE</b>	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38
<b>REV</b>	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
<b>DATE</b>	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005	9/29/2005

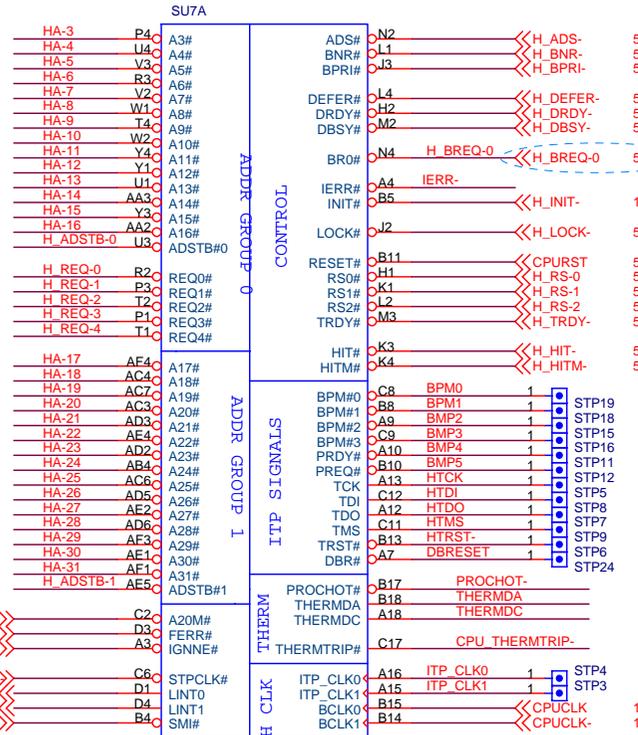
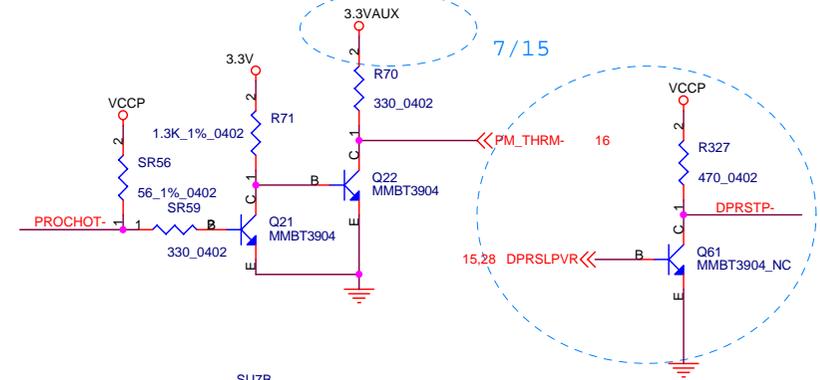
**P/N: 15-F67-012000**



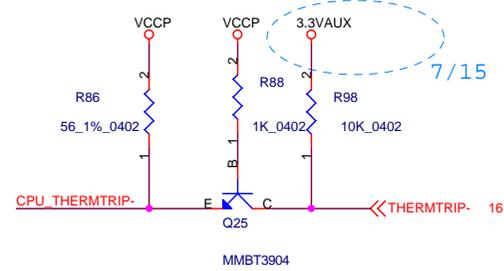
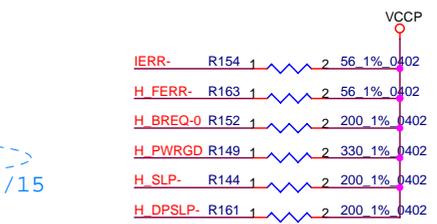
Cap close to thermal sensor



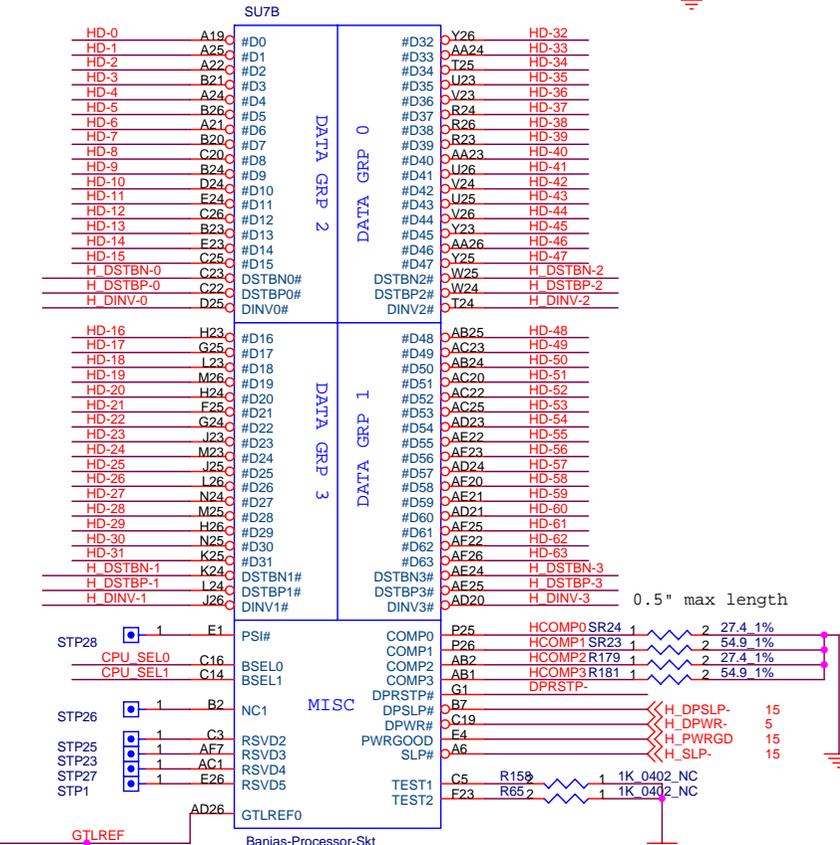
Close to CPU socket



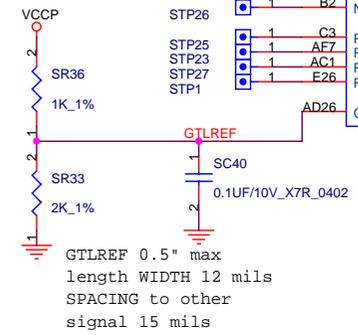
Banias-Processor-Skt



7/15

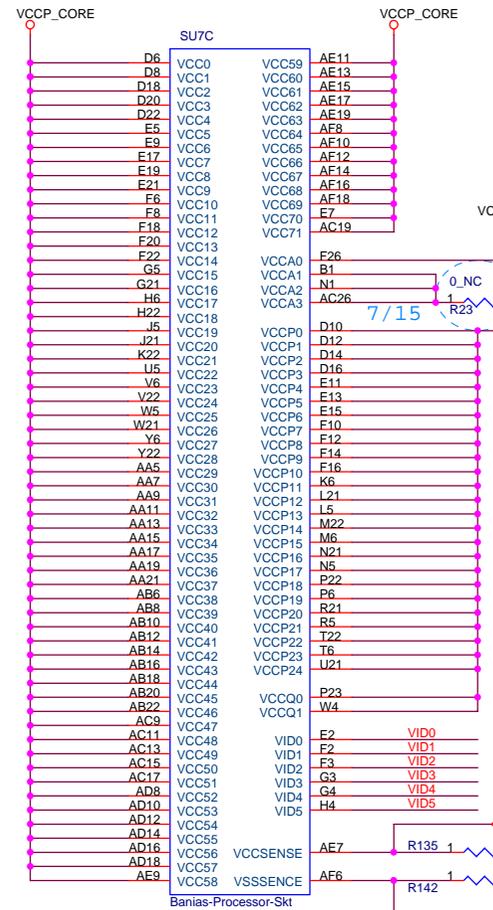
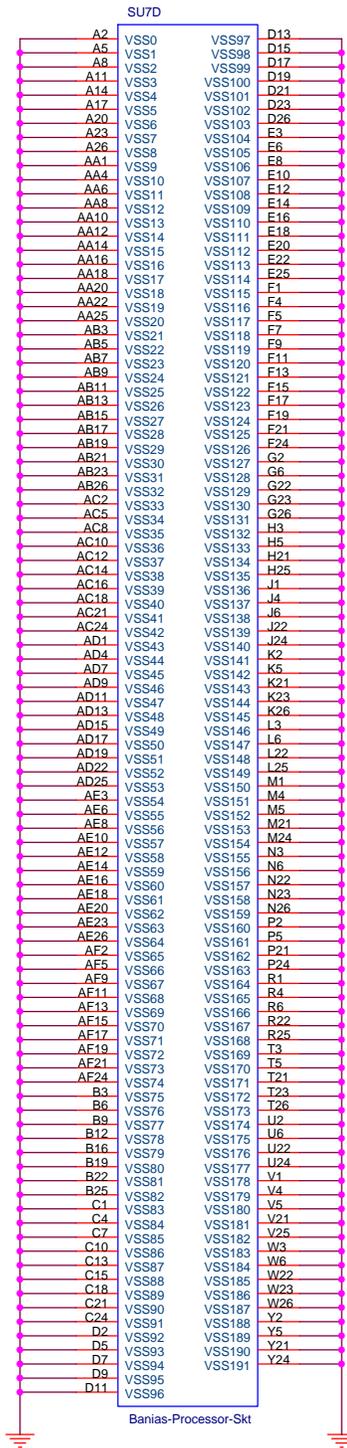


Banias-Processor-Skt



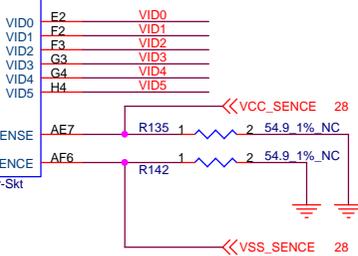
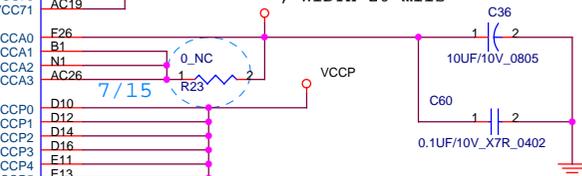
GTLREF 0.5" max length WIDTH 12 mils SPACING to other signal 15 mils

Title		
<b>G332 CPU I/O</b>		
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VCCP\_CORE\_27A

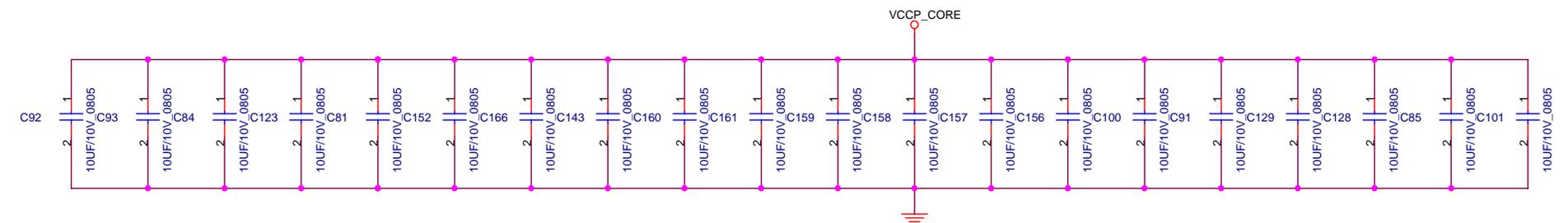
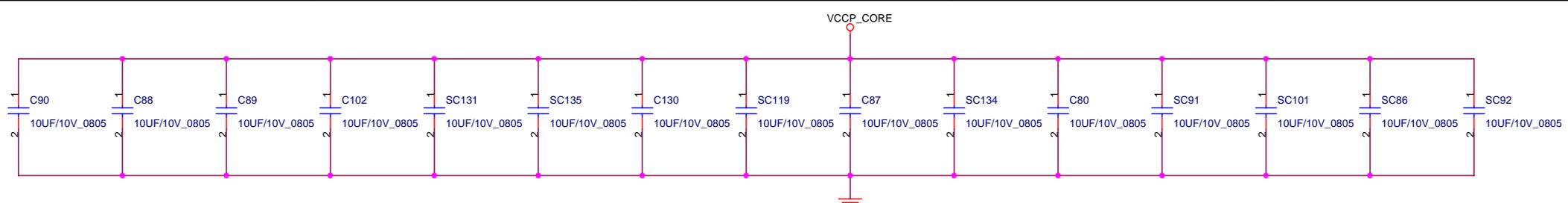
0.1uf and 10uf of a pair capacitors, WIDTH 20 mils



R135, R142 Close to CPU

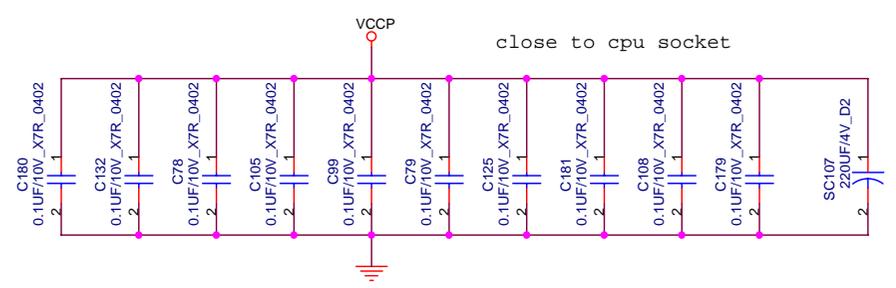
VID[0..5] <<< VID[0..5]

Title		
G332 CPU POWER		
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	G332-1-4-01	2.0
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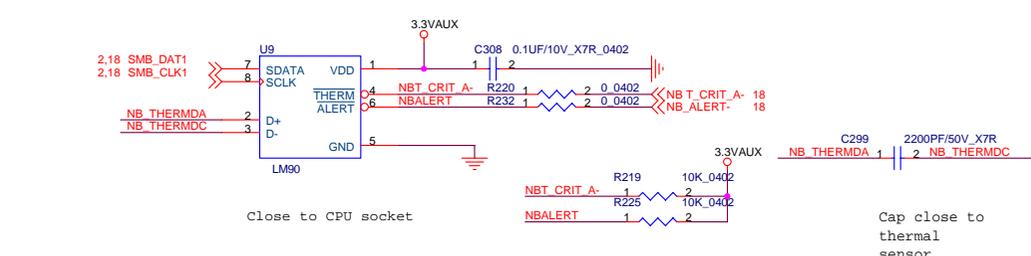
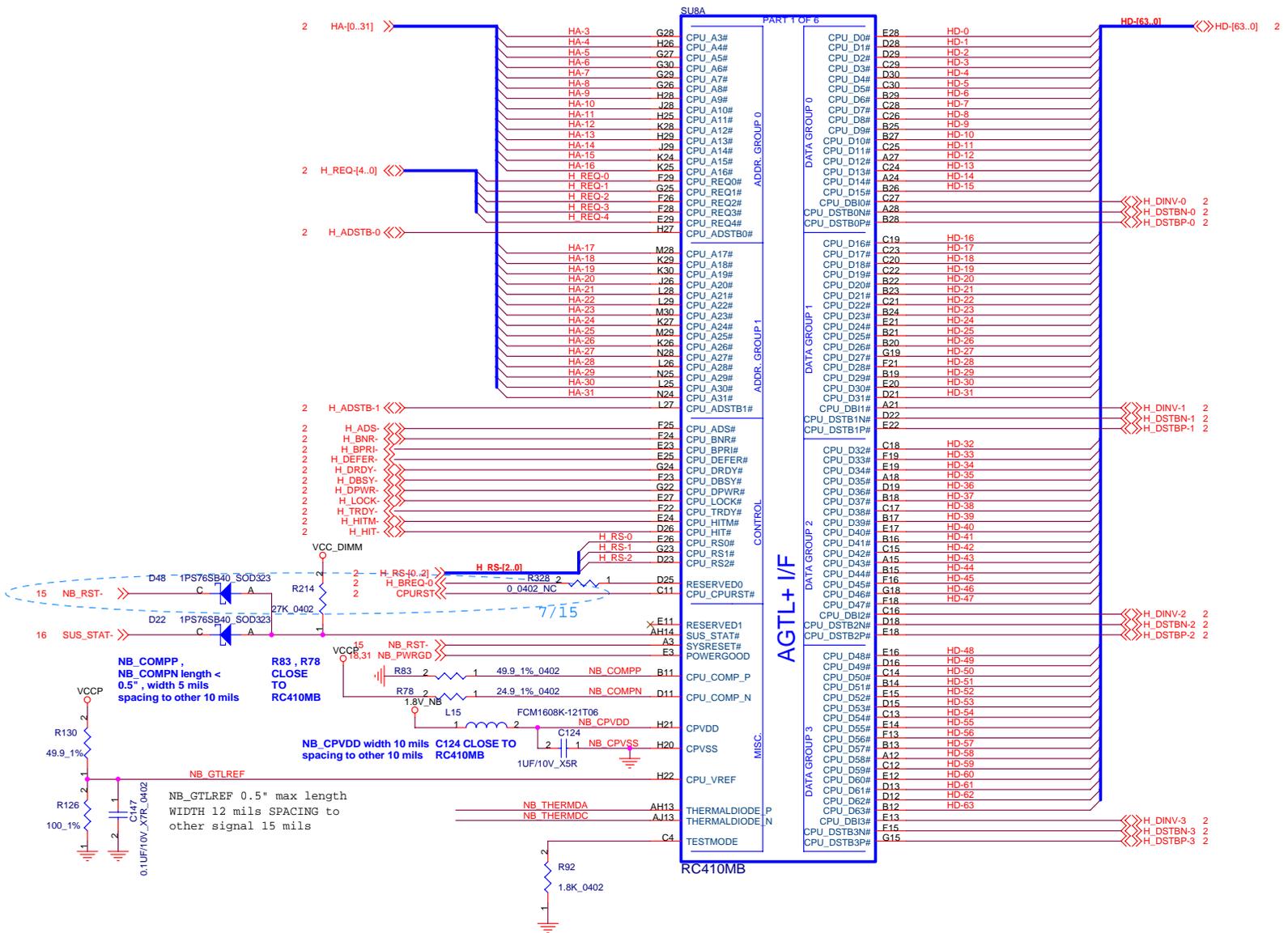


**IMVP4 VID TABLE**

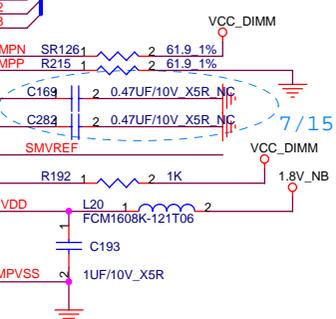
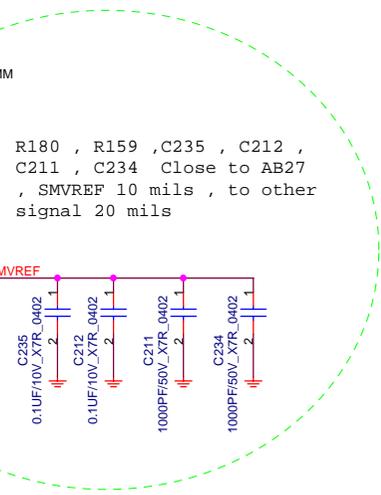
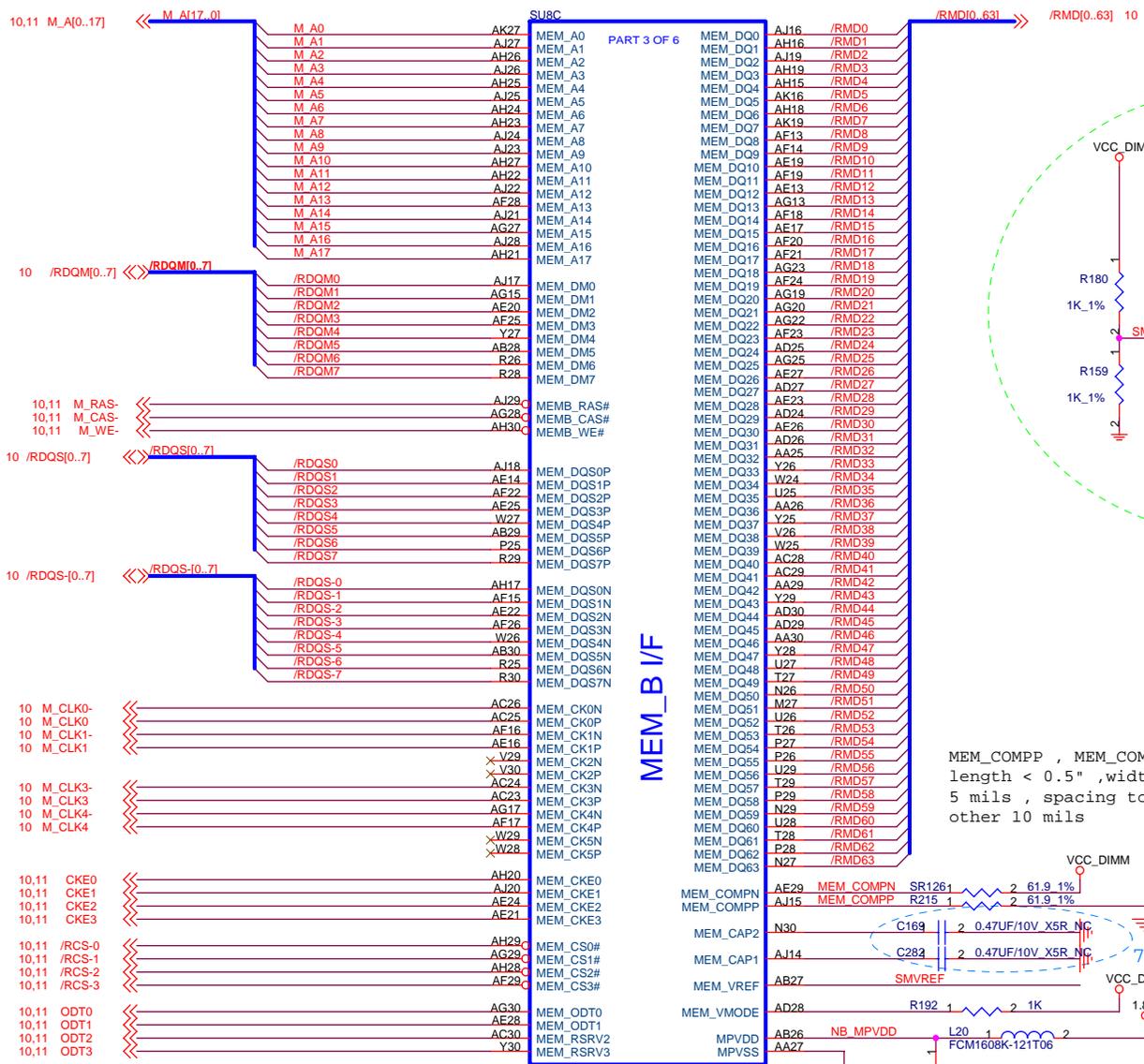
VID[5..0]	Voltage	VID[5..0]	Voltage
000000	1.708V	100000	1.196V
000001	1.692V	100001	1.180V
000010	1.676V	100010	1.164V
000011	1.660V	100011	1.148V
000100	1.644V	100100	1.132V
000101	1.628V	100101	1.116V
000110	1.612V	100110	1.100V
000111	1.596V	100111	1.084V
001000	1.580V	101000	1.068V
001001	1.564V	101001	1.052V
001010	1.548V	101010	1.036V
001011	1.532V	101011	1.020V
001100	1.516V	101100	1.004V
001101	1.500V	101101	0.988V
001110	1.484V	101110	0.972V
001111	1.468V	101111	0.956V
010000	1.452V	110000	0.940V
010001	1.436V	110001	0.924V
010010	1.420V	110010	0.908V
010011	1.404V	110011	0.892V
010100	1.388V	110100	0.876V
010101	1.372V	110101	0.860V
010110	1.356V	110110	0.844V
010111	1.340V	110111	0.828V
011000	1.324V	111000	0.812V
011001	1.308V	111001	0.796V
011010	1.292V	111010	0.780V
011011	1.276V	111011	0.764V
011100	1.260V	111100	0.748V
011101	1.244V	111101	0.732V
011110	1.228V	111110	0.716V
011111	1.212V	111111	0.700V



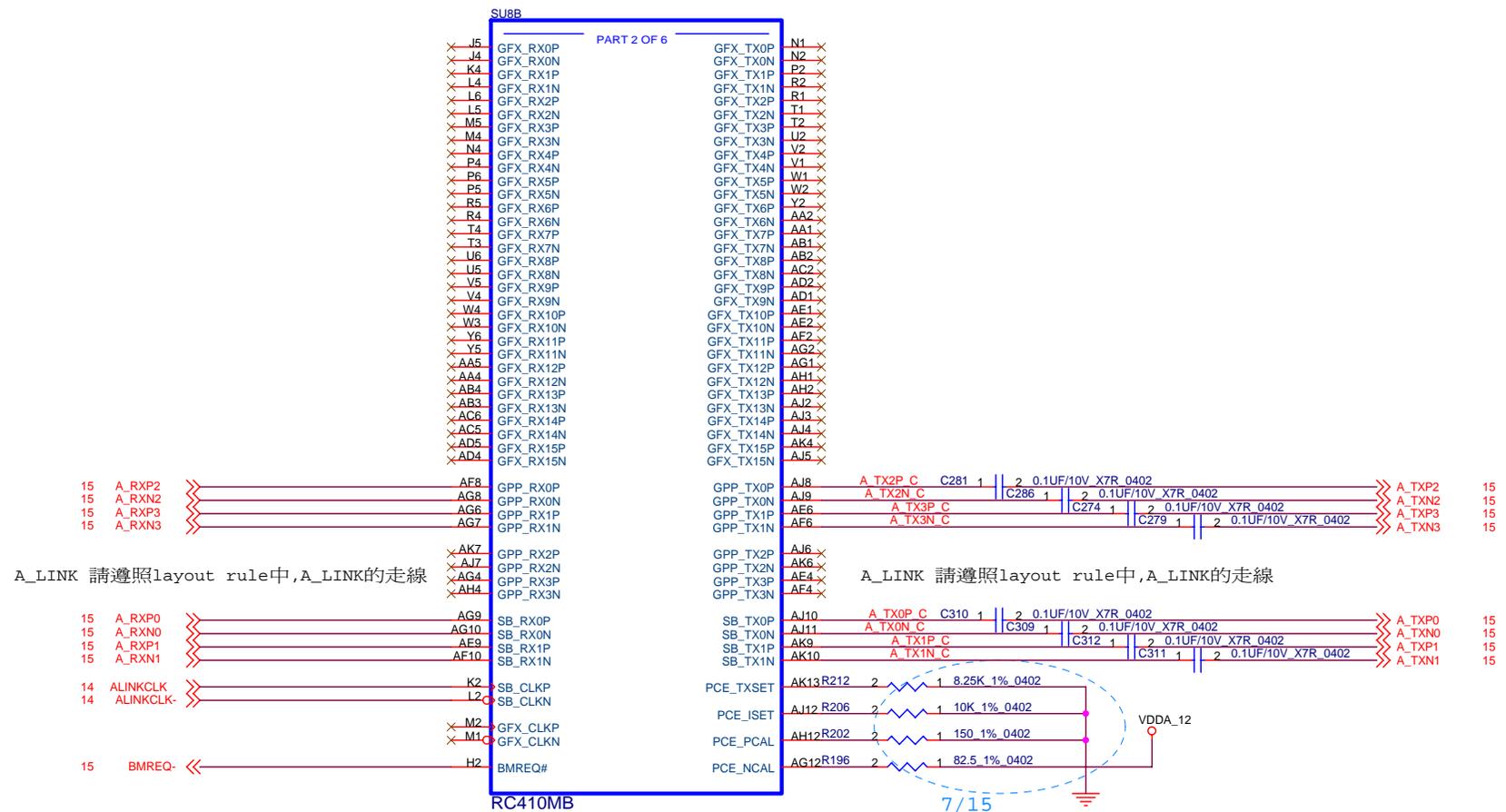
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<b>G332 CPU VCCP_CORE CAP</b>		
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G332 NB_CPU I/O		
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Title		
G332 NB_MEM_I/O		
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G332 NB_SB_LINK		
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C127 close to G4,G5 NB\_VDDR3 width 10 mils , spacing to other signals 10 mils

C63 close to C9 NB\_AVDD width 10 mils , spacing to other signals 10 mils

C70 close to D8 1.8V\_NB width 10 mils , spacing to other signals 10 mils

C69 close to B8 AVDD1 width 10 mils , spacing to other signals 10 mils

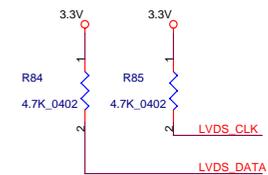
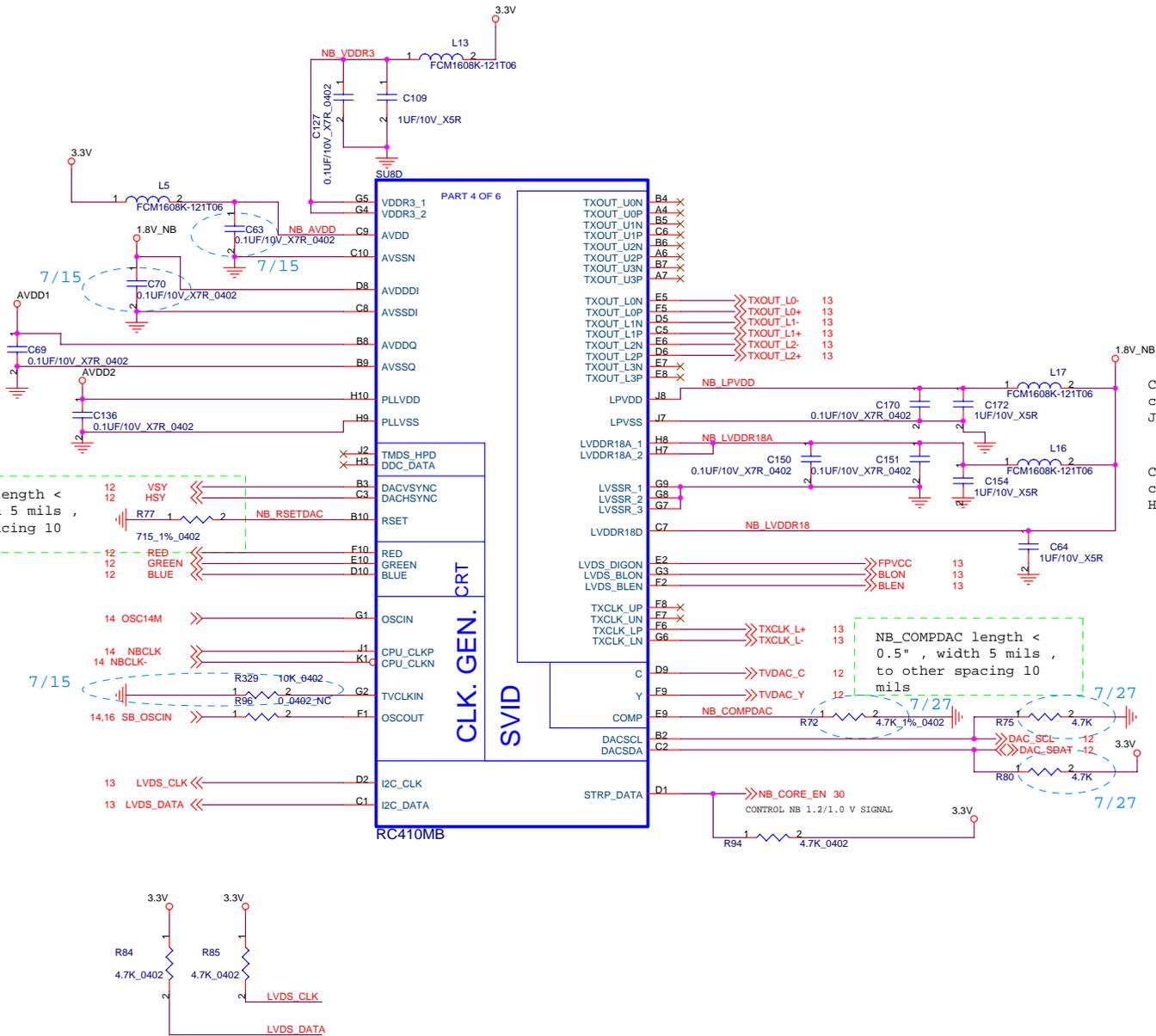
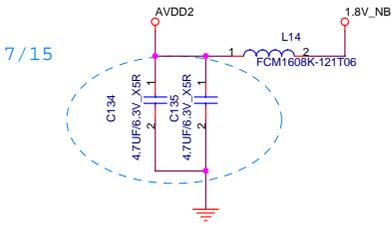
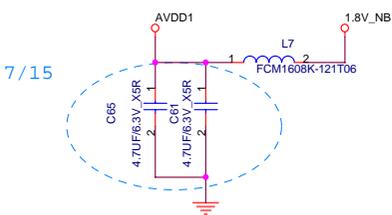
C136 close to H10 AVDD2 width 10 mils , spacing to other signals 10 mils

NB\_RSETDAC length < 0.5" , width 5 mils , to other spacing 10 mils

NB\_COMPDAC length < 0.5" , width 5 mils , to other spacing 10 mils

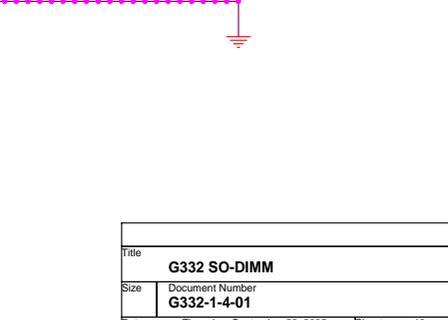
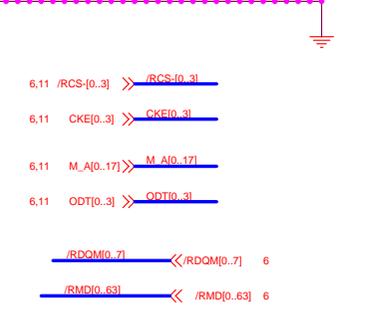
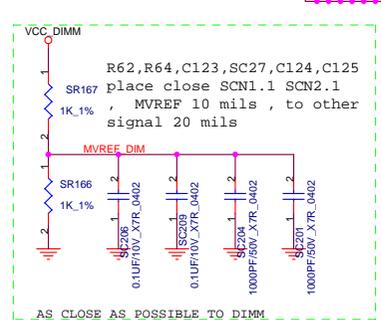
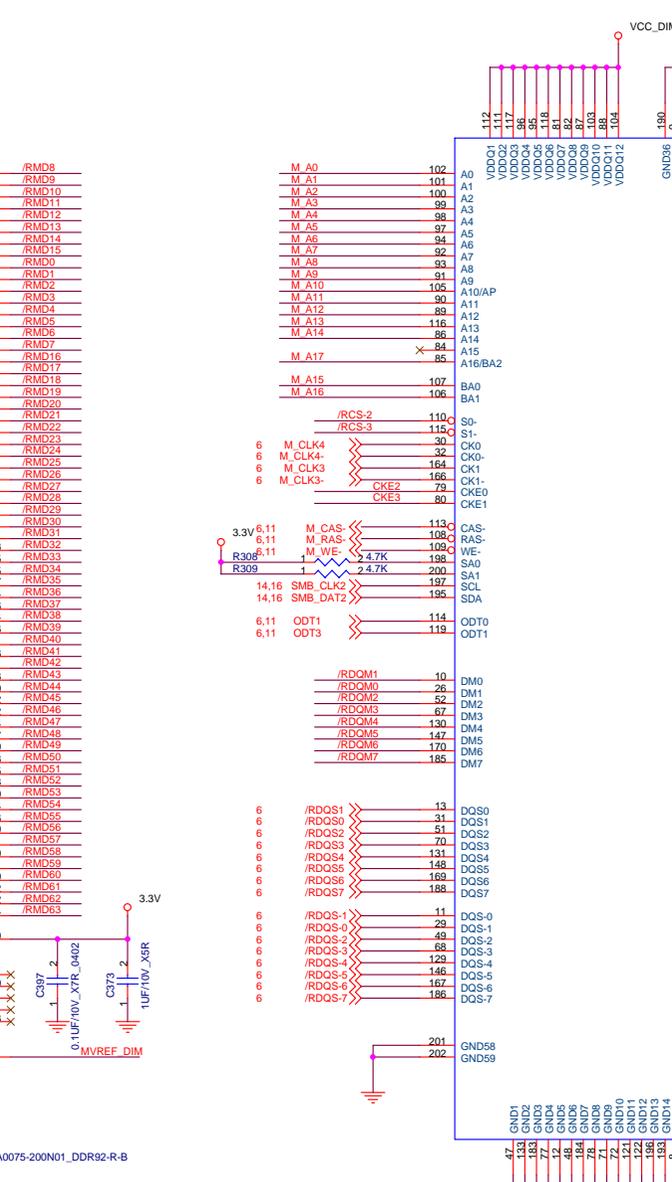
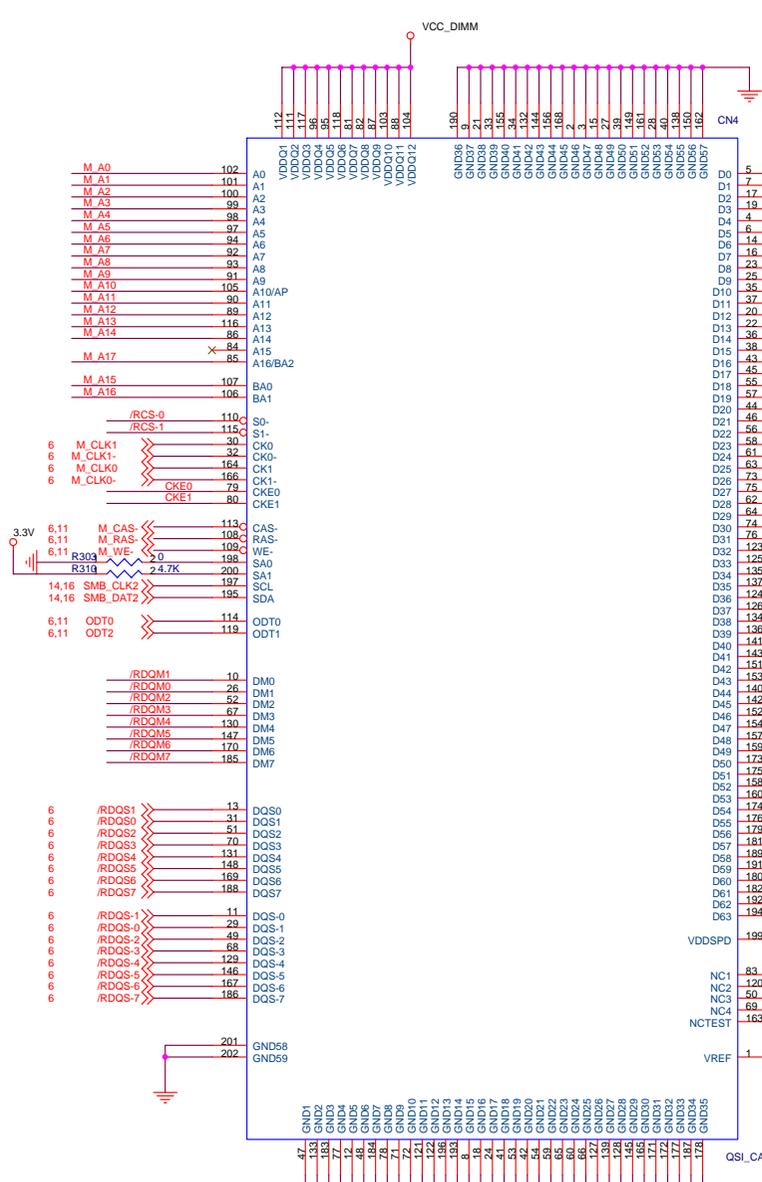
C170 close to J8 NB\_LPVDV width 10 mils , spacing to other signals 10 mils

C150,C151 close to H7,H8 NB\_LVDDR18A width 10 mils , spacing to other signals 10 mils

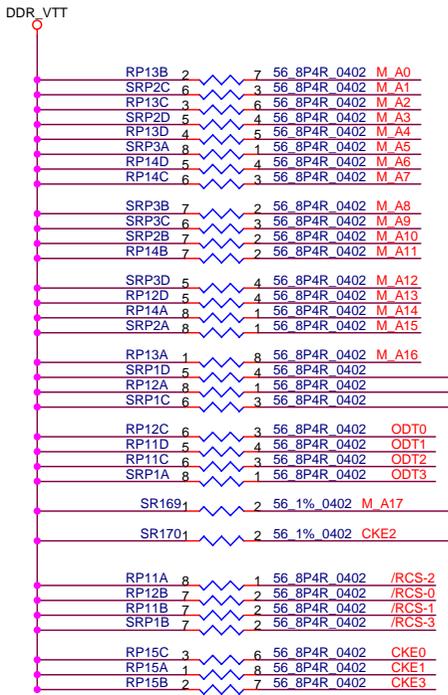


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G332 NB_VEDIO_LINK		
Size	Document Number	Rev
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Title		
G332 SO-DIMM		
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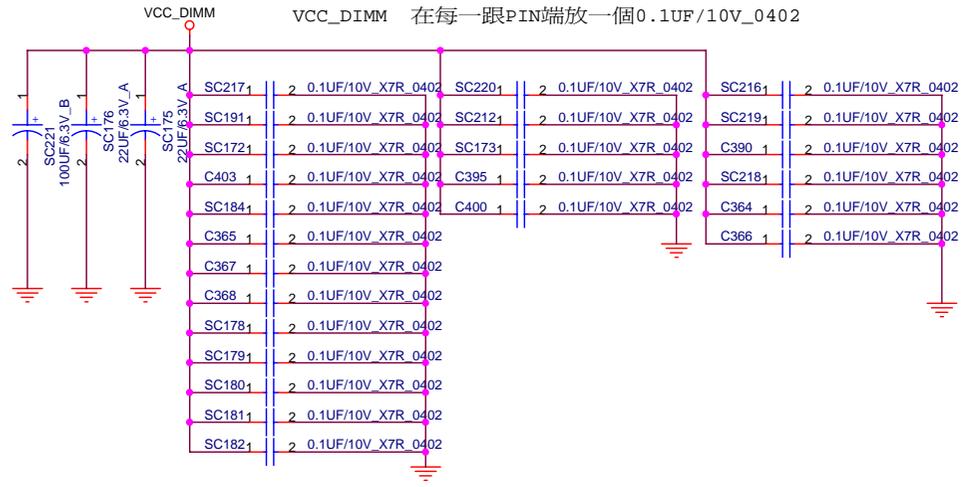


M\_WE 6,10  
 M\_RAS 6,10  
 M\_CAS 6,10

/RCS-10..3 << /RCS-[0..3] 6,10  
ODT0..3 << ODT[0..3] 6,10  
M\_A0..17 << M\_A[0..17] 6,10  
/RCS-10..3 << /RCS-[0..3] 6,10  
CKE0..3 << CKE[0..3] 6,10

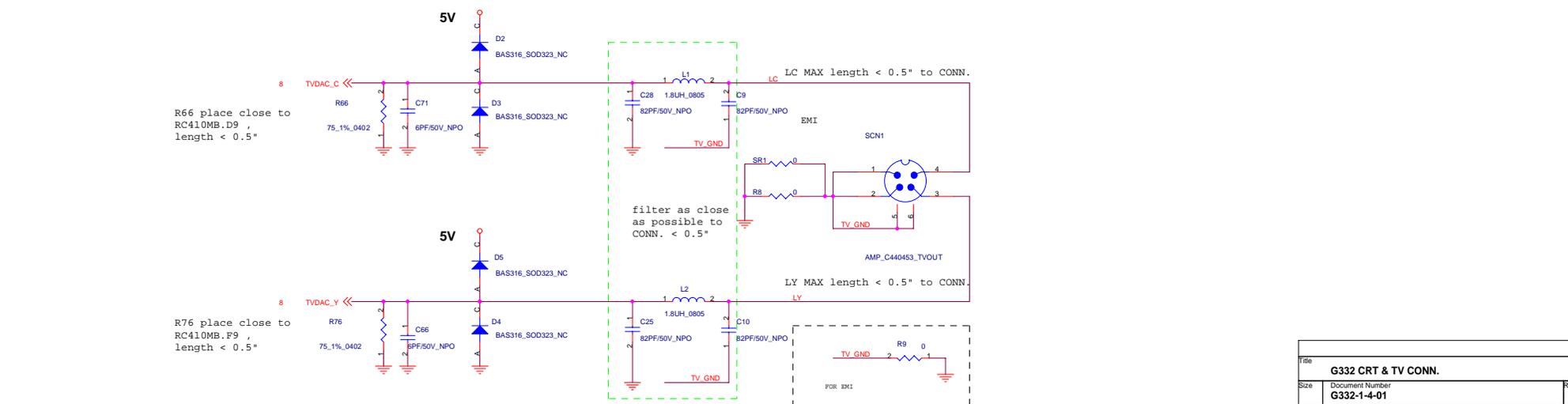
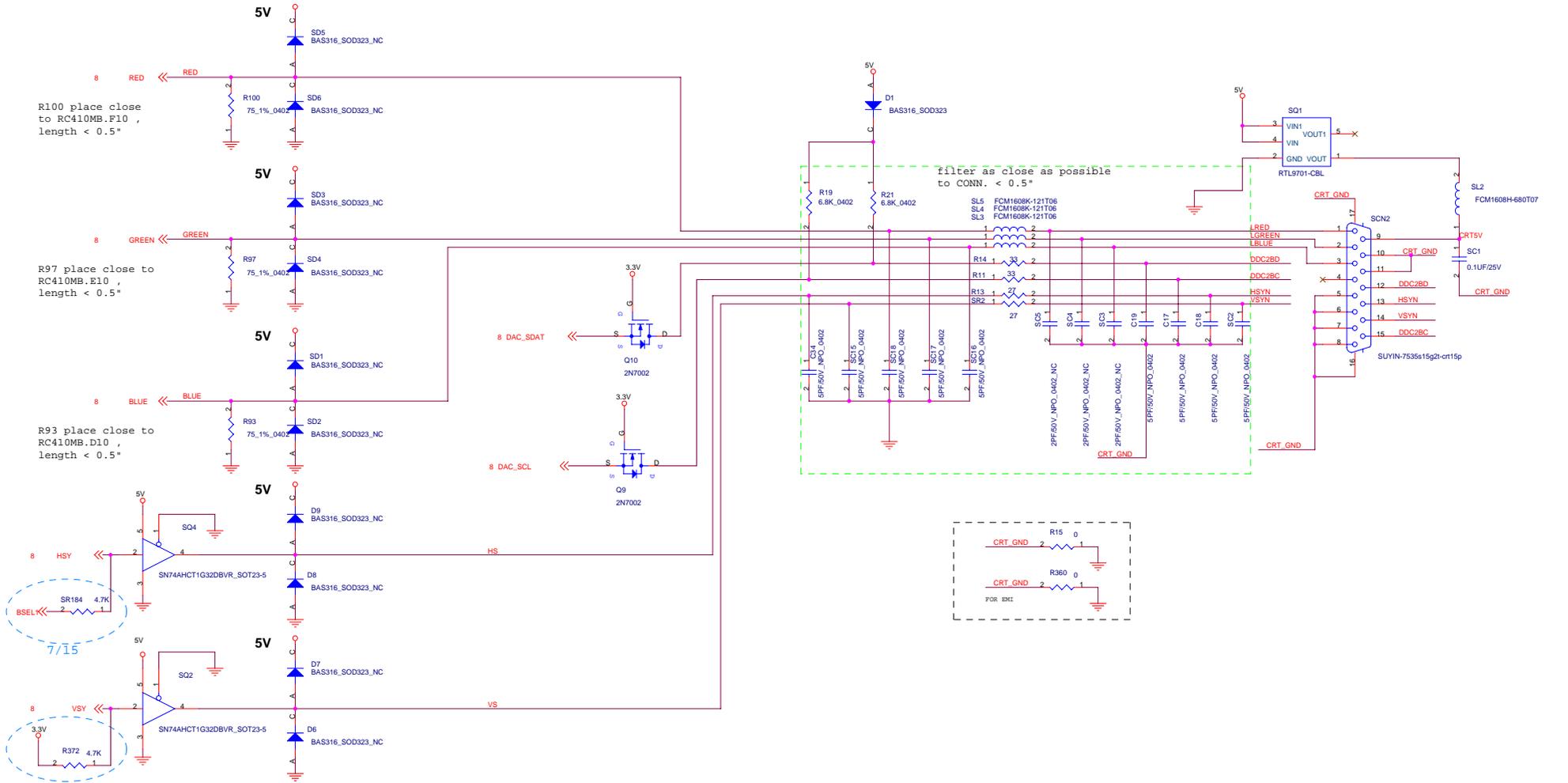


DDR\_VTT 在每兩個PULL HIGH電阻端放一個0.1UF/10V\_0402

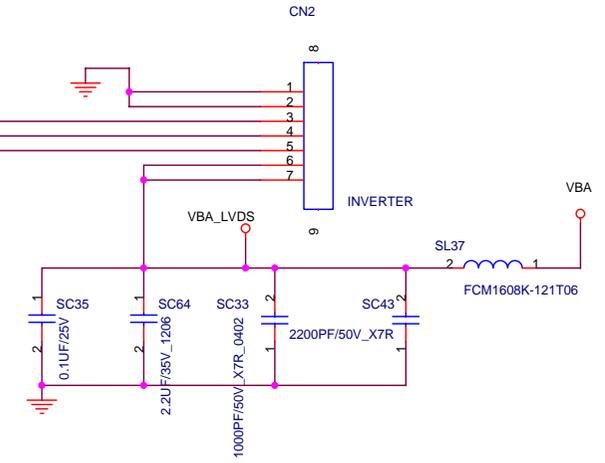
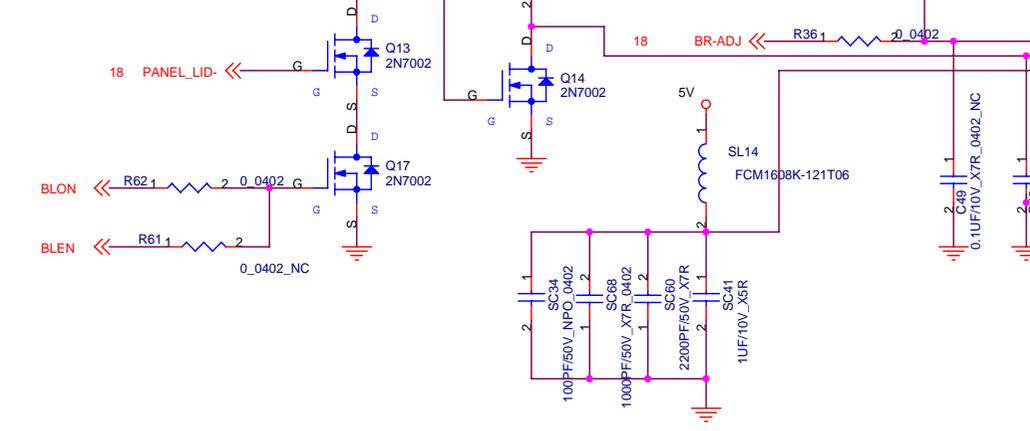
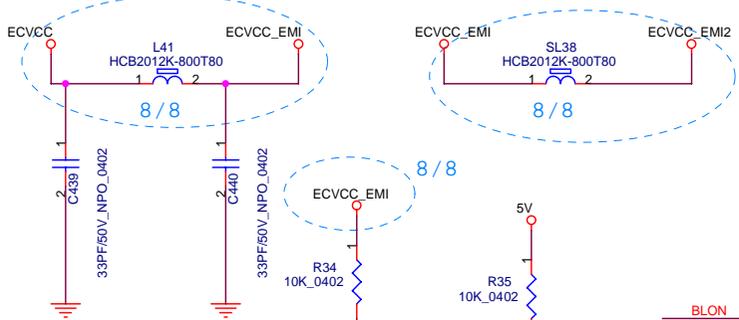
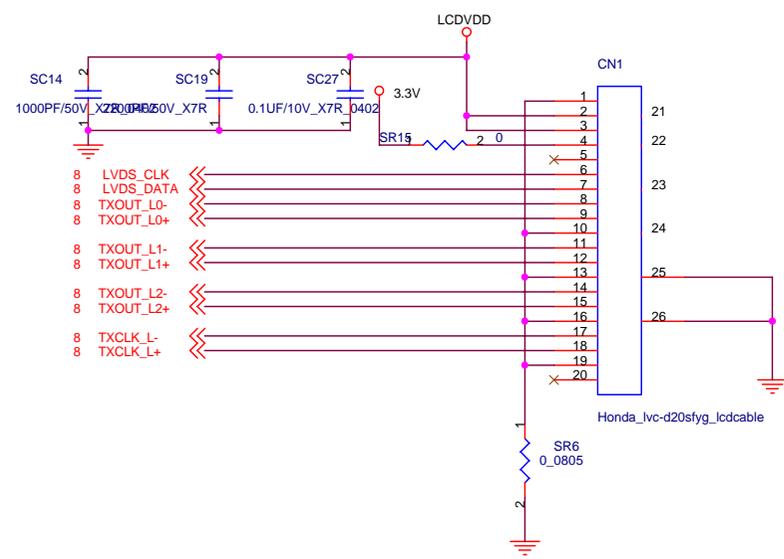
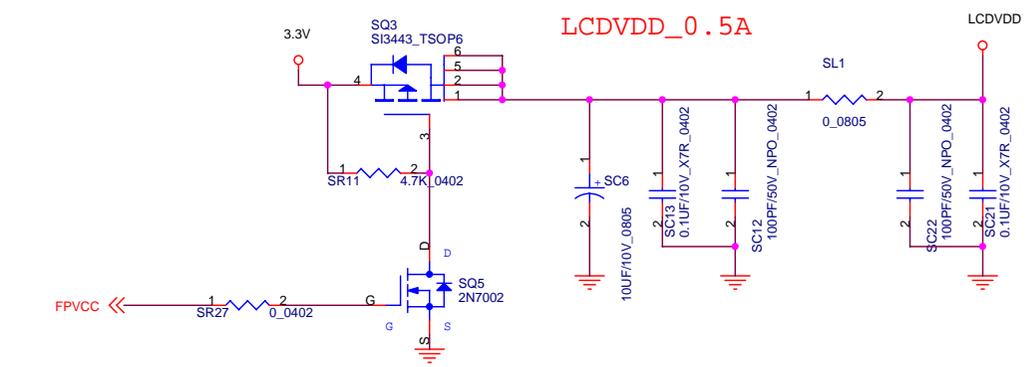


VCC\_DIMM 在每一跟PIN端放一個0.1UF/10V\_0402

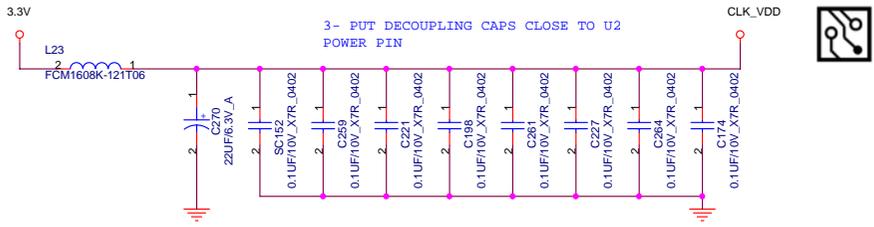
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G332 DDR_VTT CAP		
Size	Document Number	Rev
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File	<b>G332 CRT &amp; TV CONN.</b>	
Size	Document Number	Rev
	<b>G332-1-4-01</b>	<b>2.0</b>
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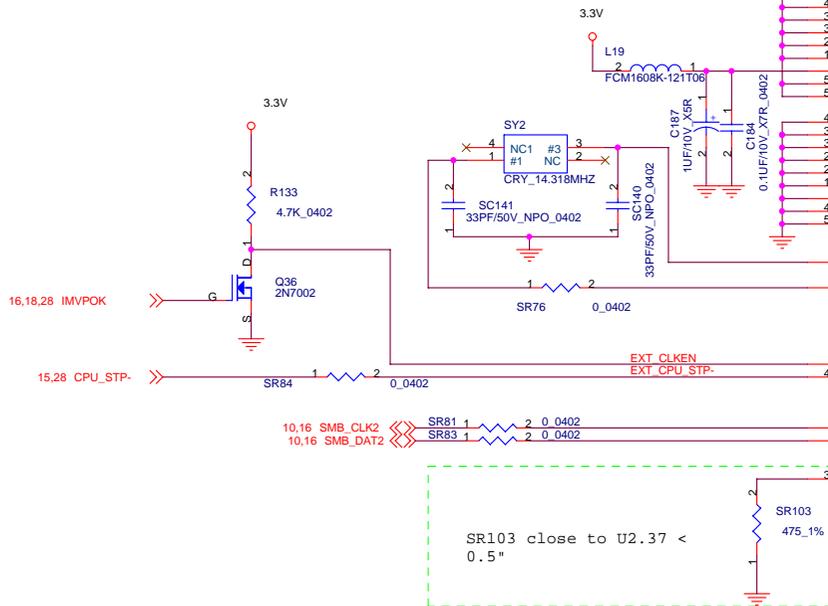


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G332 LVDS CONN.		
Size	Document Number	Rev
	G332-1-4-01	2.0
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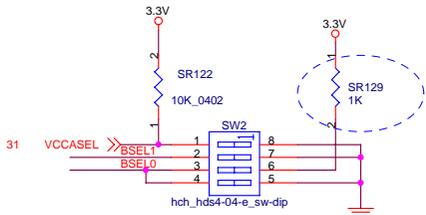
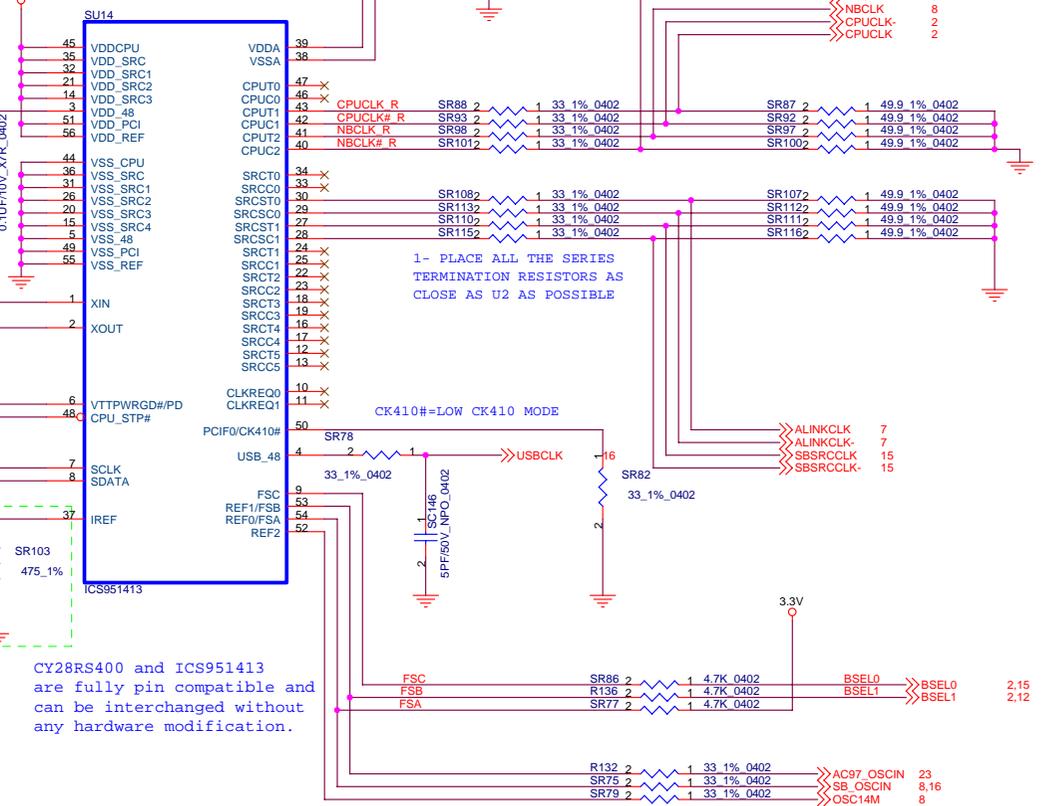
C246 close to U2.39, CLK\_VDDA wide 10 mils, to other signal 10 mils

2- ROUTE ALL CPUCCLK/-, NBCLK/- AND ALINKCLK/-, NBSRCCLK/-, SBSRCCLK/- AS DIFFERENT PAIR RULE



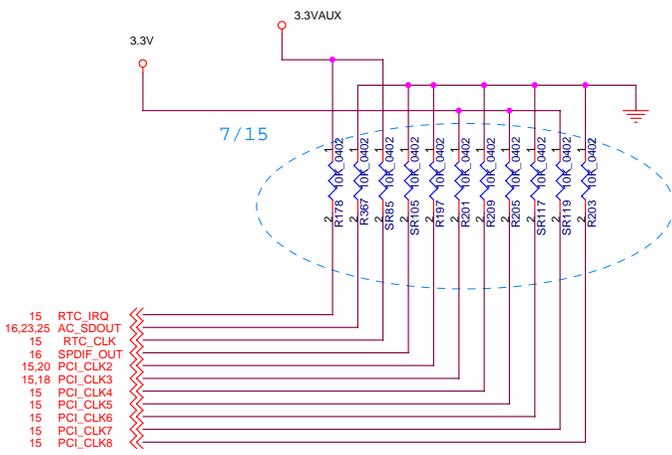
CY28RS400 and ICS951413 are fully pin compatible and can be interchanged without any hardware modification.

FSC	FSB	FSA	FSB
1	0	1	100
0	0	1	133









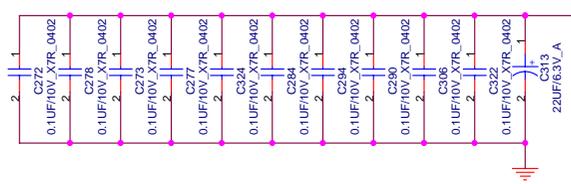
- 15 RTC\_IRQ
- 16,23,25 AC\_SDOUT
- 15 RTC\_CLK
- 16 SPDIF\_OUT
- 15,20 PCI\_CLK2
- 15,18 PCI\_CLK3
- 15 PCI\_CLK4
- 15 PCI\_CLK5
- 15 PCI\_CLK6
- 15 PCI\_CLK7
- 15 PCI\_CLK8

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Place all the decoupling caps on this sheet close to SB.



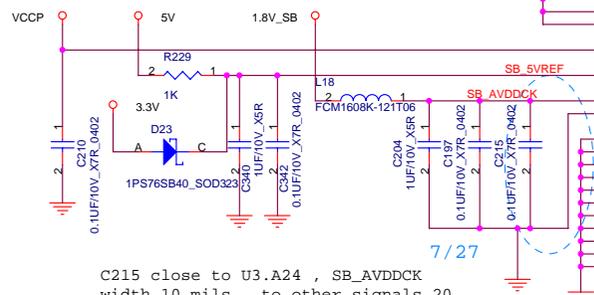
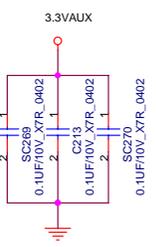
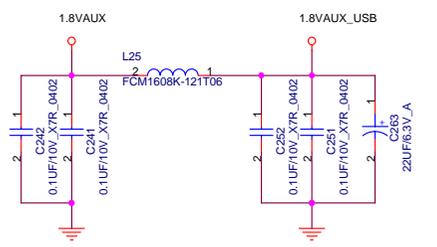
Place all the decoupling caps on this sheet close to SB.



C96 , C213 , C185 close U3 3.3VAUX pin

C241 , C242 close to U3.E9 , U3.E10 , U3.E20 , U3.E21 , 1.8VAUX to U3 trace width 20 mils , to other signals 20 mils

C251 , C252 close to U3.E13 , U3.E14 , U3.E16 , U3.E17 , 1.8VAUX\_USB to U3 trace width 20 mils , to other signals 20 mils



C215 close to U3.A24 , SB\_AVDDCK width 10 mils , to other signals 20 mils

C342 close to U3.AG6 , SB\_5VREF width 10 mils , to other signals 20 mils

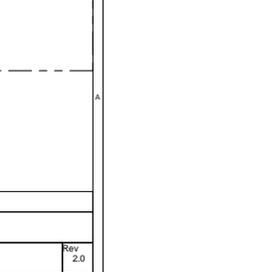
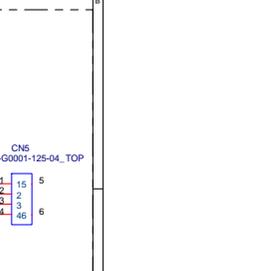
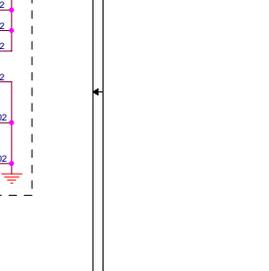
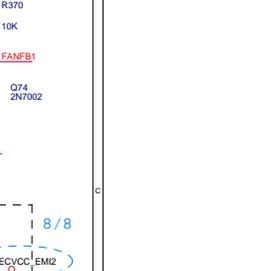
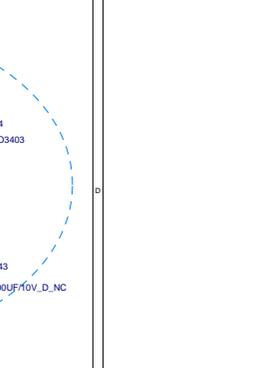
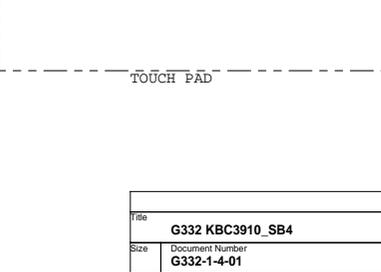
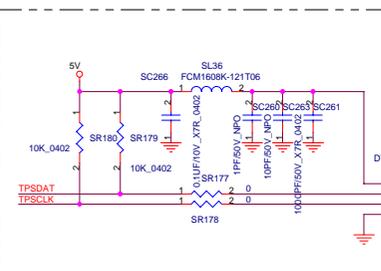
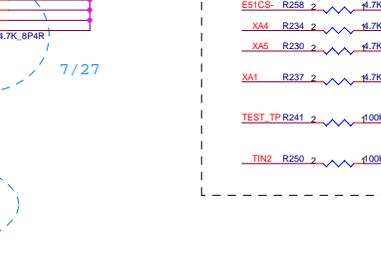
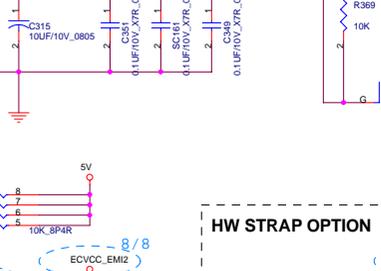
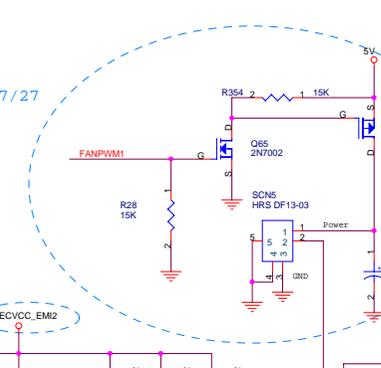
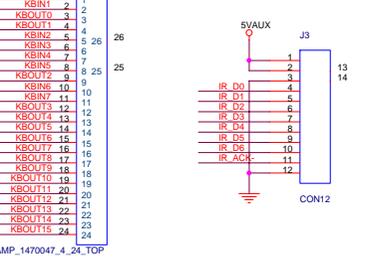
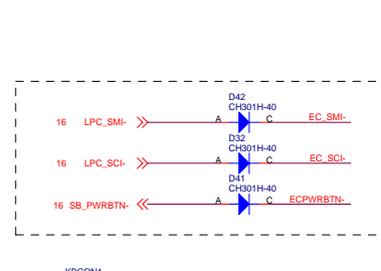
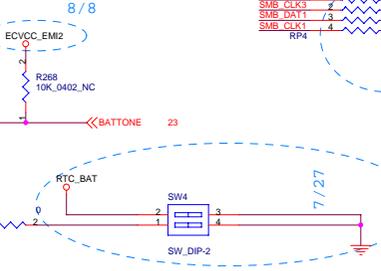
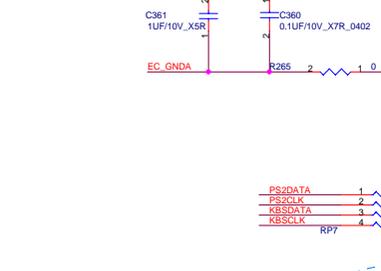
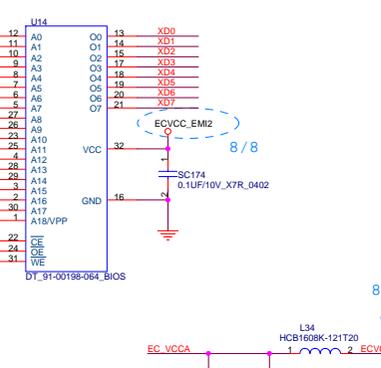
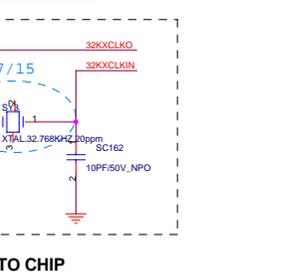
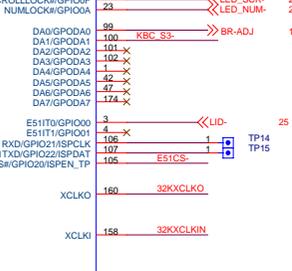
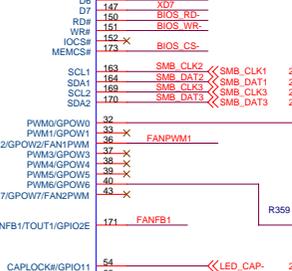
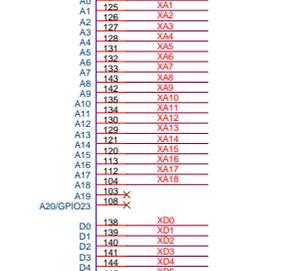
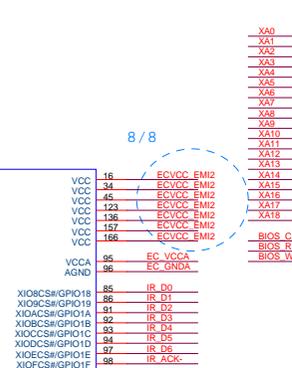
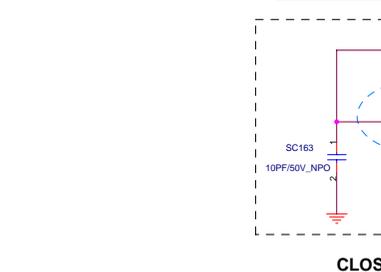
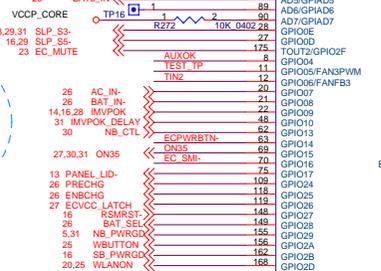
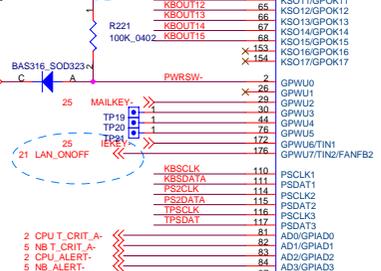
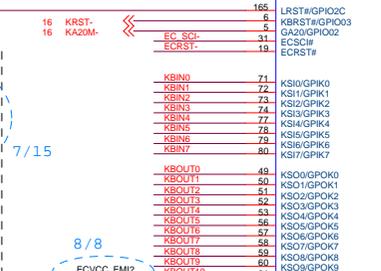
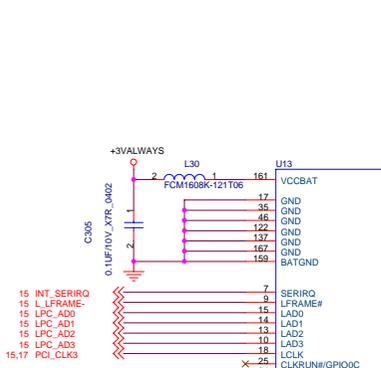
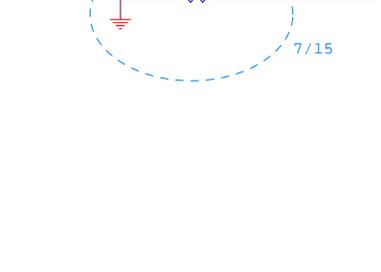
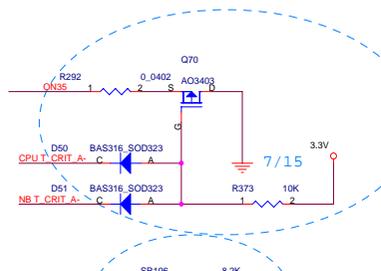
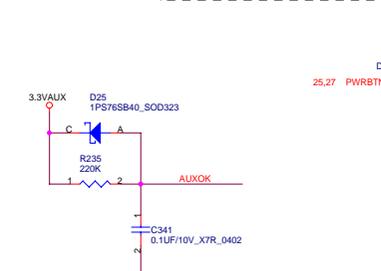
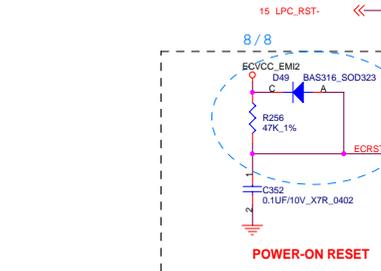
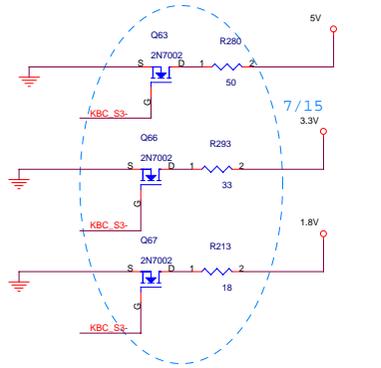
C210 close to U3.C30 , VCCP to SB width 10 mils , to other signals 20 mils

7/27

SB400 SB Part 3 of 4		
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D30	VDDQ_2	VSS_13
E24	VDDQ_3	VSS_14
E25	VDDQ_4	VSS_15
K1	VDDQ_5	VSS_16
K5	VDDQ_6	VSS_17
N5	VDDQ_7	VSS_18
N6	VDDQ_8	VSS_19
P5	VDDQ_9	VSS_20
E1	VDDQ_10	VSS_21
U5	VDDQ_11	VSS_22
U26	VDDQ_12	VSS_23
U30	VDDQ_13	VSS_24
V5	VDDQ_14	VSS_25
V26	VDDQ_15	VSS_26
Y1	VDDQ_16	VSS_27
Y26	VDDQ_17	VSS_28
AA5	VDDQ_18	VSS_29
AA26	VDDQ_19	VSS_30
AB5	VDDQ_20	VSS_31
AC30	VDDQ_21	VSS_32
AD5	VDDQ_22	VSS_33
AD26	VDDQ_23	VSS_34
AE1	VDDQ_24	VSS_35
AE5	VDDQ_25	VSS_36
AE26	VDDQ_26	VSS_37
AF6	VDDQ_27	VSS_38
AF7	VDDQ_28	VSS_39
AF24	VDDQ_29	VSS_40
AF26	VDDQ_30	VSS_41
AK1	VDDQ_31	VSS_42
AK4	VDDQ_32	VSS_43
AK26	VDDQ_33	VSS_44
AK30	VDDQ_34	VSS_45
M12	VDD_1	VSS_46
M13	VDD_2	VSS_47
M18	VDD_3	VSS_48
M19	VDD_4	VSS_49
N12	VDD_5	VSS_50
N13	VDD_6	VSS_51
N18	VDD_7	VSS_52
N19	VDD_8	VSS_53
V12	VDD_9	VSS_54
V13	VDD_10	VSS_55
V18	VDD_11	VSS_56
V19	VDD_12	VSS_57
W12	VDD_13	VSS_58
W13	VDD_14	VSS_59
W18	VDD_15	VSS_60
W19	VDD_16	VSS_61
A3	S5_3.3V_1	VSS_62
A7	S5_3.3V_2	VSS_63
E6	S5_3.3V_3	VSS_64
E7	S5_3.3V_4	VSS_65
E1	S5_3.3V_5	VSS_66
F5	S5_3.3V_6	VSS_67
E9	S5_1.8V_1	VSS_68
E10	S5_1.8V_2	VSS_69
E20	S5_1.8V_3	VSS_70
E21	S5_1.8V_4	VSS_71
E13	USB_PHY_1.8V_1	VSS_72
E14	USB_PHY_1.8V_2	VSS_73
E16	USB_PHY_1.8V_3	VSS_74
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C30	CPU_PWR	VSS_76
AG6	V5_VREF	VSS_77
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B24	AVSSCK	VSS_79
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A8	VSS_2	VSS_81
A29	VSS_3	VSS_82
B28	VSS_4	VSS_83
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E8	VSS_7	VSS_86
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E15	VSS_10	VSS_89
E18	VSS_11	VSS_90
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S840	VSS_16	VSS_95
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S840	VSS_19	VSS_98

POWER

DNI

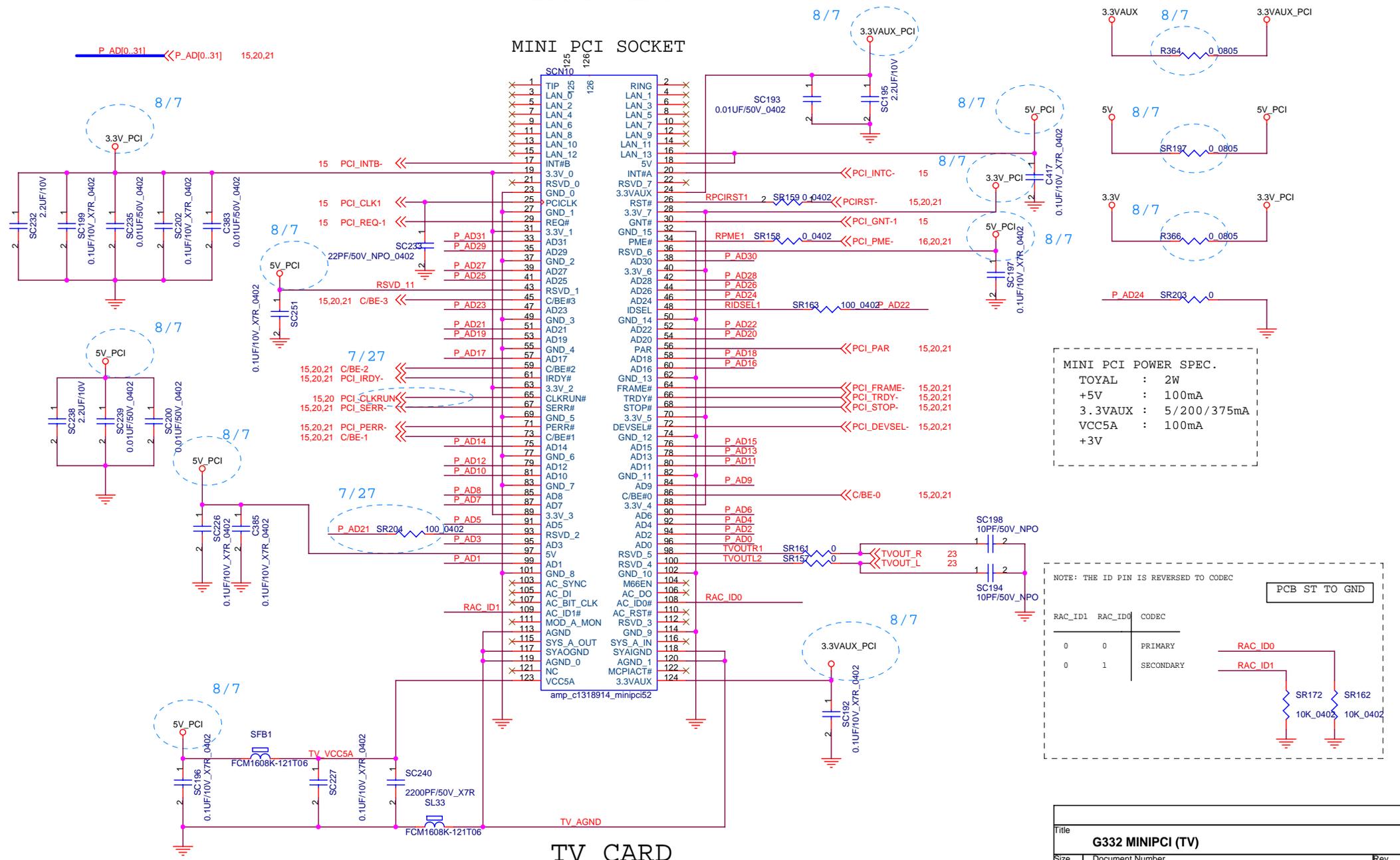


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MINI PCI SOCKET 1

MINI PCI SOCKET



MINI PCI POWER SPEC.  
 TOYAL : 2W  
 +5V : 100mA  
 3.3VAUX : 5/200/375mA  
 VCC5A : 100mA  
 +3V

NOTE: THE ID PIN IS REVERSED TO CODEC

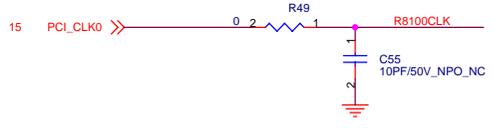
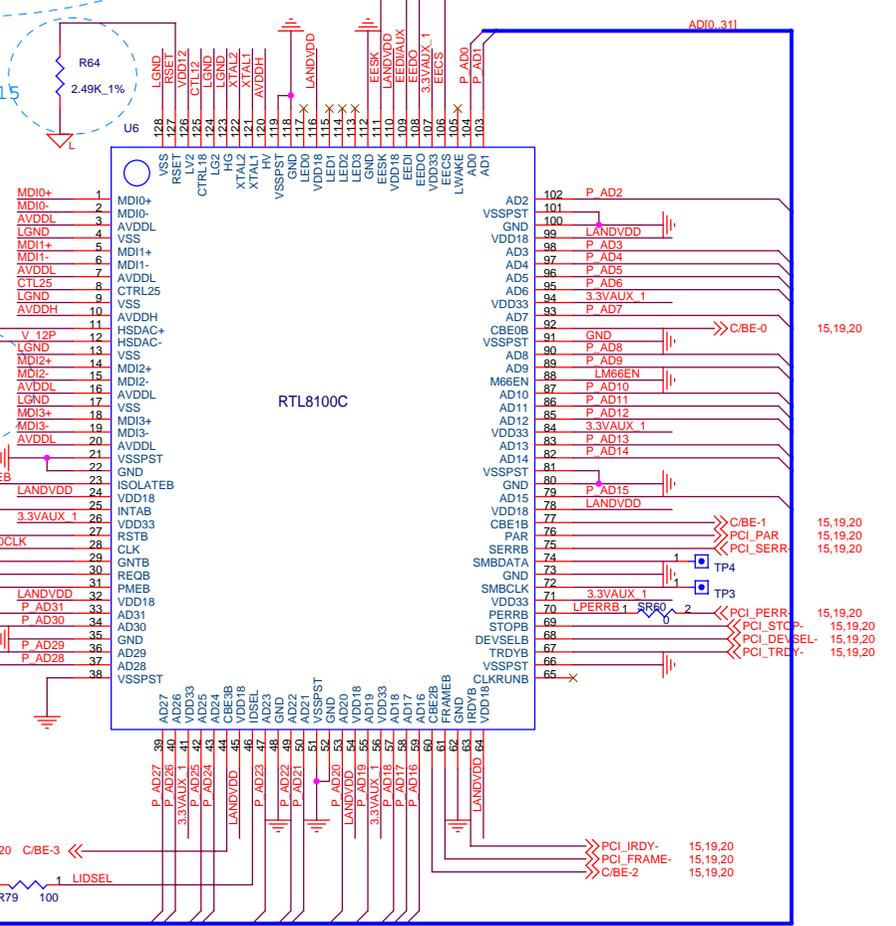
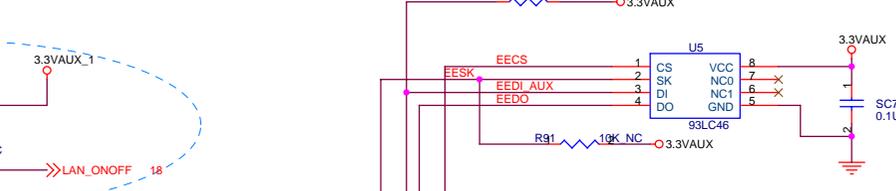
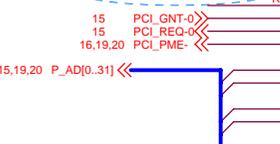
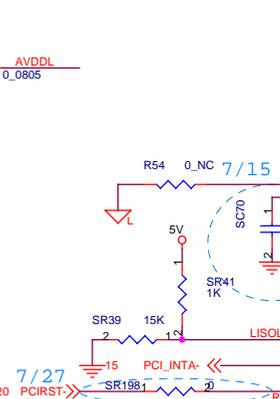
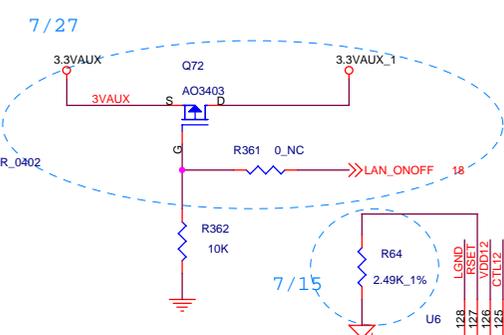
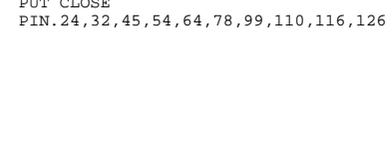
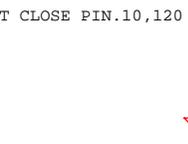
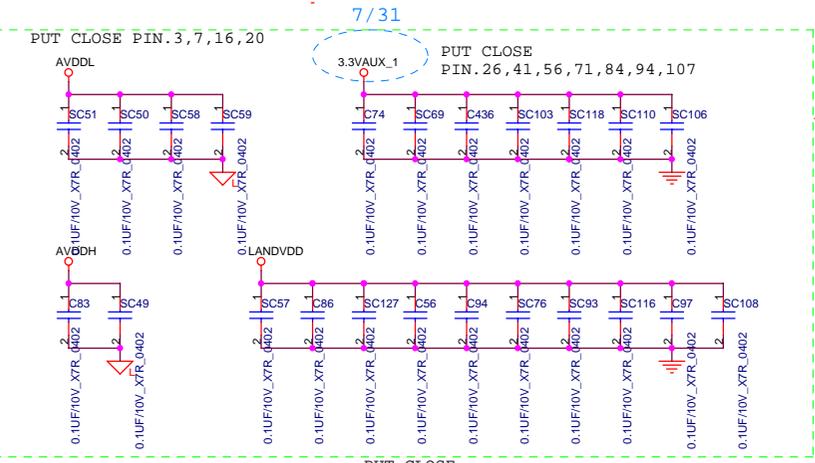
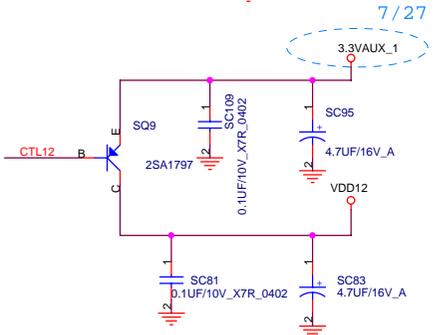
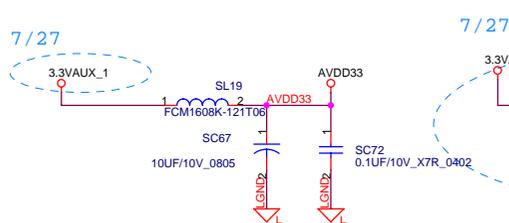
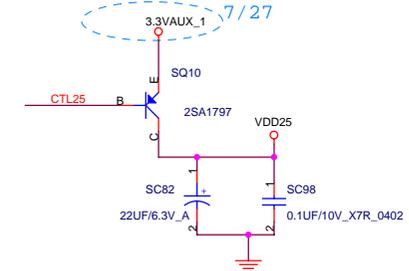
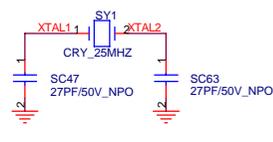
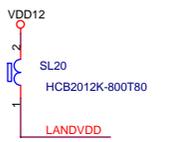
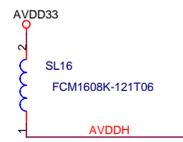
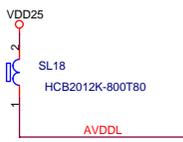
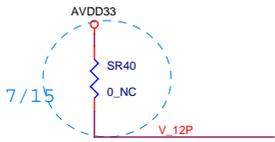
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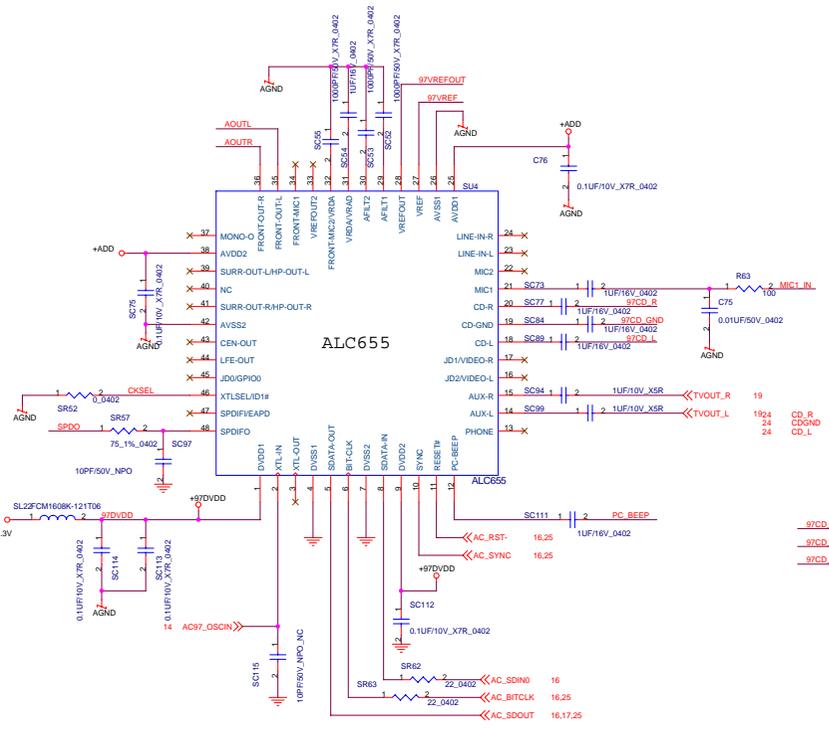
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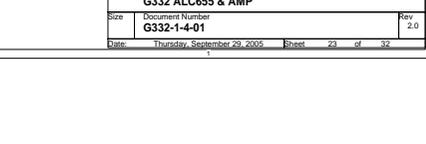
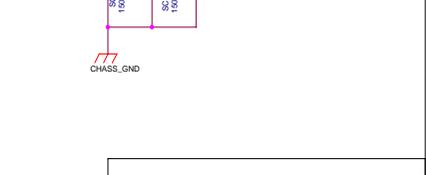
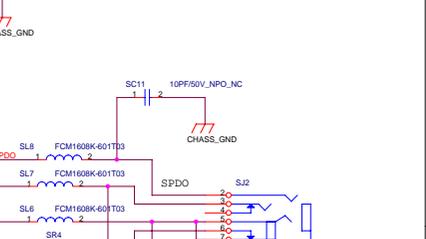
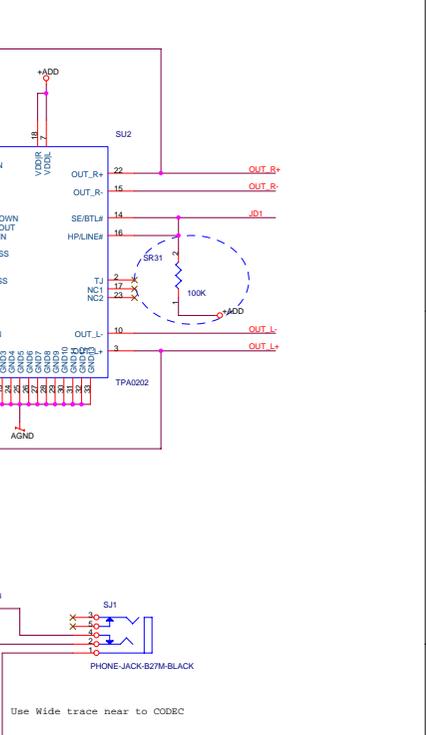
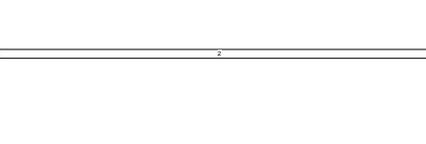
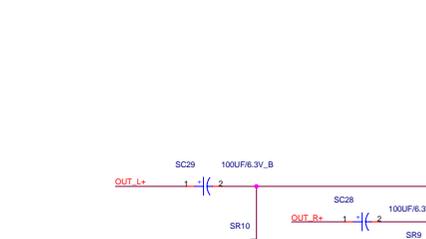
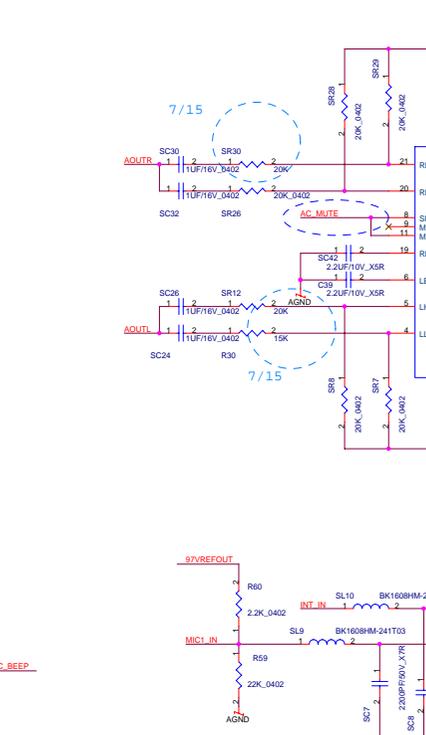
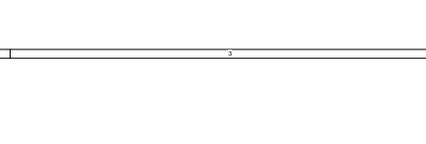
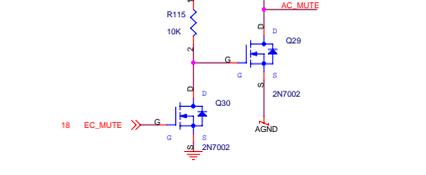
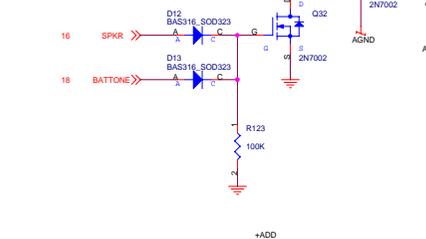
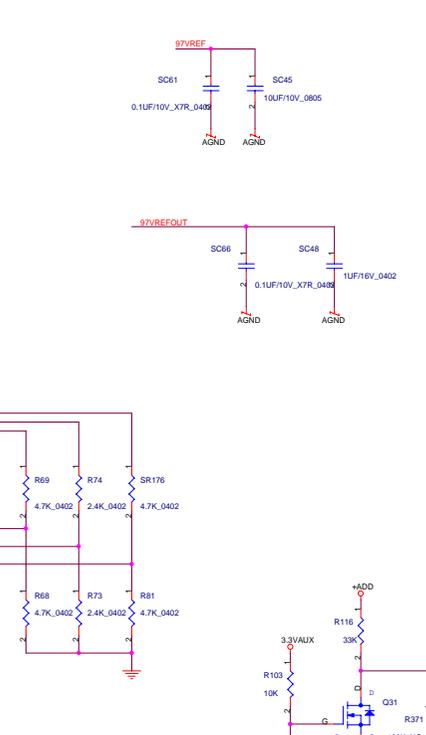
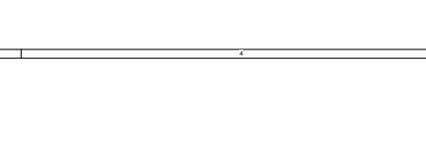
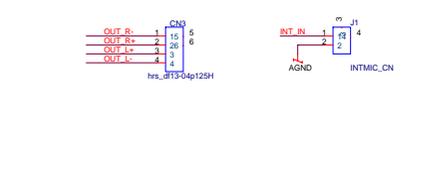
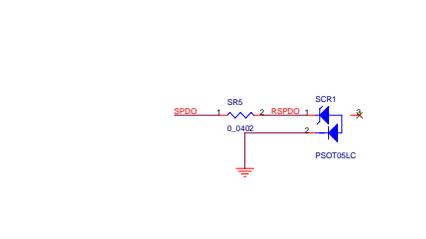
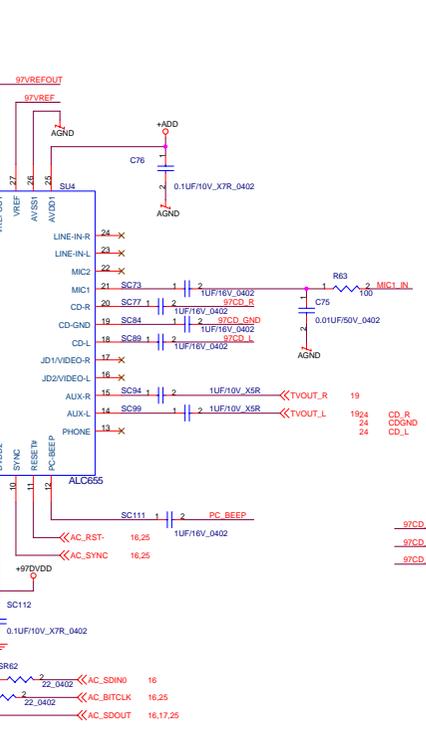
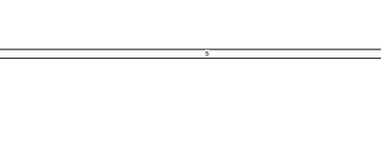
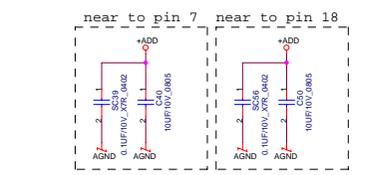
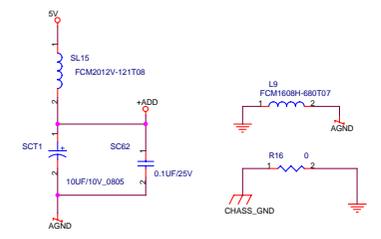


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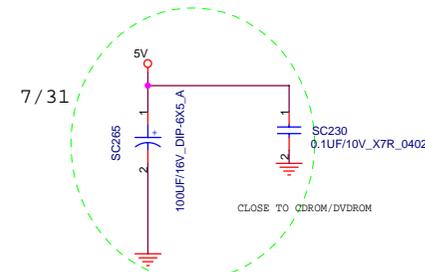
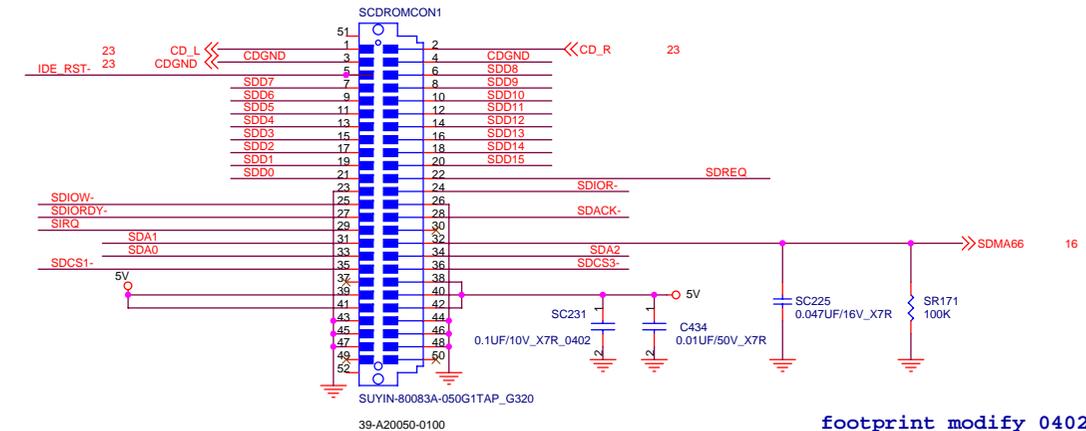
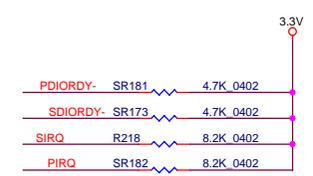
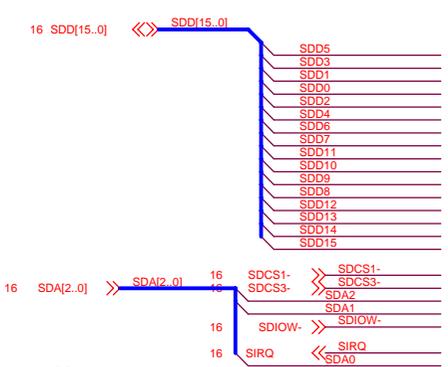
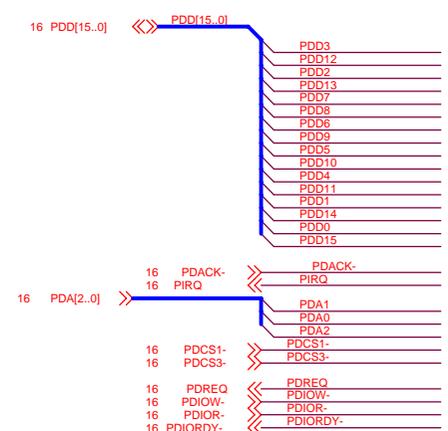
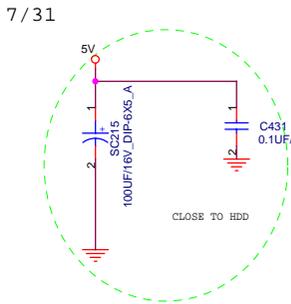
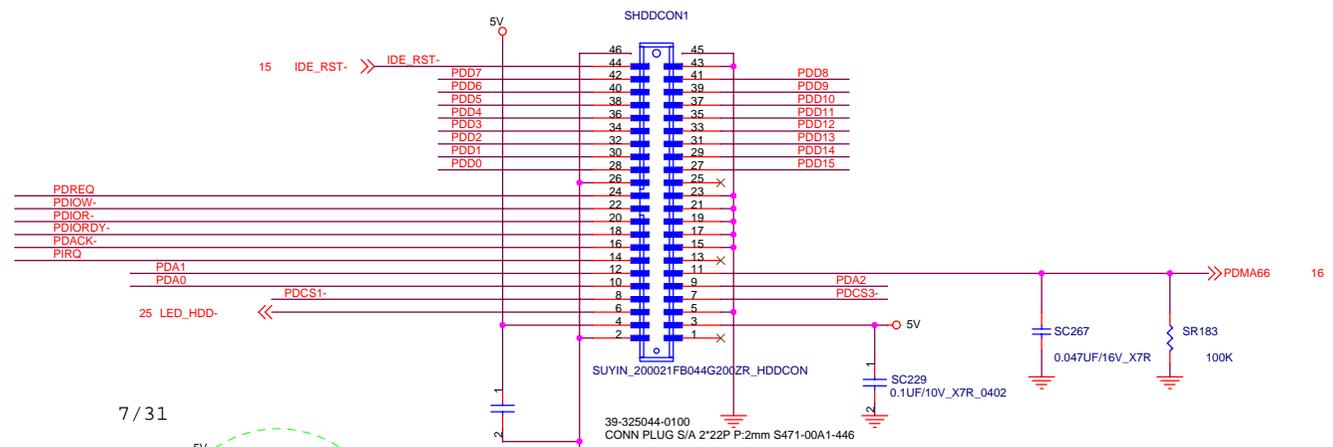




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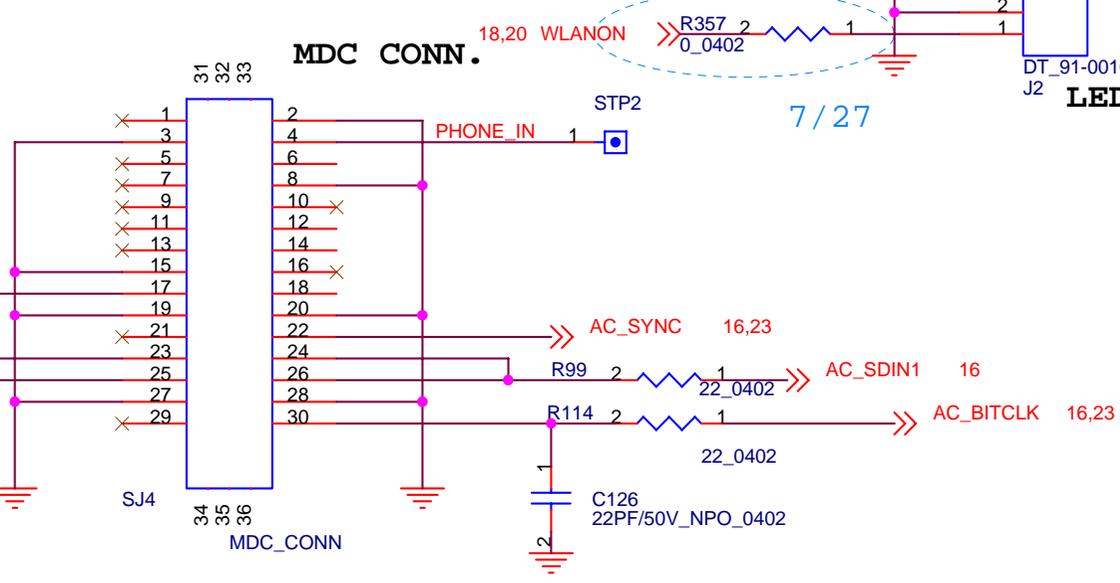
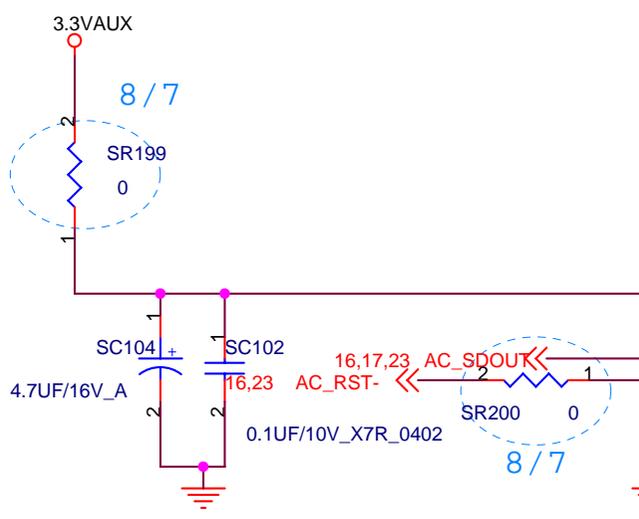
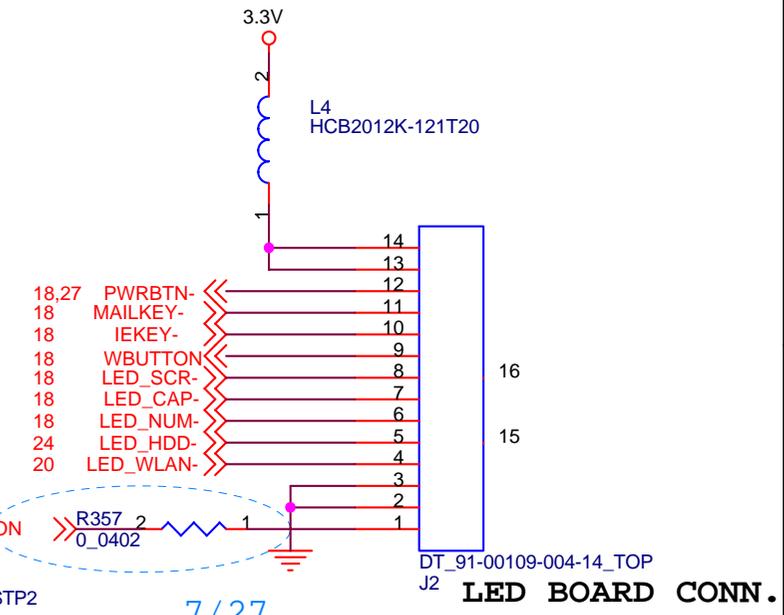
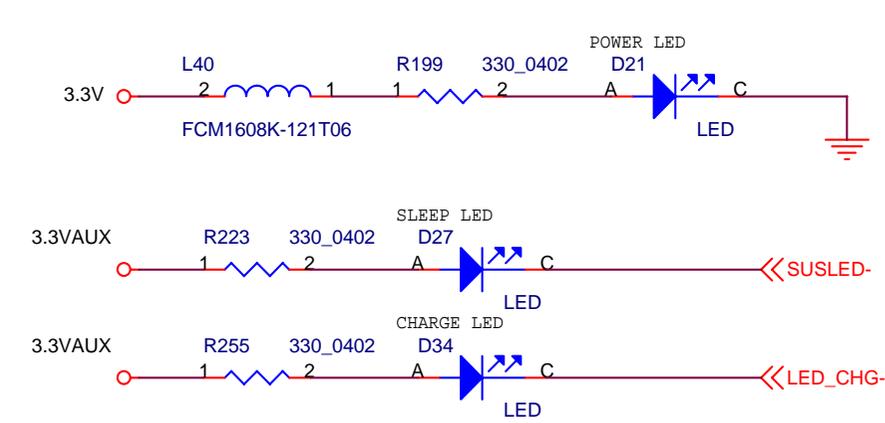


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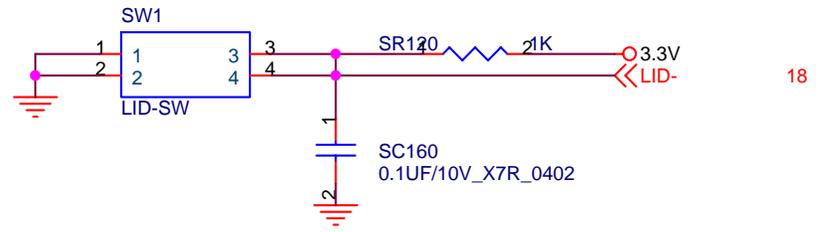


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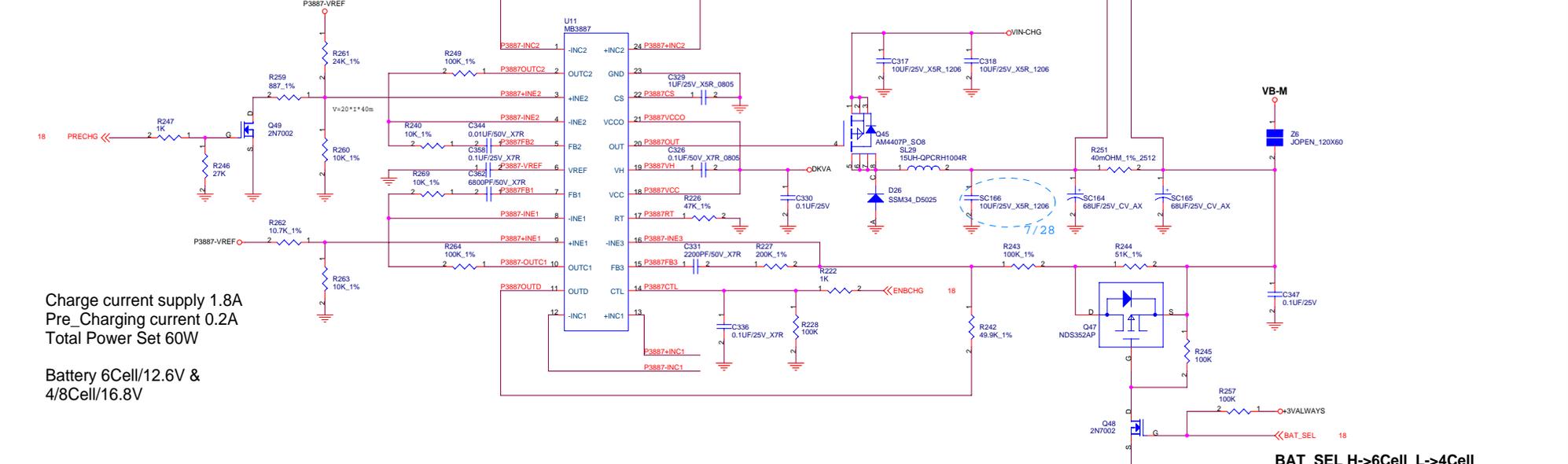
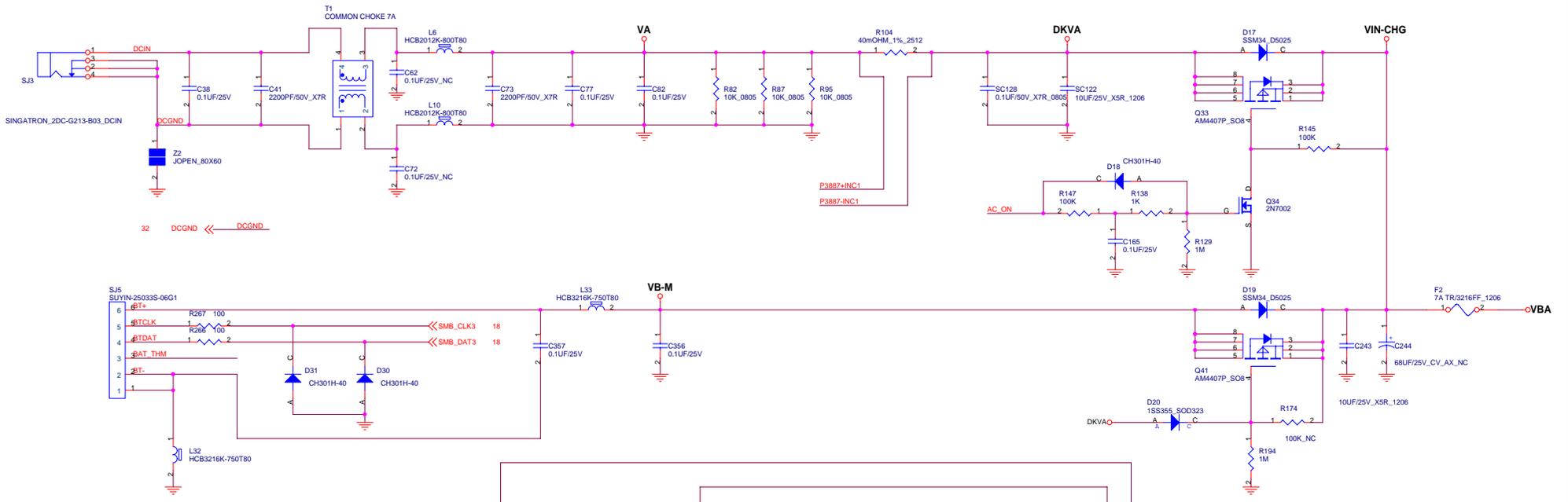
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pin 1 & 4 to press



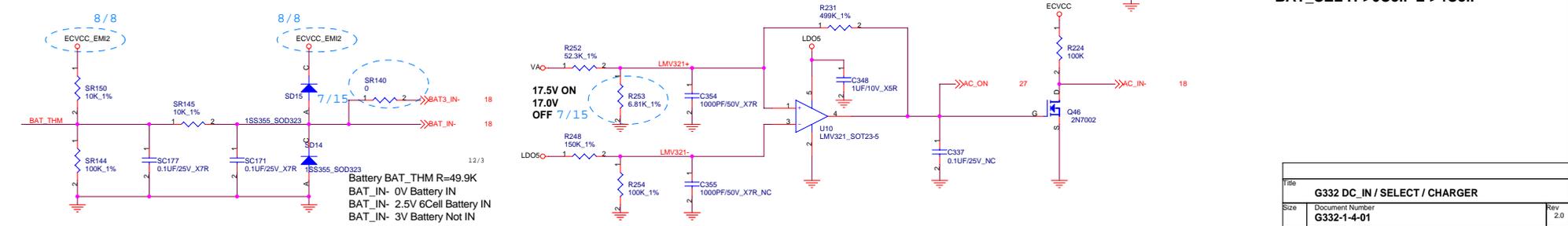
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Charge current supply 1.8A  
 Pre\_Charging current 0.2A  
 Total Power Set 60W

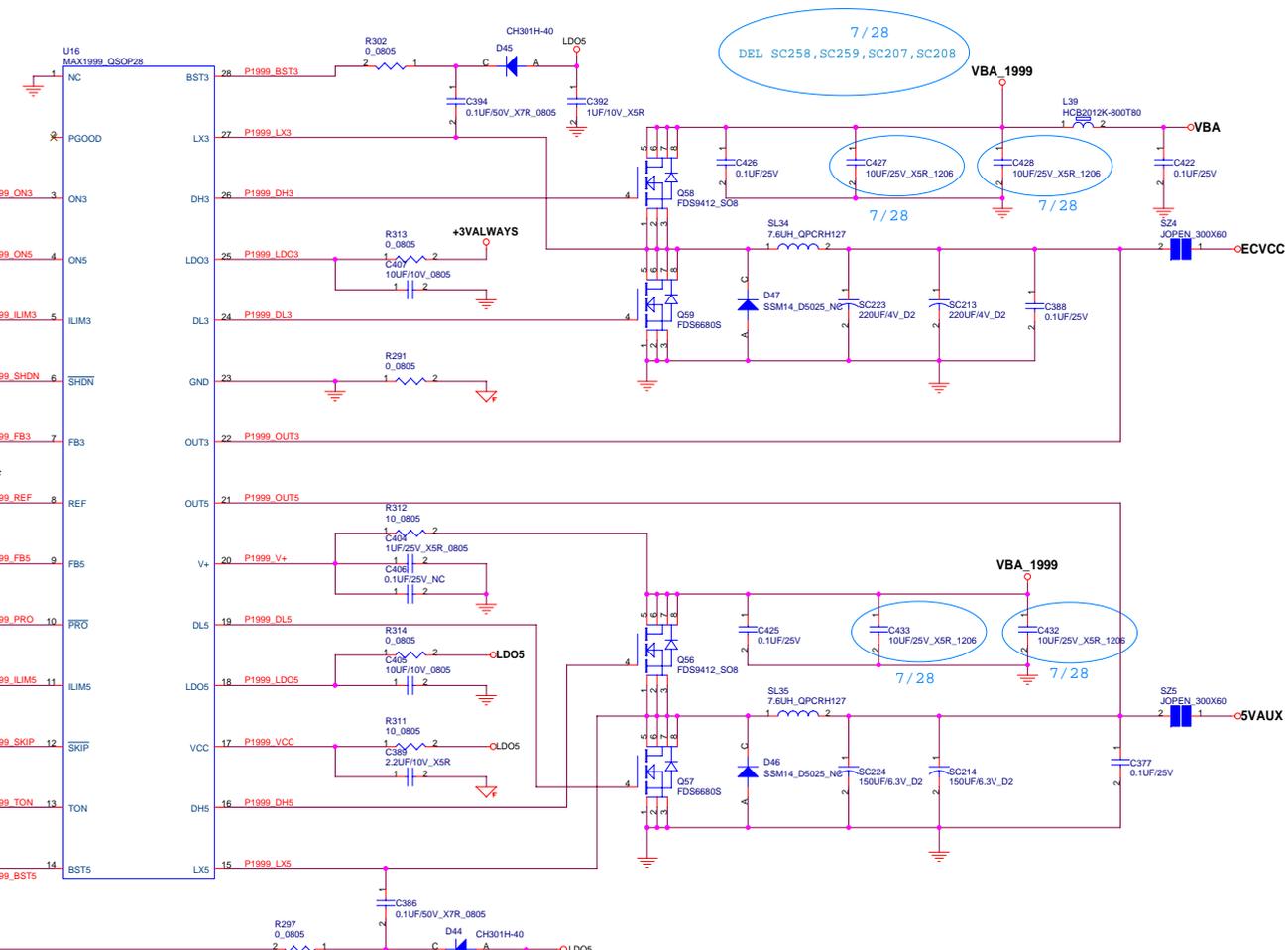
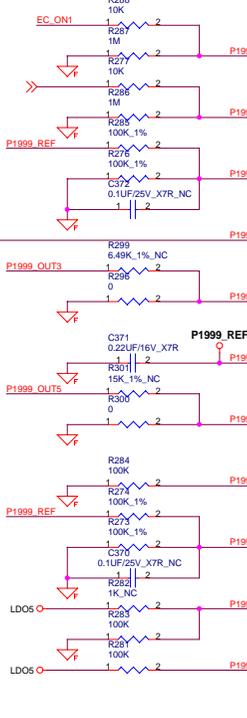
Battery 6Cell/12.6V &  
 4/8Cell/16.8V

BAT\_SEL H->6Cell L->4Cell



Battery BAT\_THM R=49.9K  
 BAT\_IN- 0V Battery IN  
 BAT\_IN- 2.5V 6Cell Battery IN  
 BAT\_IN- 3V Battery Not IN

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G332 DC_IN / SELECT / CHARGER		
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DEL SC258, SC259, SC207, SC208

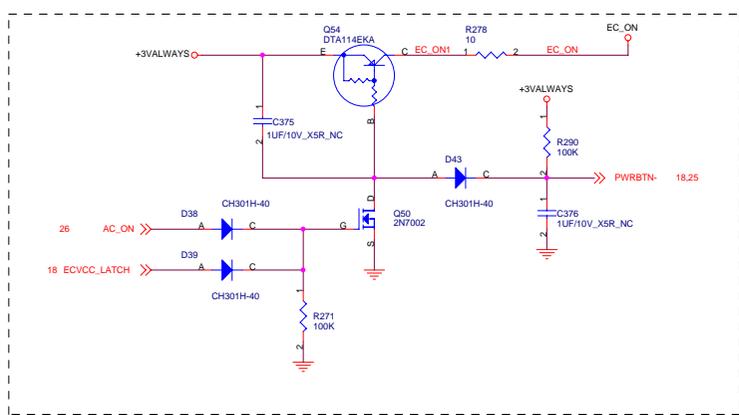
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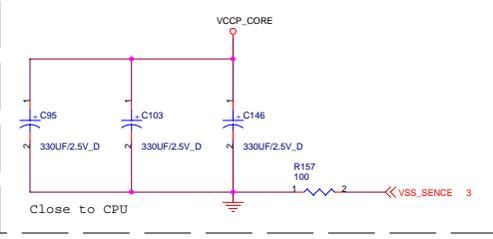
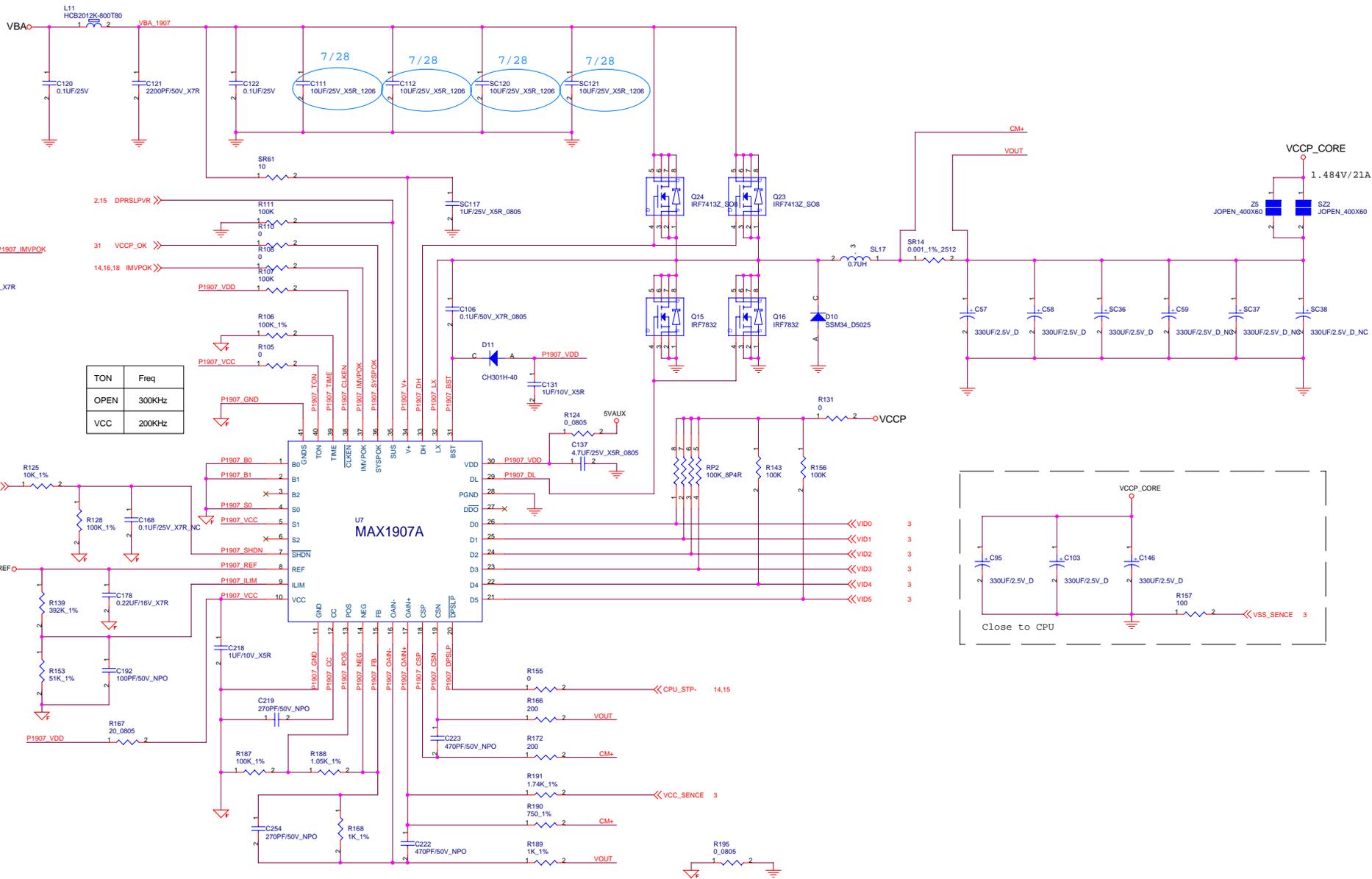
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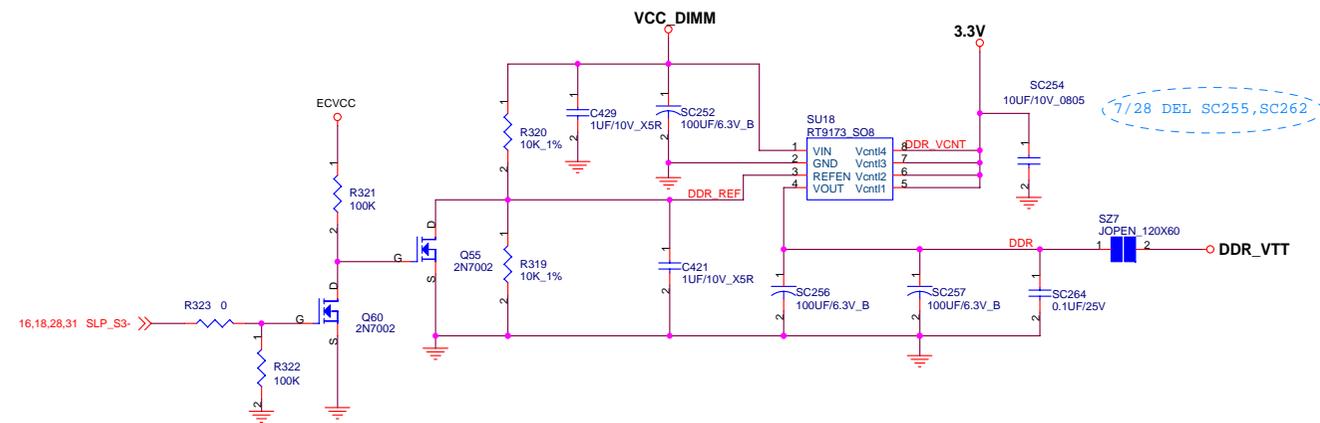
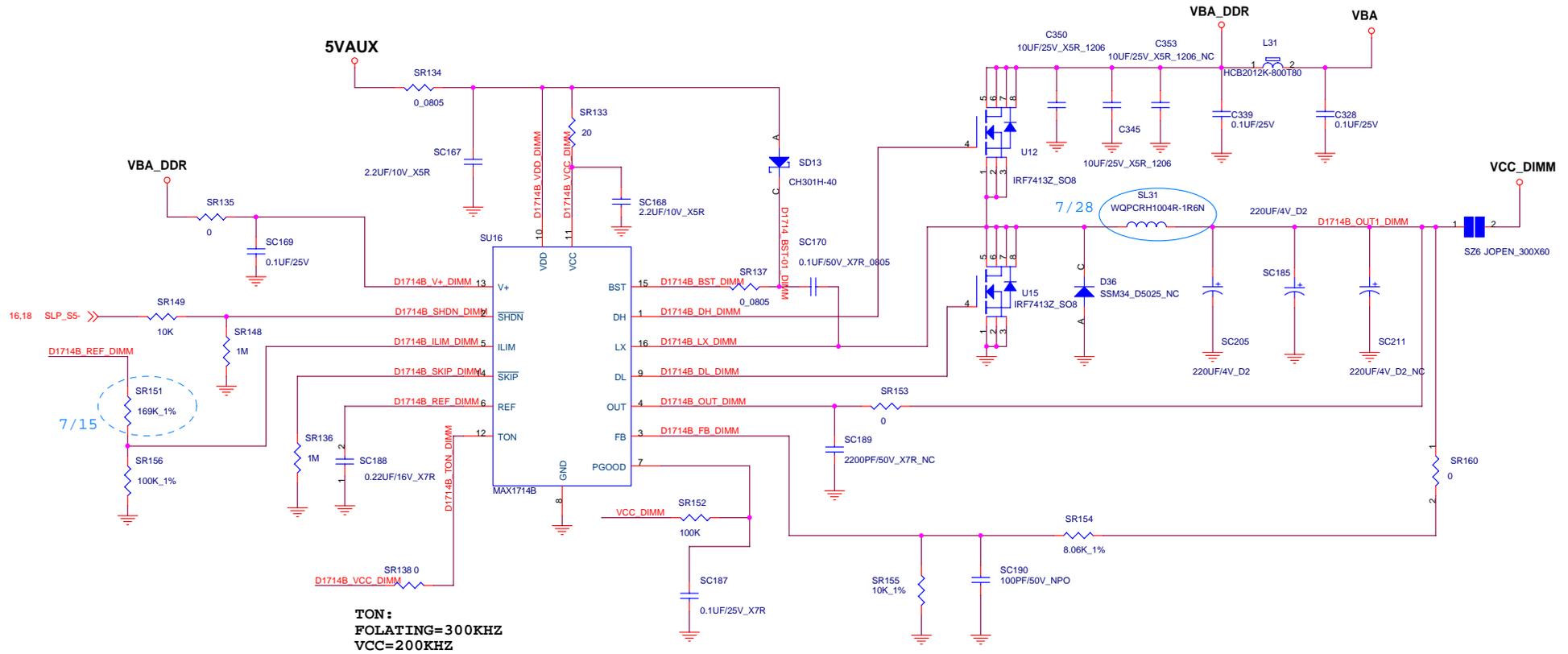
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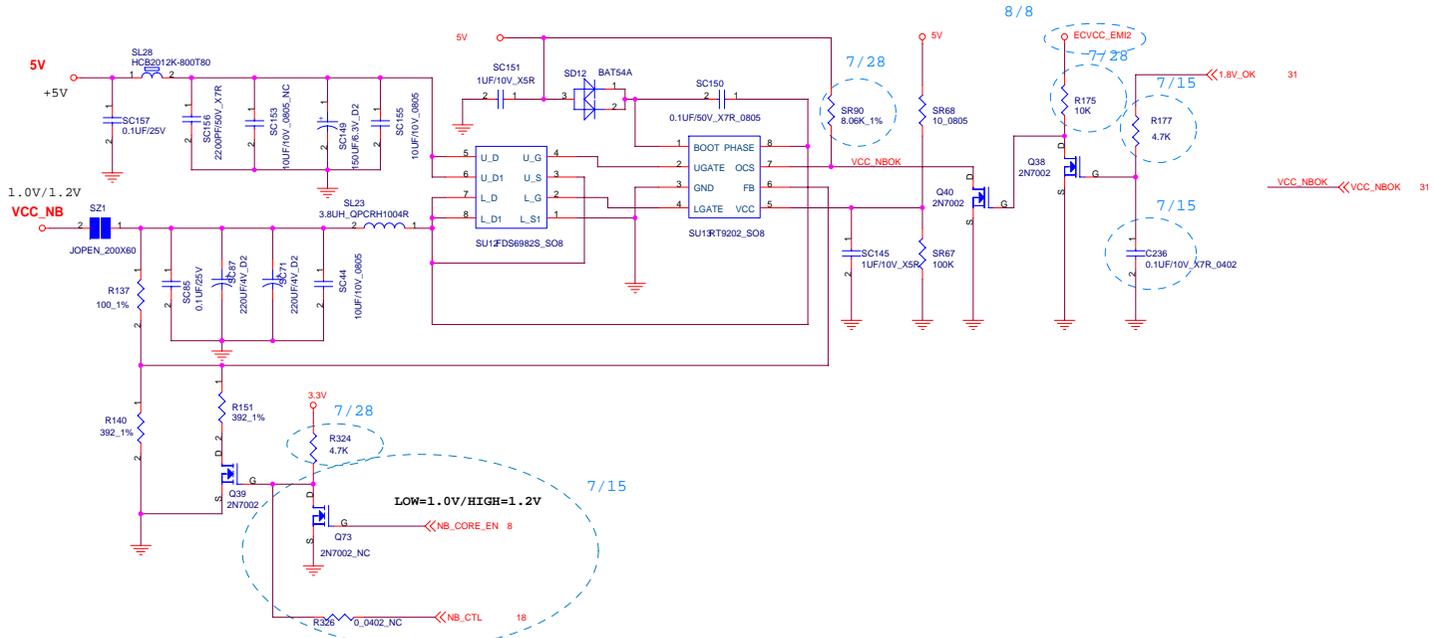
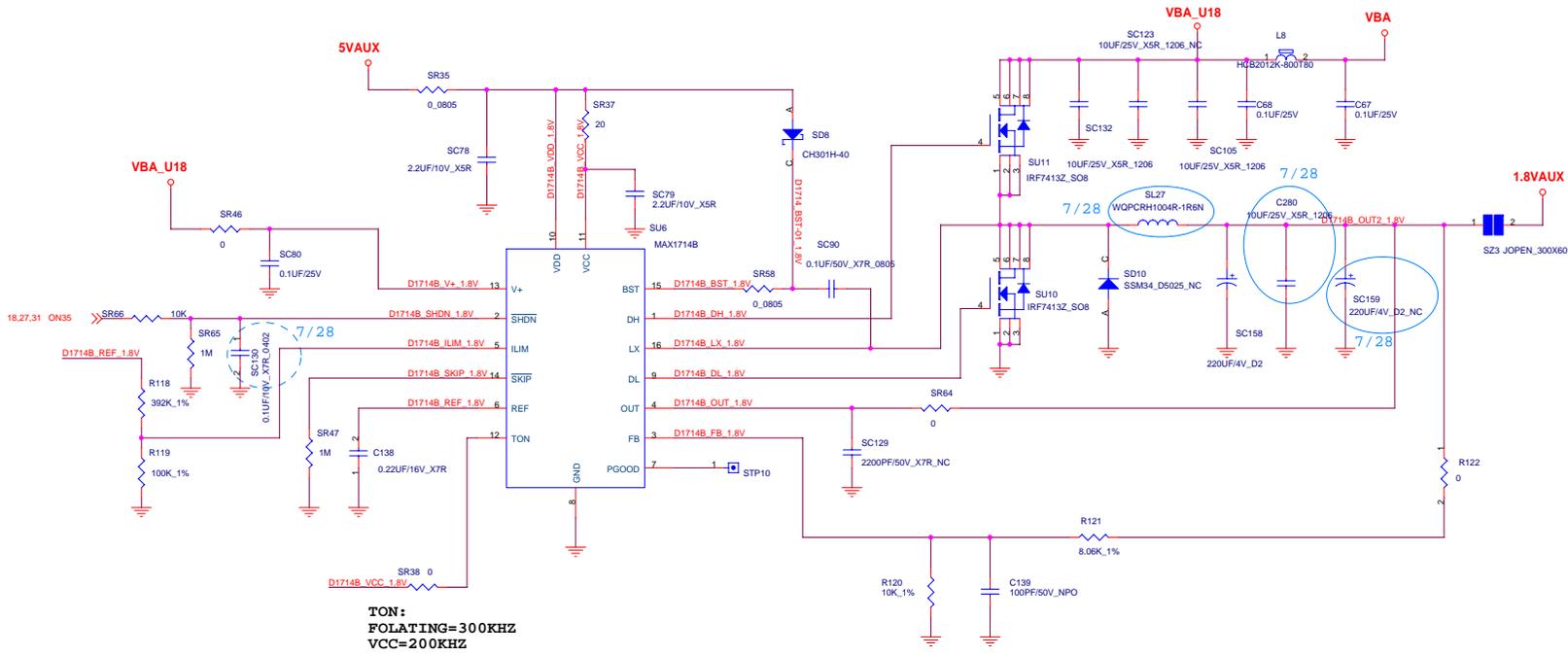


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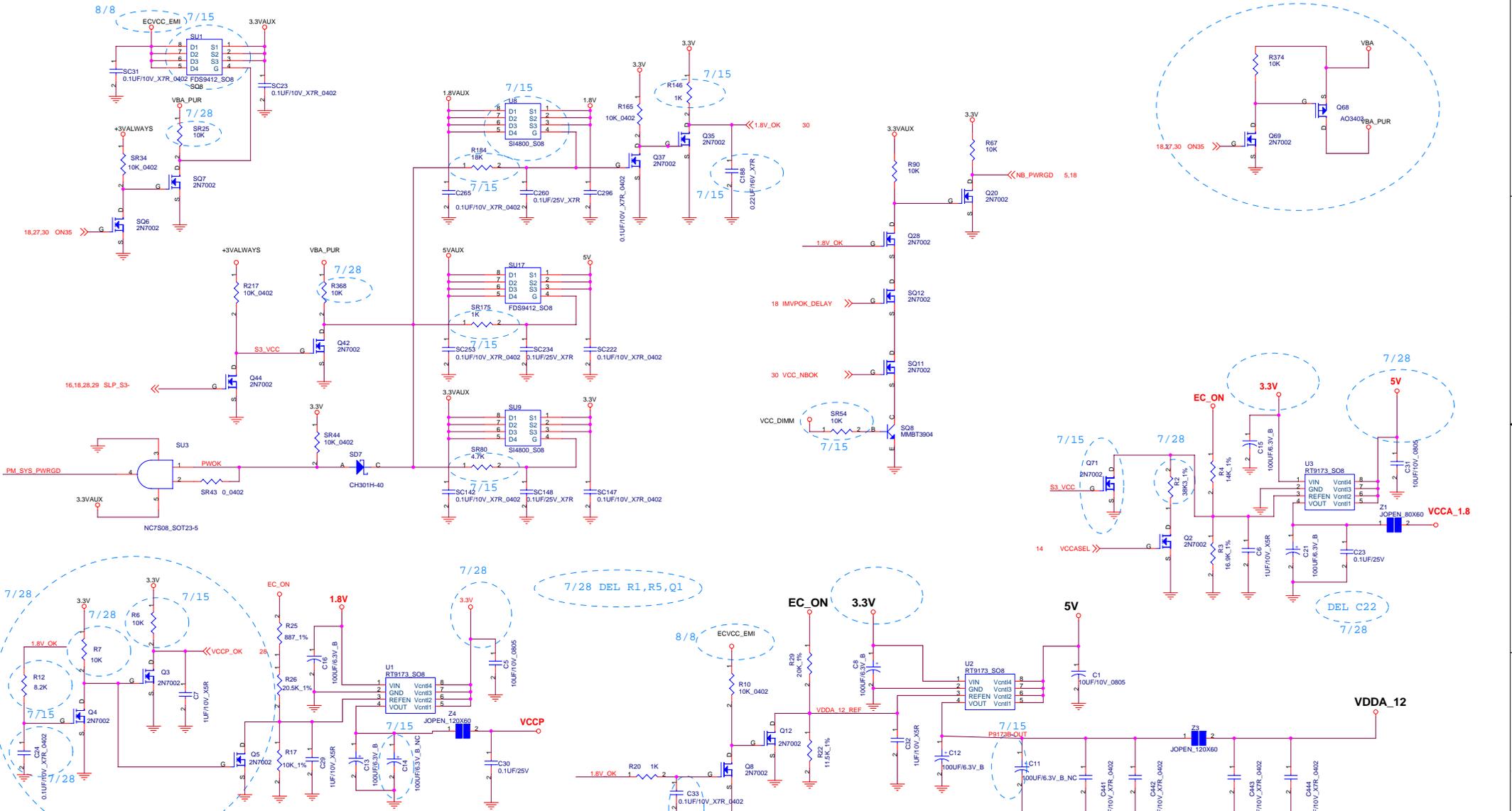




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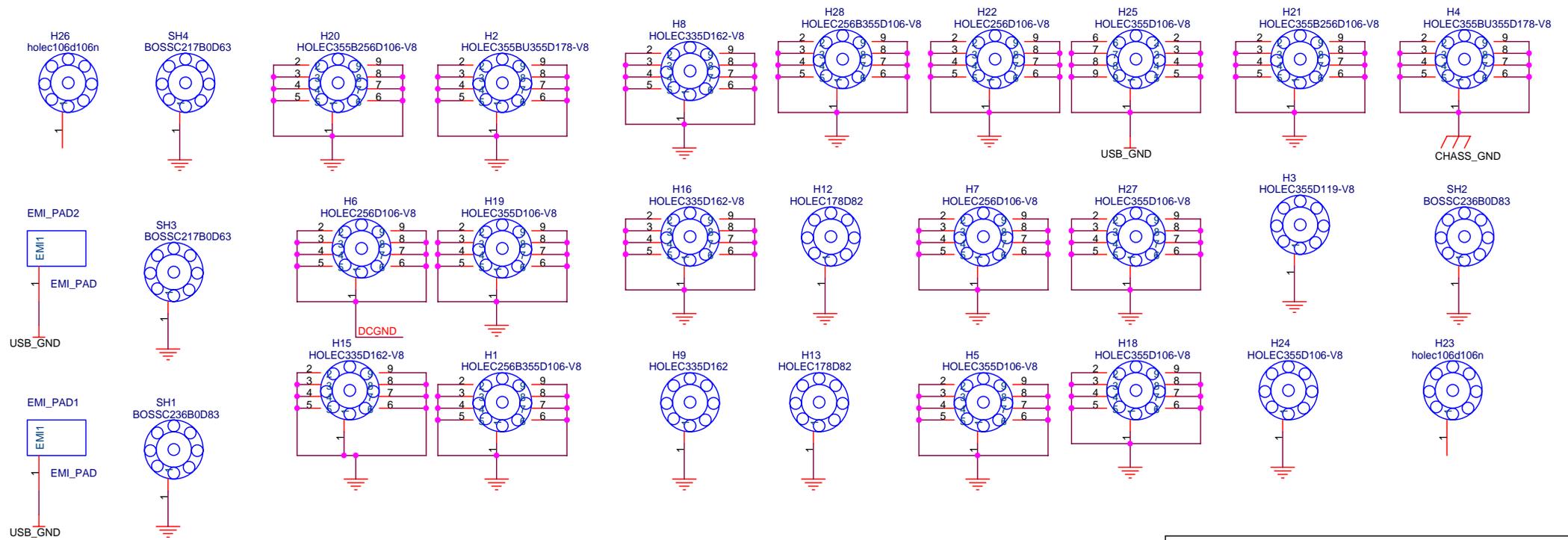
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# 332 HISTORY

- A.0 RELEASE CIRCUIT SCH.
- B.0 RELEASE CIRCUIT SCH.
- 1.0 RELEASE CIRCUIT SCH.
- 2.0 RELEASE CIRCUIT SCH.



26 DCGND ←← DCGND

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<b>G332 SCREW HOLE &amp; HISTORY</b>		
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