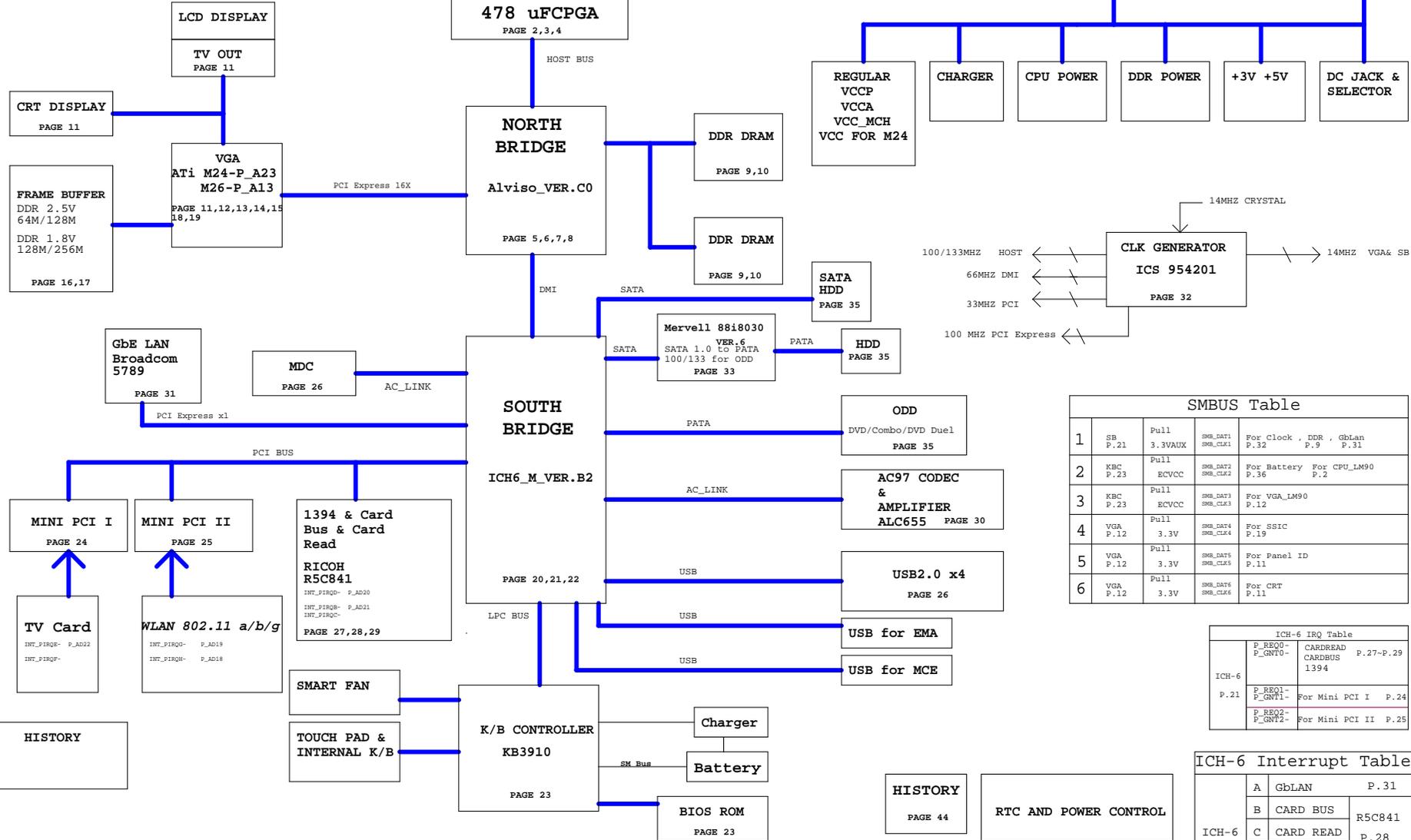


G600

SOCKET 479
Dothan
478 uFCPGA
PAGE 2,3,4

SYS POWER

M24-POWER



SMBUS Table

Index	SB	Control	SMB_DATx	SMB_CLKx	Function
1	P.21	Pull 3.3V/IOx	SMB_DAT1	SMB_CLK1	For clock, DDR, GbLAN P.32, P.9, P.31
2	P.23	Pull ECVCC	SMB_DAT2	SMB_CLK2	For Battery P.36, For CPU_LM90 P.2
3	P.23	Pull ECVCC	SMB_DAT3	SMB_CLK3	For VGA_LM90 P.12
4	P.12	Pull 3.3V	SMB_DAT4	SMB_CLK4	For SSIC P.19
5	P.12	Pull 3.3V	SMB_DAT5	SMB_CLK5	For Panel ID P.11
6	P.12	Pull 3.3V	SMB_DAT6	SMB_CLK6	For CRT P.11

ICH-6 IRQ Table

ICH-6	IRQ	Function
P.21	P_IRQ0-	CARDREAD CARDBUS P.27~P.29, I394
	P_IRQ1-	For Mini PCI I P.24
P.21	P_IRQ2-	For Mini PCI II P.25

ICH-6 Interrupt Table

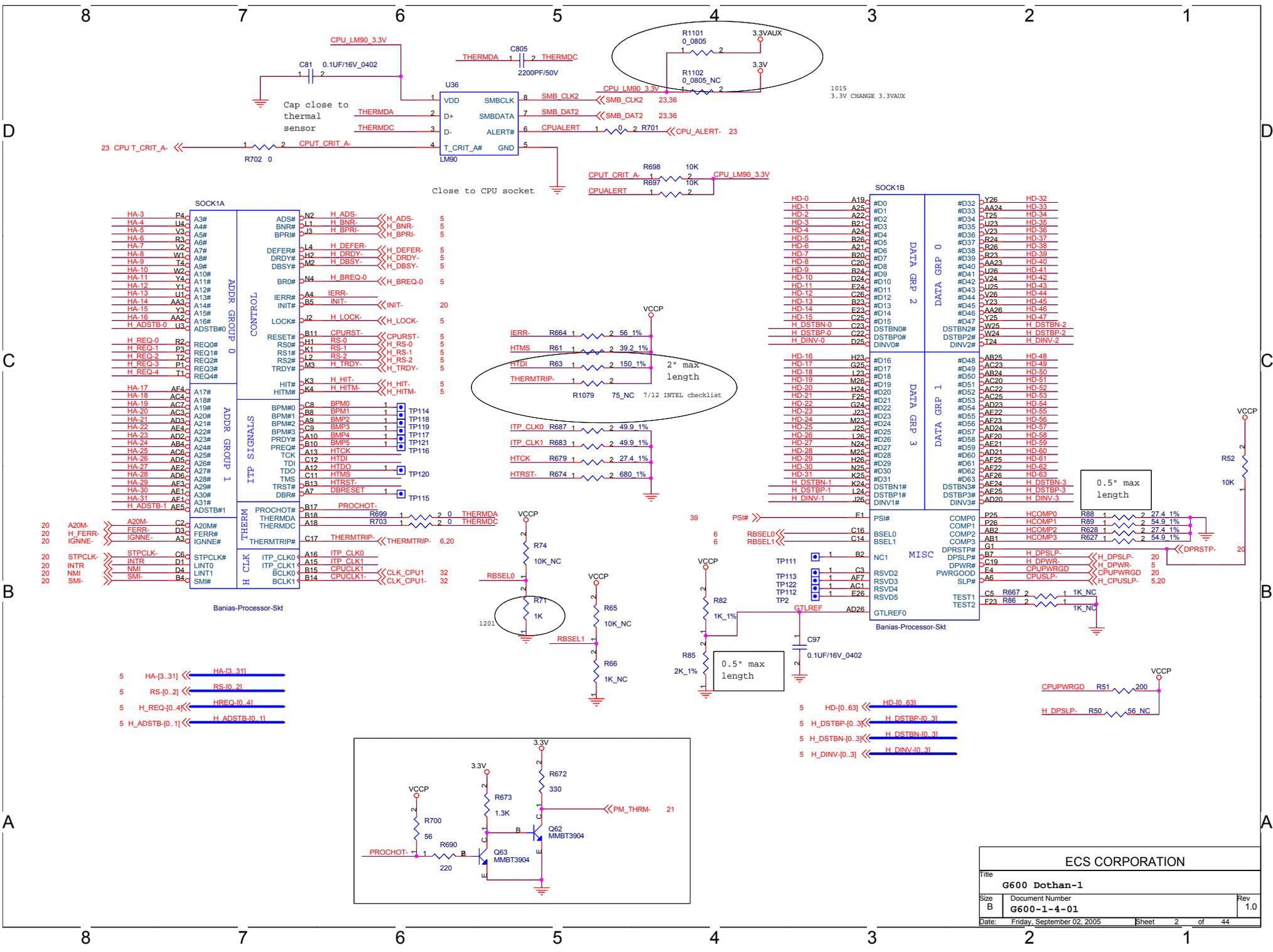
ICH-6	Interrupt	Function
P.21	A	GbLAN P.31
	B	CARD BUS R5C841
	C	CARD READ P.28
	D	I394
P.21	E	MiniPCI I P.24
	F	MiniPCI II P.25

PAGE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	
REV	1.D	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.A	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.D	1.0	1.D	
DATE	9/2 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/27 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	9/2 2005	4/6 2005	9/2 2005

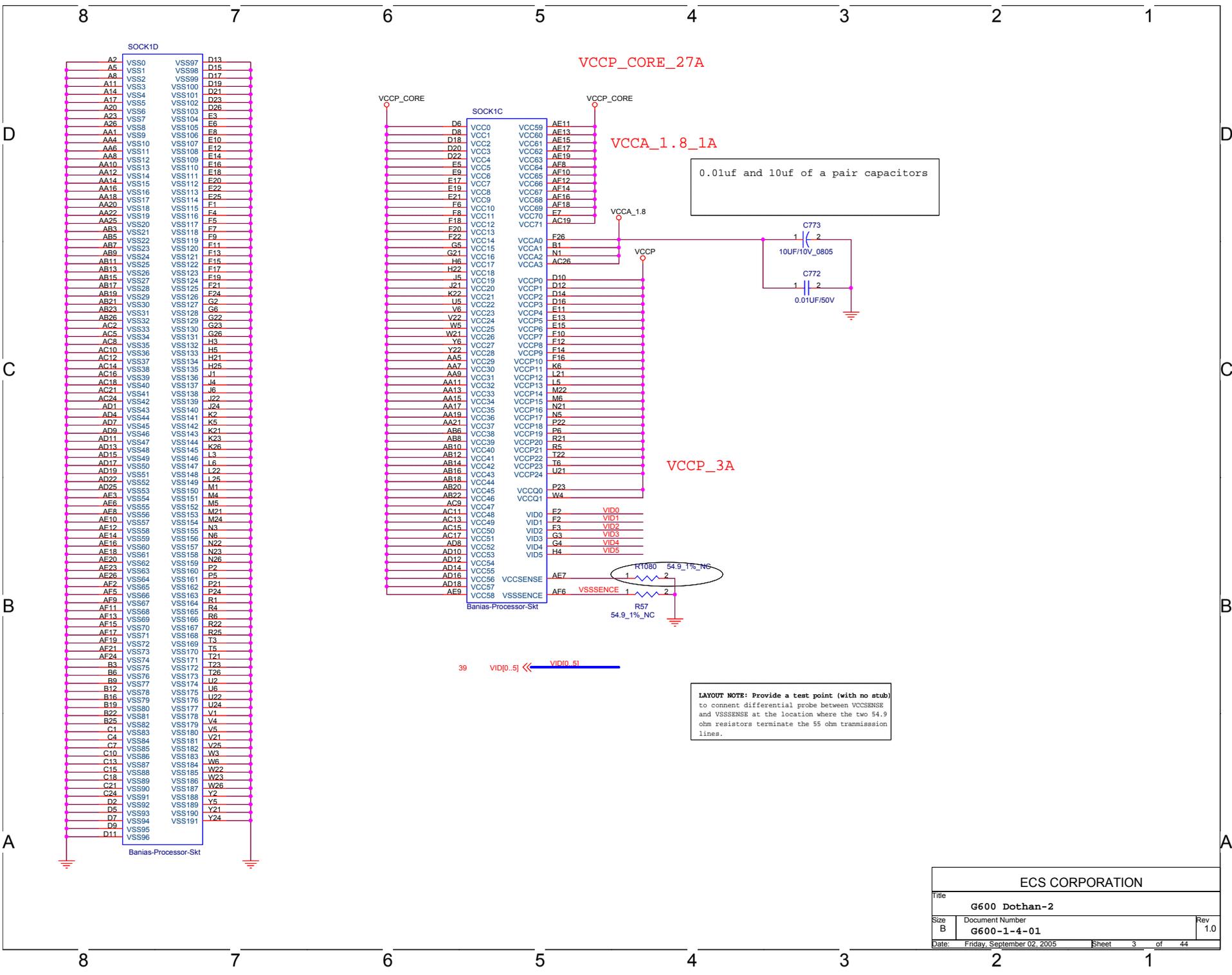
PAGE	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
REV	1.0	1.0	1.A	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.D	1.0	1.0	1.B	1.B	1.B	1.0	1.0	1.D	1.0	1.D
DATE	4/6 2005	4/6 2005	4/27 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	4/6 2005	9/2 2005	4/6 2005	4/6 2005	5/30 2005	5/30 2005	5/30 2005	4/6 2005	4/6 2005	9/2 2005	4/6 2005	9/2 2005

PCB 1.0 NO:15-F53-011000

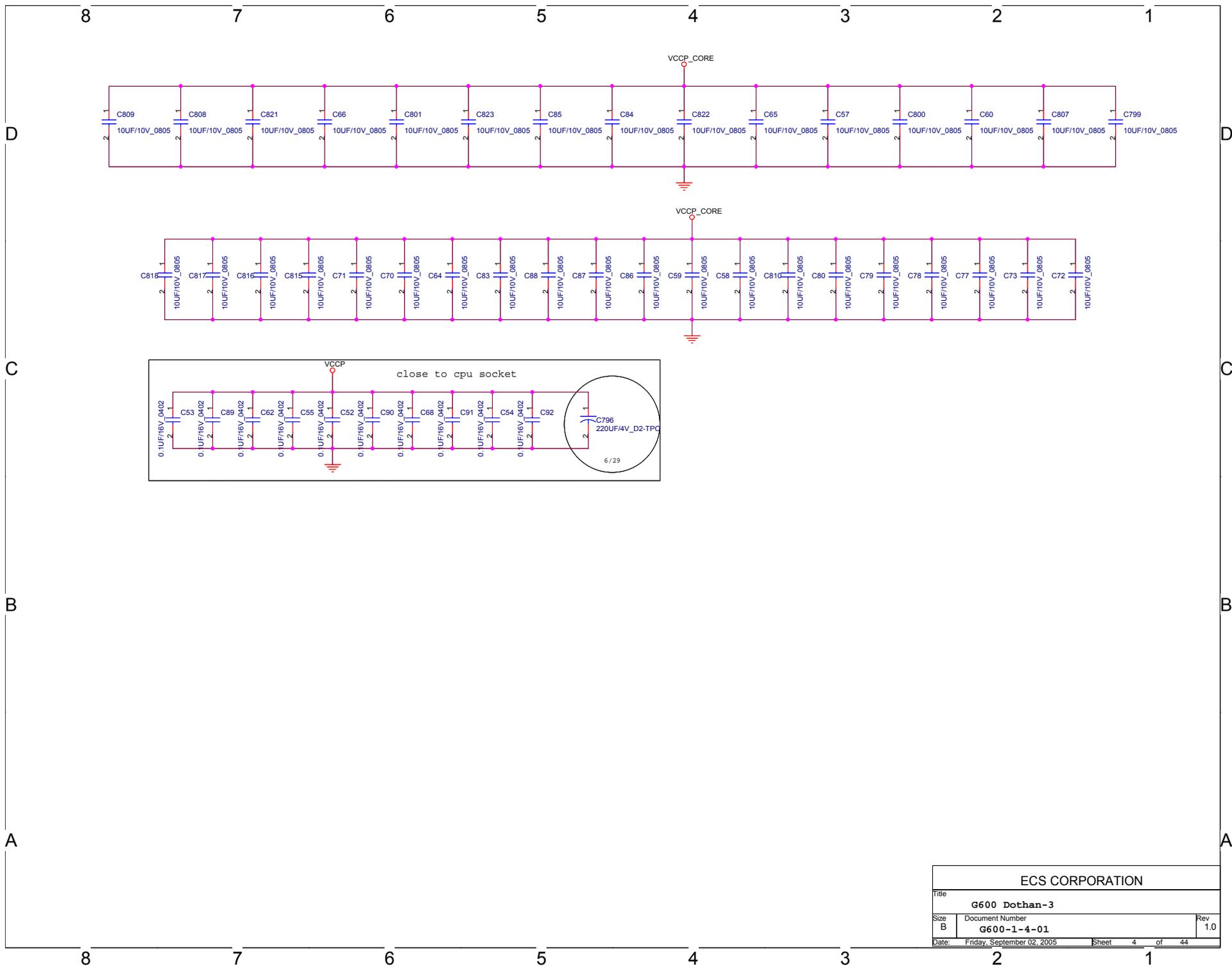
Title		G600 BLOCK	
Size	C	Document Number	G600-14-01
Date	Friday, September 02, 2005	Sheet	1 of 44



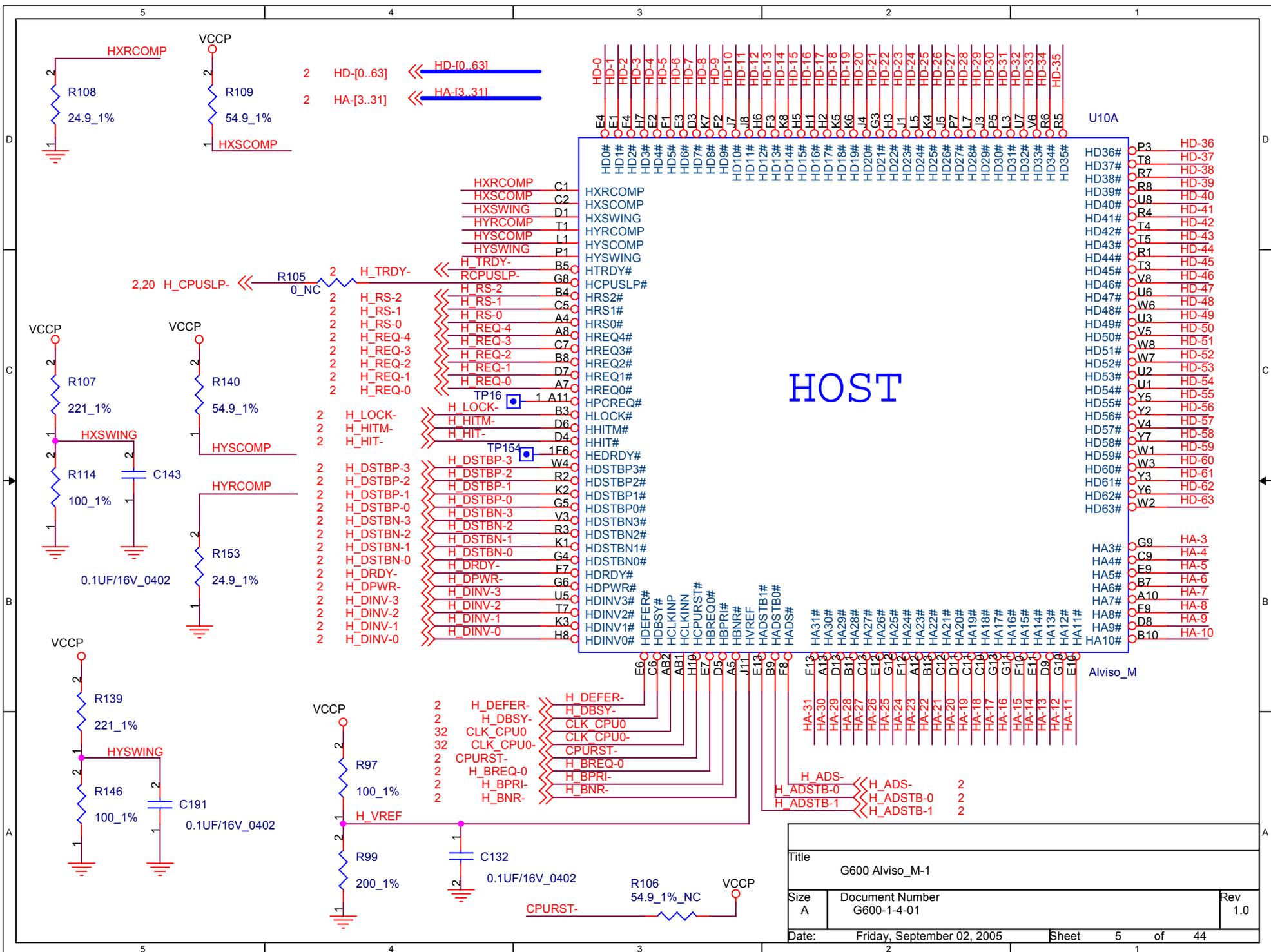
ECS CORPORATION		
File	G600 Dothan-1	
Size	Document Number	Rev
B	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 2 of 44



ECS CORPORATION		
File: G600 Dothan-2		
Size: B	Document Number: G600-1-4-01	Rev: 1.0
Date: Friday, September 02, 2005	Sheet: 3	of 44

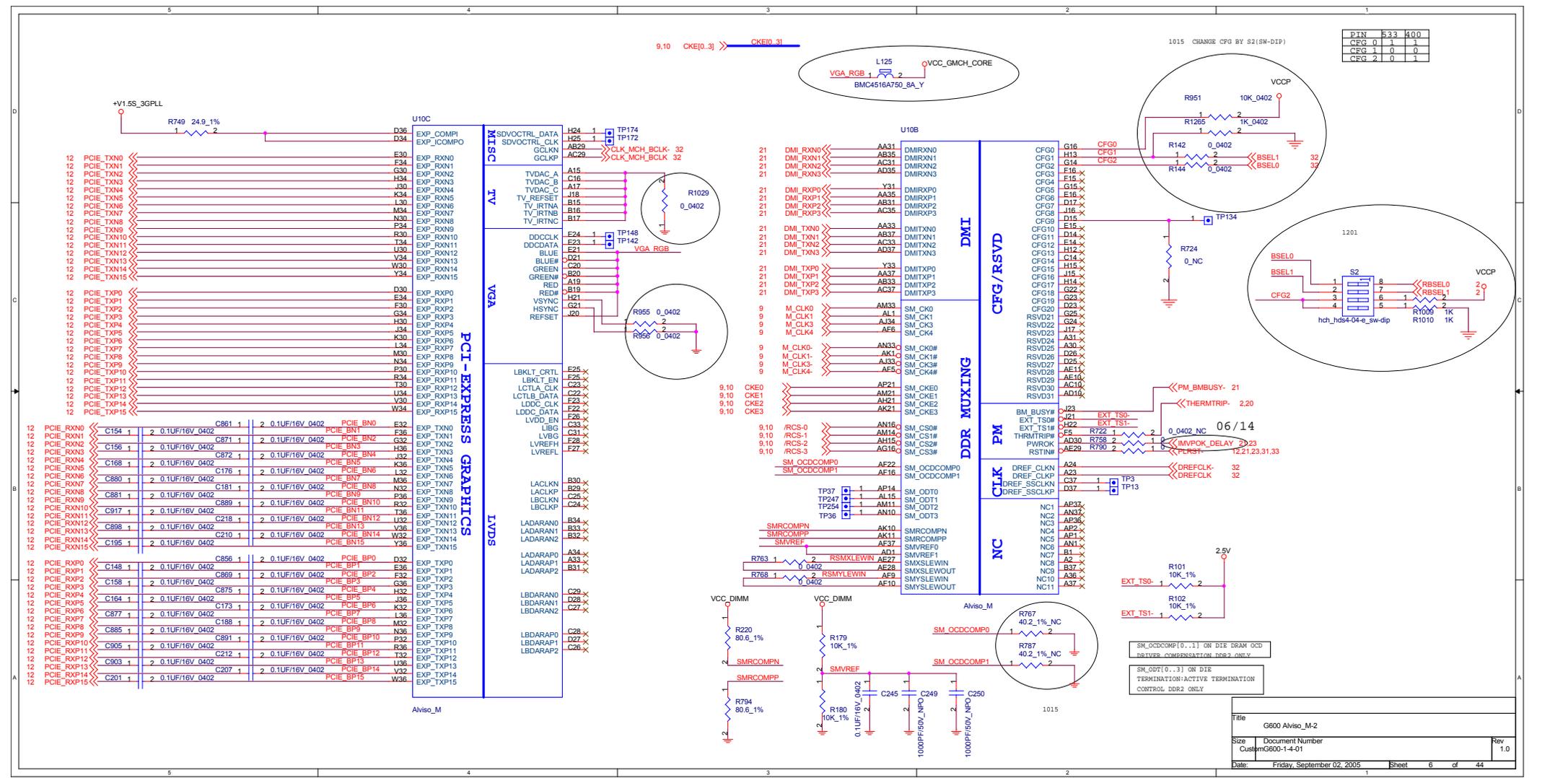


ECS CORPORATION		
File		
G600 Dothan-3		
Size	Document Number	Rev
B	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 4 of 44



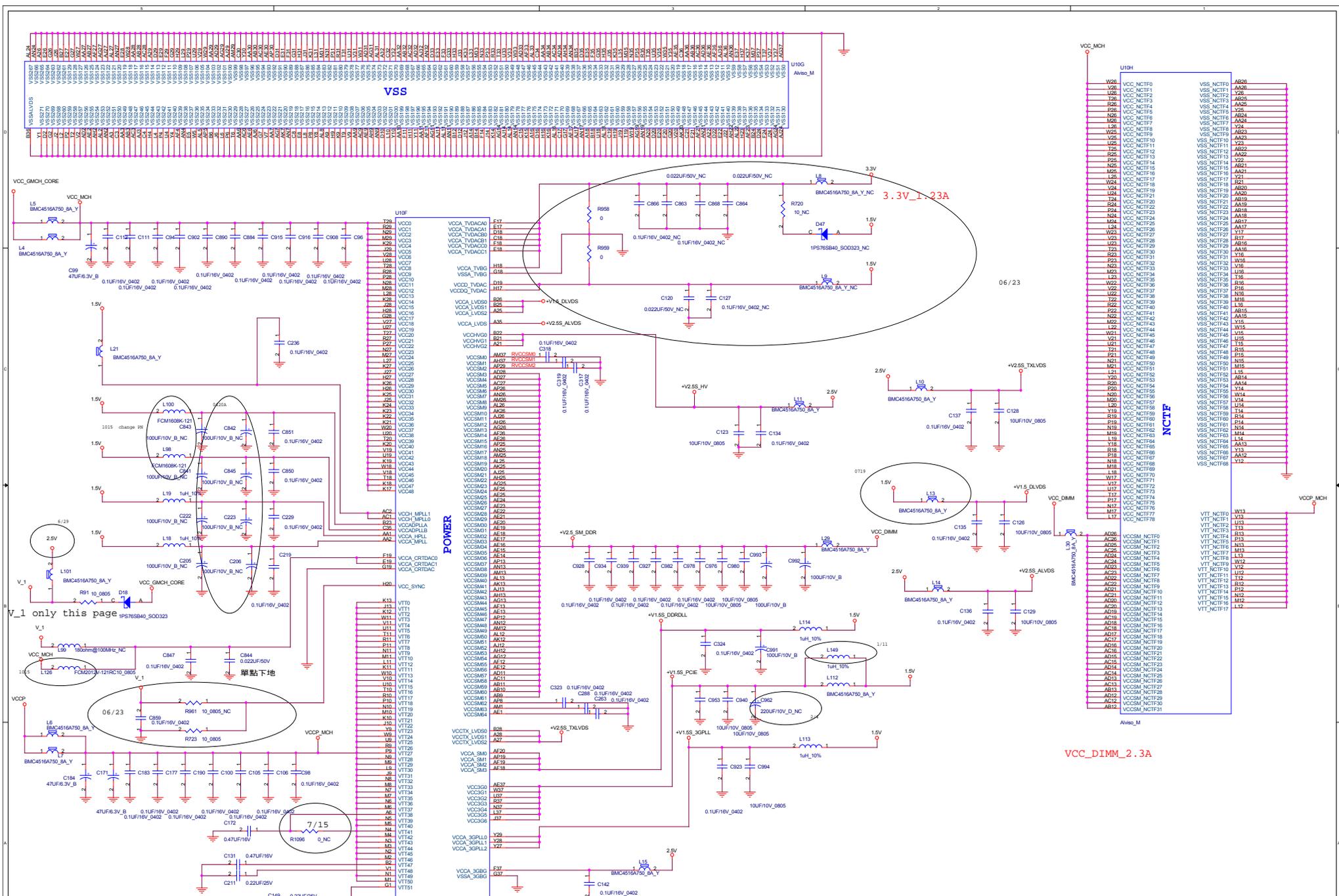
HOST

Title		
G600 Alviso_M-1		
Size	Document Number	Rev
A	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 5 of 44



PIN	533	800
CFG 0	1	1
CFG 1	0	0
CFG 2	0	1

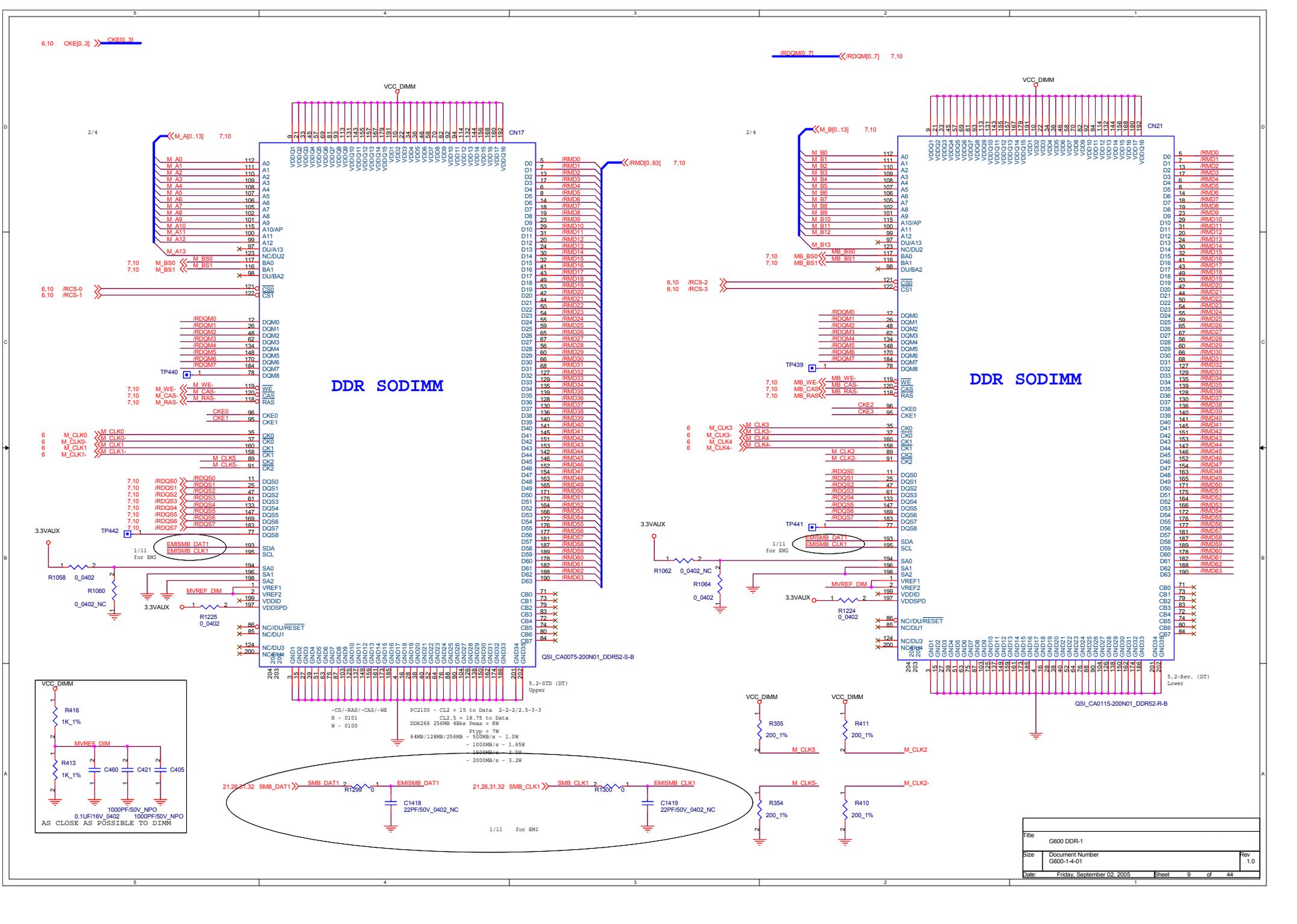
Title	G600 Aviso_M-2	Rev	1.0
Size	Document Number	CustomG600-1-4-01	
Date:	Friday, September 02, 2005	Sheet	6 of 44



File	G600 Avic_M-4
Name	Document Number
Date	G600-1-4-01
Size	Friday, September 02, 2005
Sheet	8 of 44
Rev	1.0

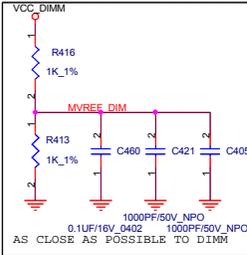
6.10 CKE[0..3] << CKE[0..3]

/RDCM[0..7] << /RDCM[0..7] 7.10



DDR SODIMM

DDR SODIMM

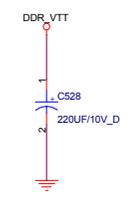
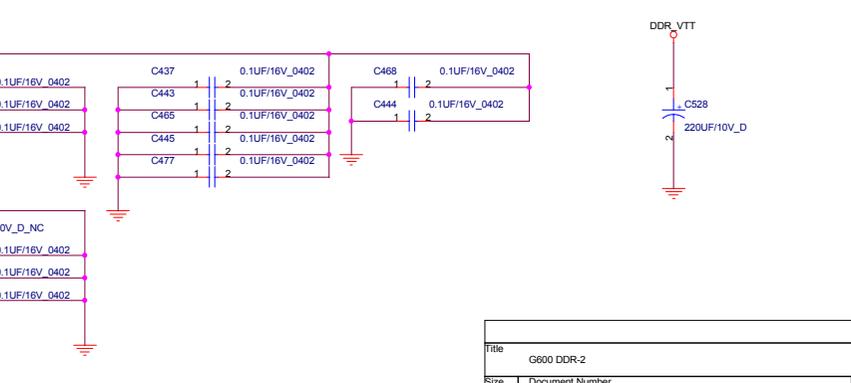
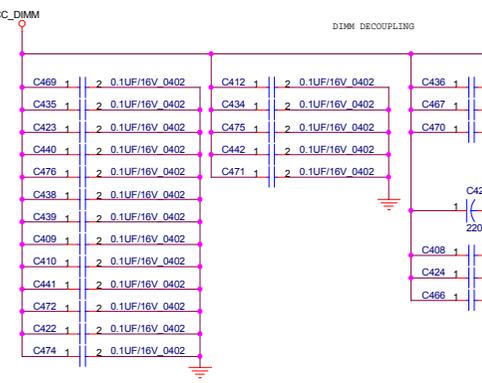
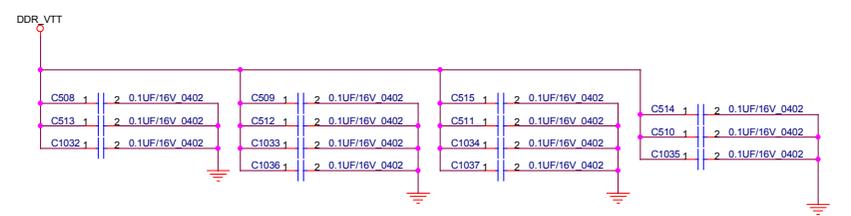
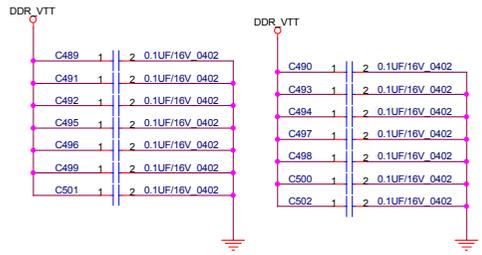
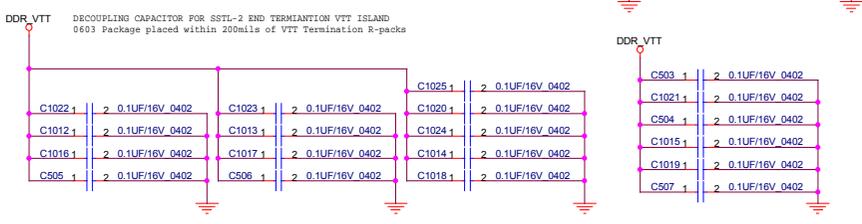
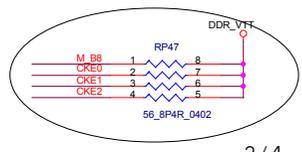
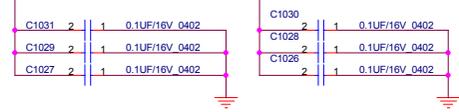
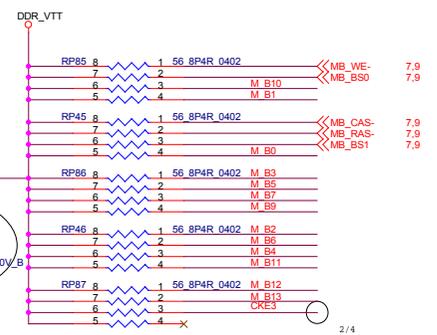
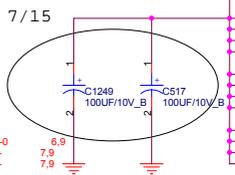
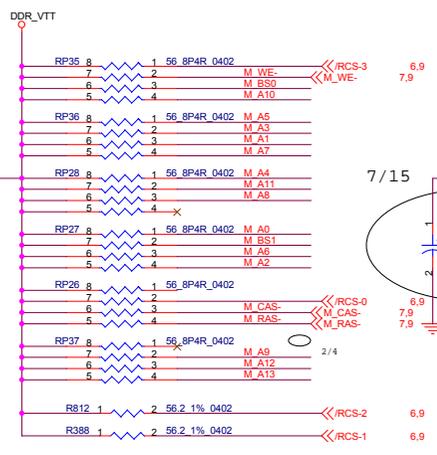
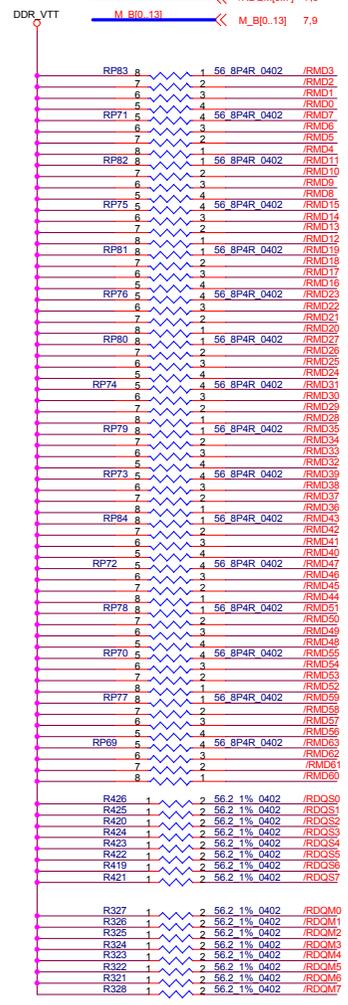


Title	G600 DDR-1	
Size	Document Number	Rev
	G600-1.4-01	1.0
Date:	Friday, September 02, 2005	Sheet 9 of 44

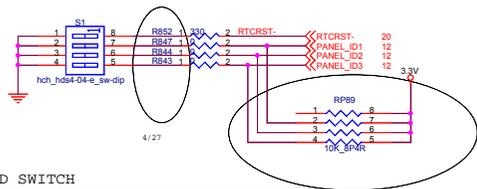
6.9 CKE[0..3] >> CKEID_3 2/4

/RMD[0..63] << /RMD[0..63] 7.9
/RDQS[0..7] << /RDQS[0..7] 7.9
M_A[0..13] << M_A[0..13] 7.9

M_BS[0..11] << M_BS[0..11] 7.9
/RDQM[0..7] << /RDQM[0..7] 7.9
M_B[0..13] << M_B[0..13] 7.9

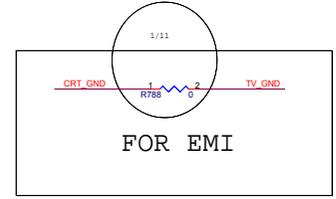
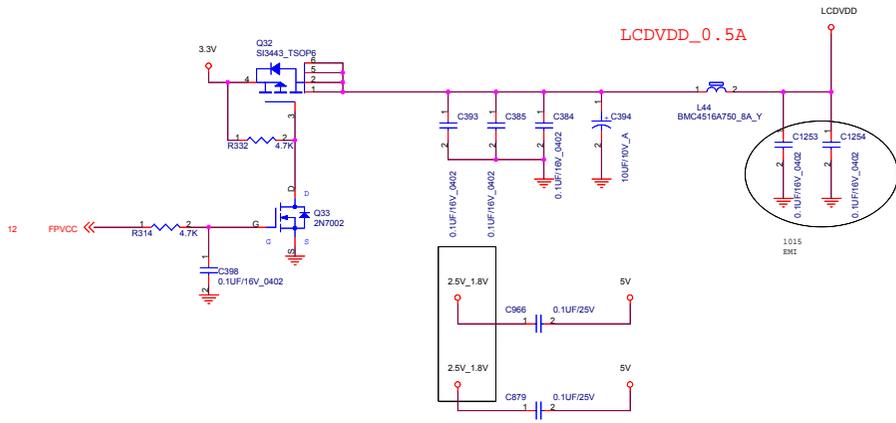
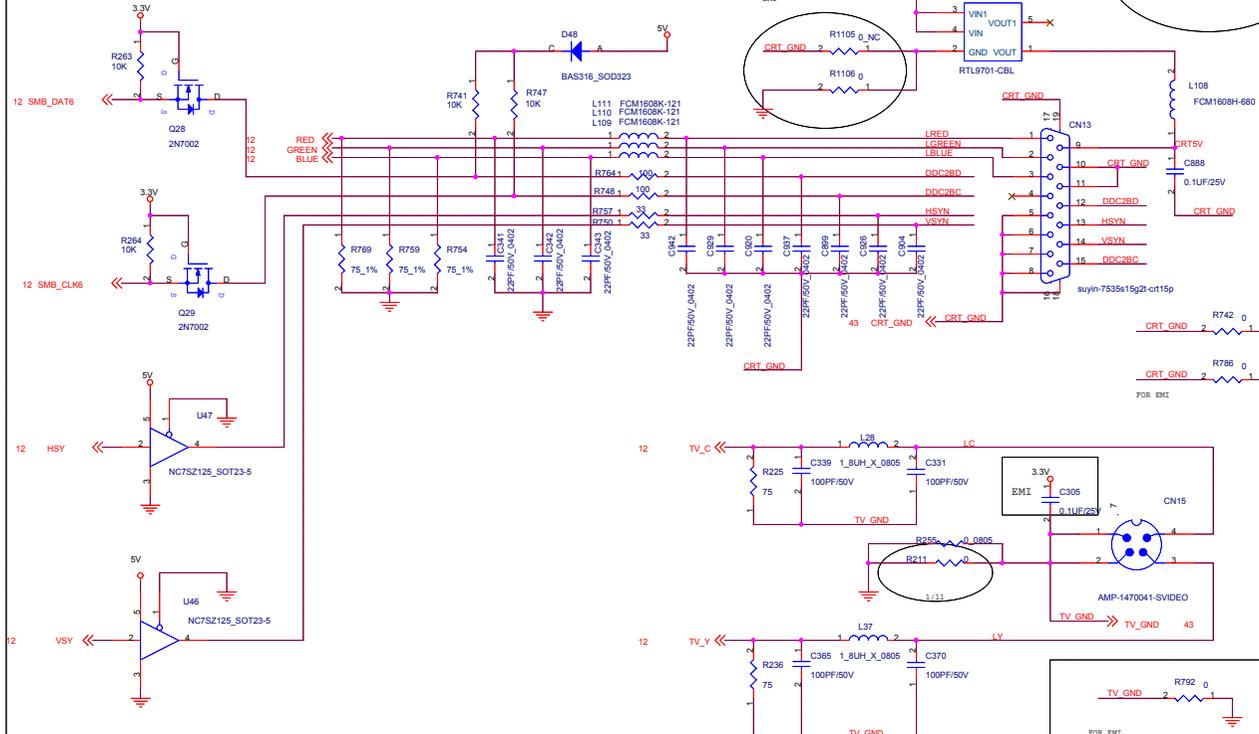
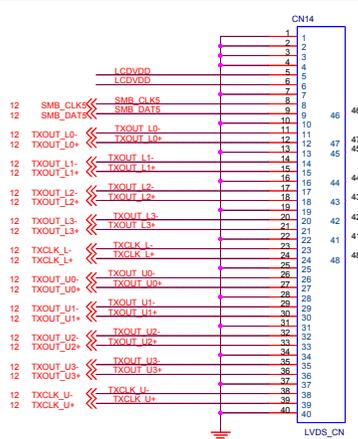


Title		
G600 DDR-2		
Size	Document Number	Rev
G600-1-4-01		1.0
Date:	Friday, September 02, 2005	Sheet 10 of 44



PANEL ID SWITCH

ID1	ID2	ID3	PANEL TYPE	ON:GND	OFF:3.3V
0	ON	ON	1280*800		
1	OFF	ON	AU		
2	ON	OFF	ON		
3	OFF	OFF	ON		
4	ON	ON	OFF		
5	OFF	ON	OFF		
6	ON	OFF	OFF		
7	OFF	OFF	OFF		Default, LG, TORISAN



FOR EMI

7/13 FOR M26 1.8V

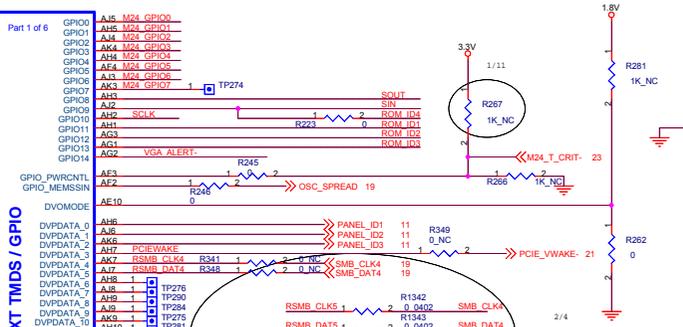
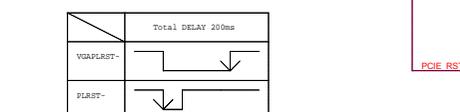
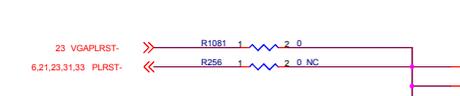
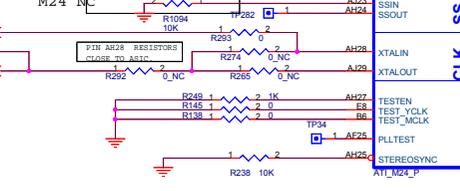
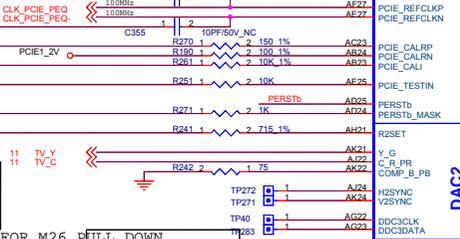
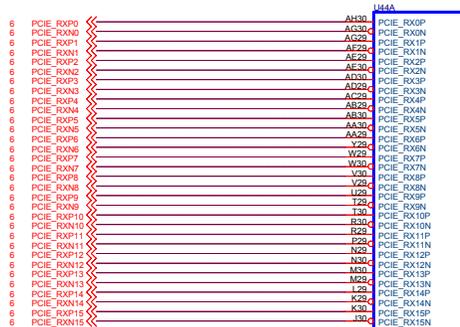
REFER TO INTEL
PCI EXPRESS
DESIGN GUIDE
FOR RECOMMENDED
AC COUPLING
CAPS PLACEMENT
ALONG THE TX
INTERCONNECT

PLACE TV
TERMINATION
RESISTORS CLOSE
TO ASIC.

7/15 R1094 FOR M26 FULL DOWN
M24 NC

OSC1.8

23 VGAPLRS-
6,21,23,31,33 PLRST-



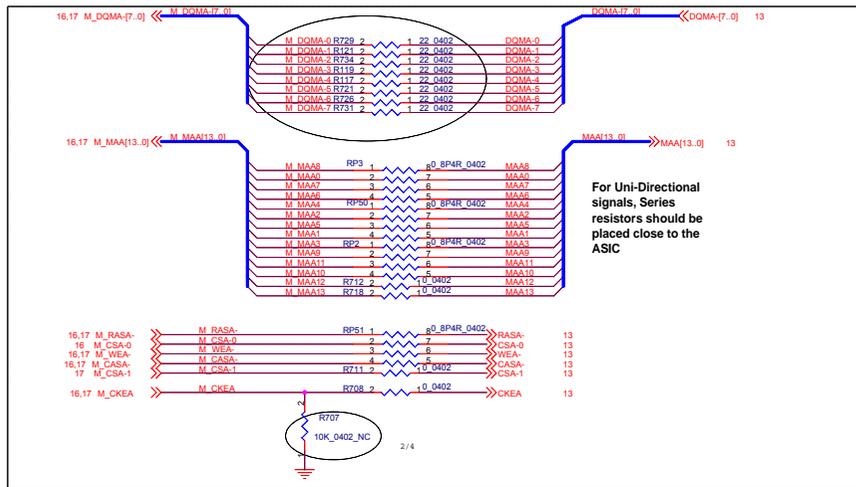
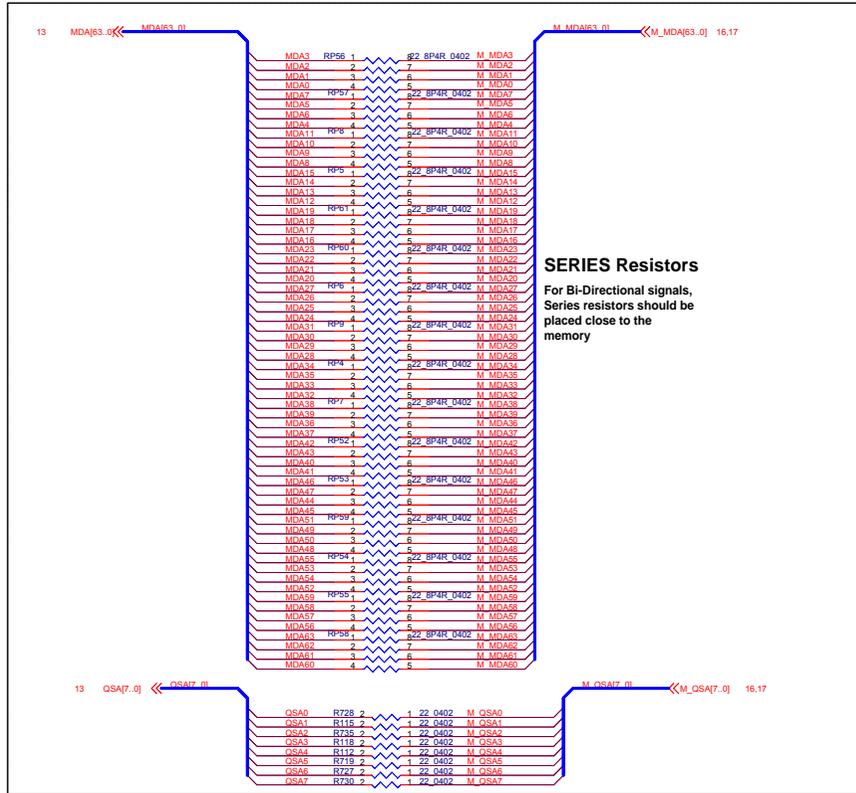
TERMINATION FOR MEMORY CHANNEL A

PARALLEL TERMINATION RESISTORS AND DECOUPLING

SSTL_2 CLASS 1

PLACE AT NETS MID POINT

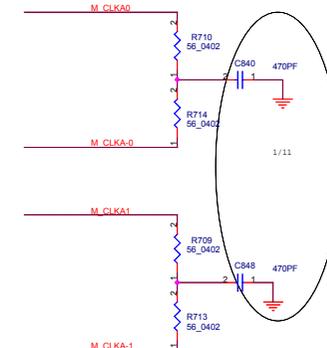
PARALLEL TERMINATION COULD BE OMITTED UNDER SOME CIRCUMSTANCES. PLEASE CONSULT WITH ANI FOR DETAIL.



CLOCK terminations

Change from 1:1 spacing to at least a 2.5:1 spacing between the pair

These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory



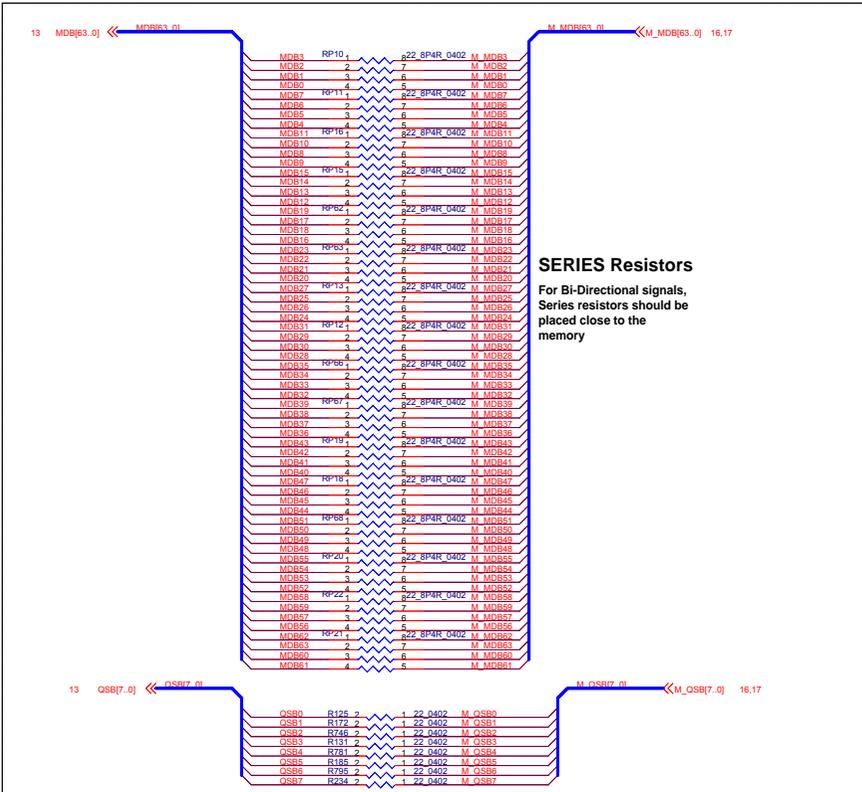
TERMINATION FOR MEMORY CHANNEL B

PARALLEL TERMINATION RESISTORS AND DECOUPLING

SSTL_2 CLASS I

PLACE AT NETS MID POINT

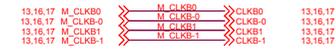
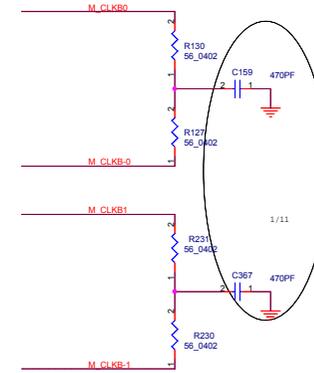
PARALLEL TERMINATION COULD BE OMITTED UNDER SOME CIRCUMSTANCE. PLEASE CONSULT WITH AN EXPERIMENTAL



CLOCK terminations

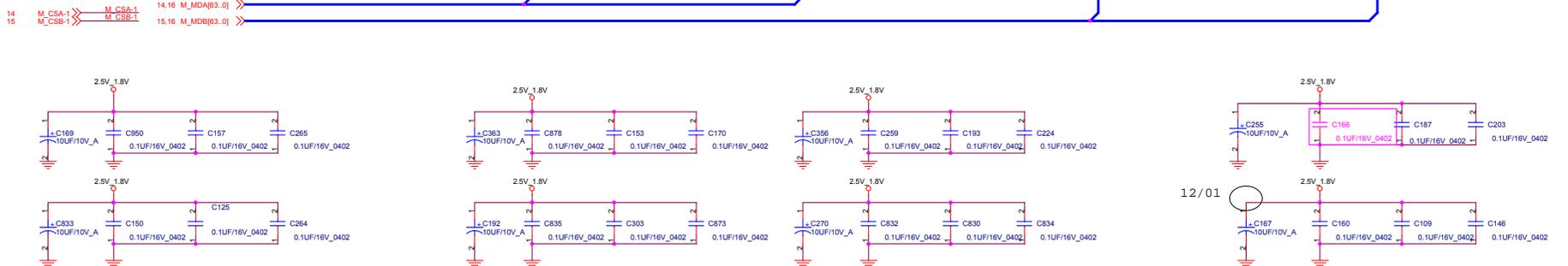
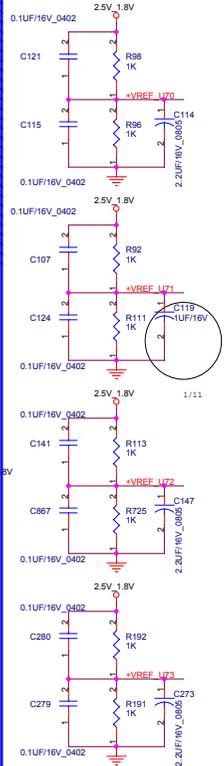
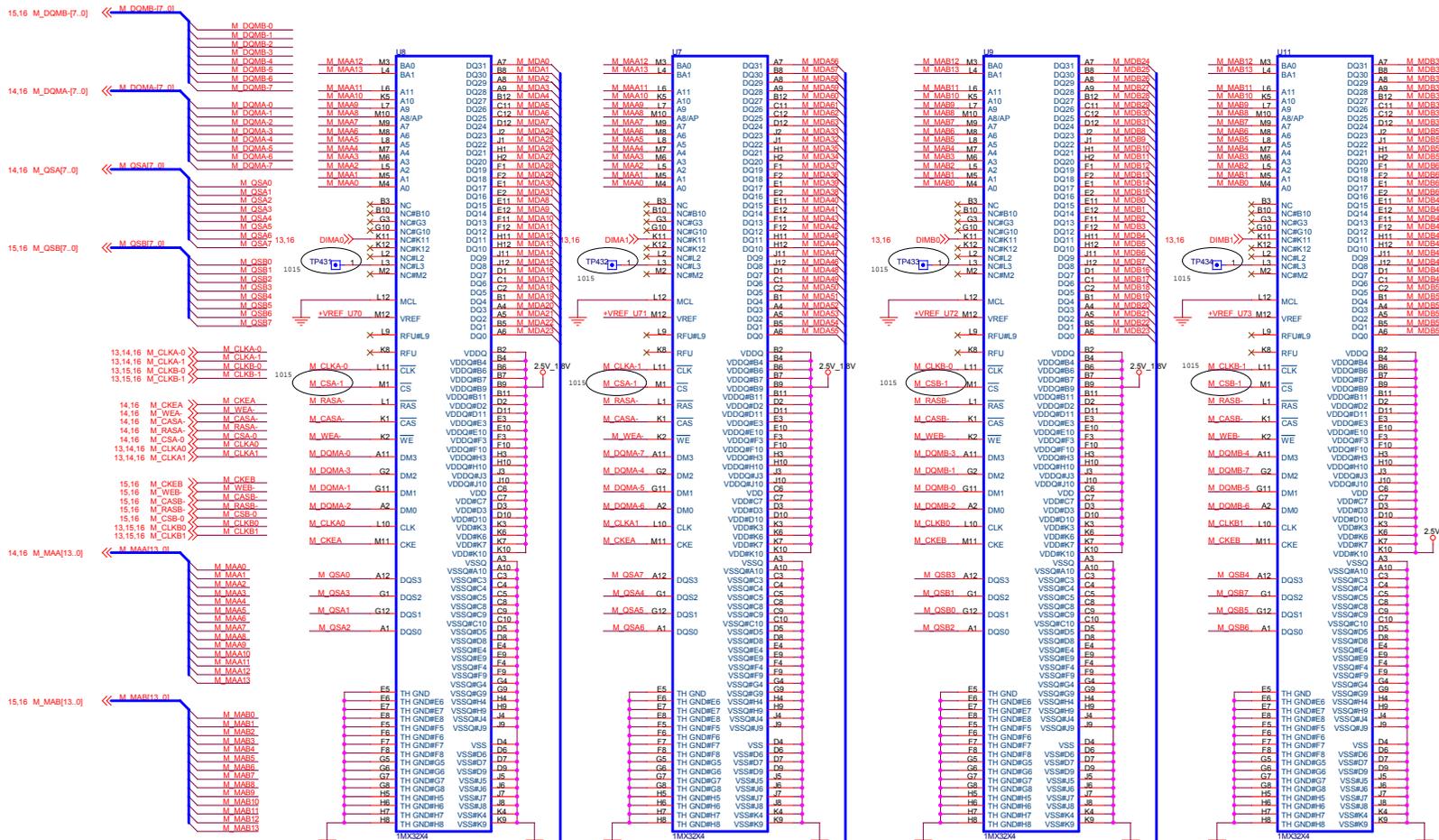
Change from 1:1 spacing to at least a 2.5:1 spacing between the pair

These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory



For Uni-Directional signals, Series resistors should be placed close to the ASIC

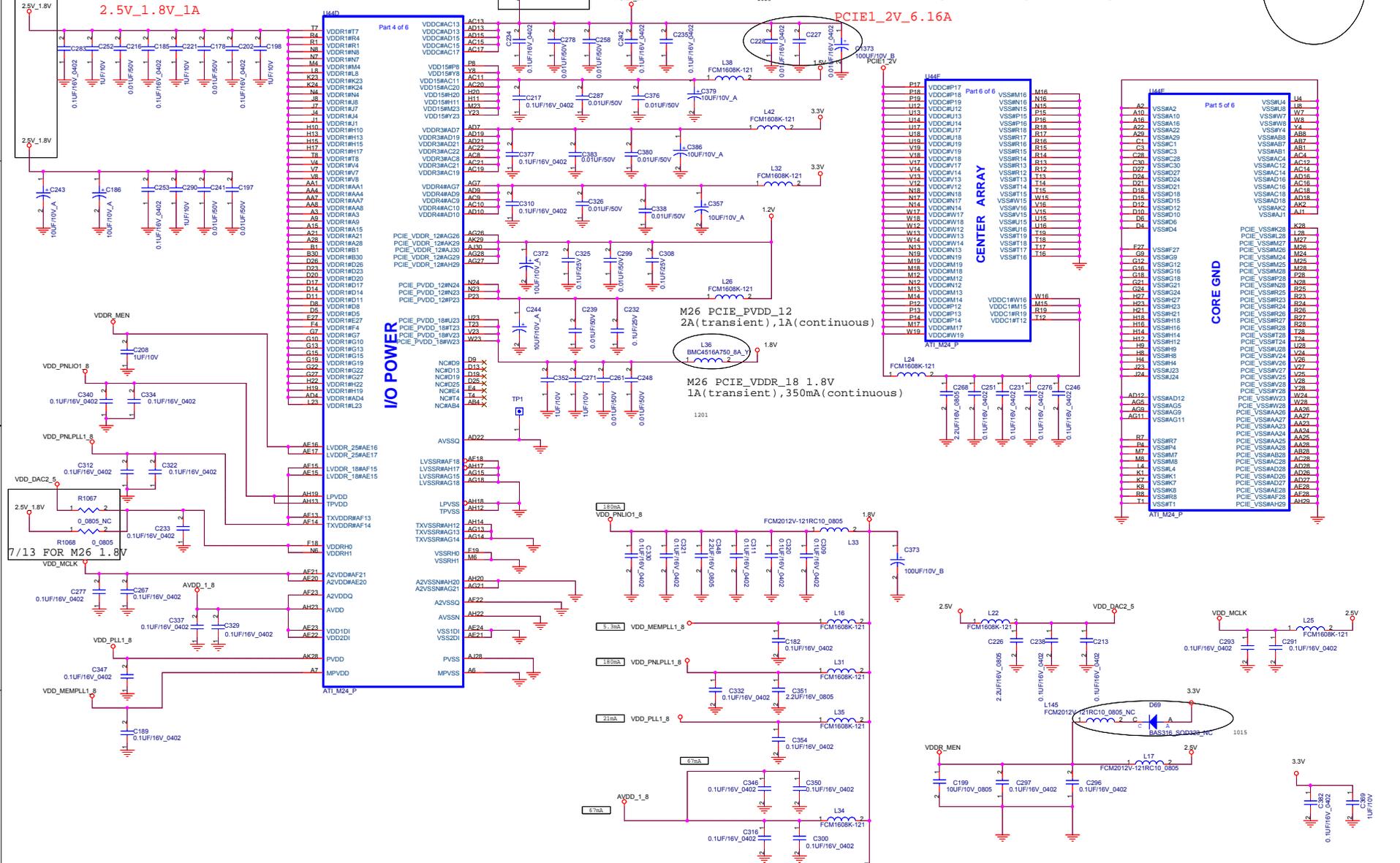
Title		
G600 M24 P-4		
Size	Document Number	Rev
C	G600-1-4-01	1.0
Date	Friday, September 02, 2005	Sheet 15 of 44



	NET NAME	VOLTAGE
M24	2.5V_1.8V	2.5V
M26	2.5V_1.8V	1.8V

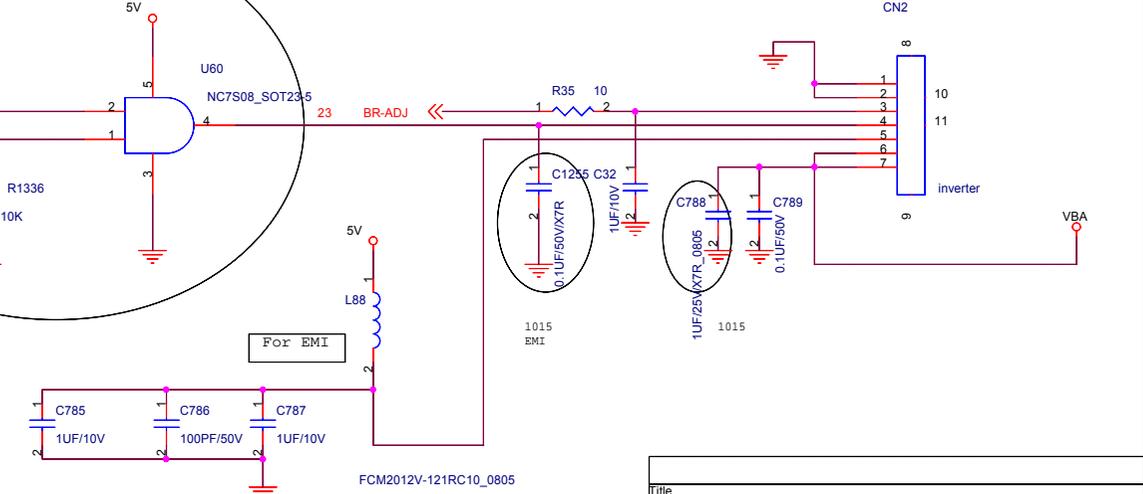
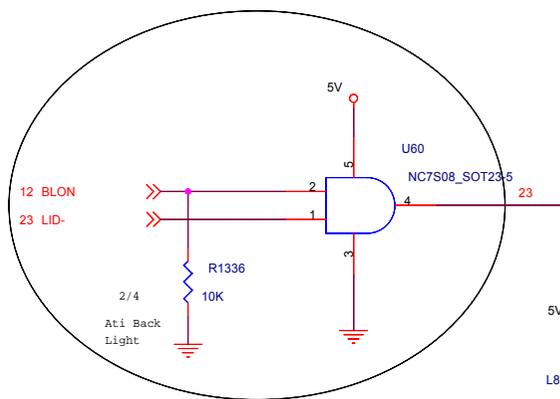
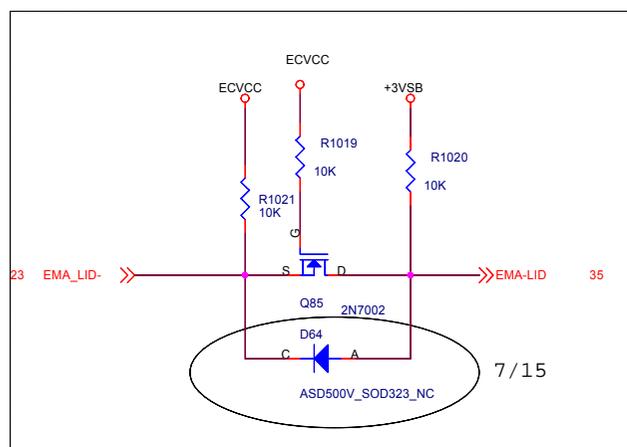
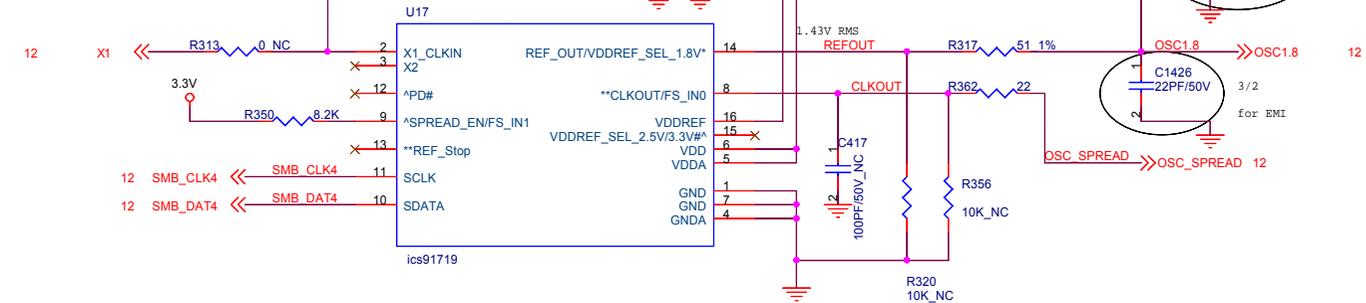
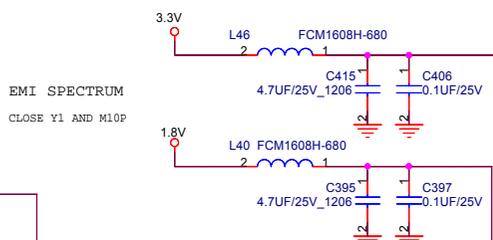
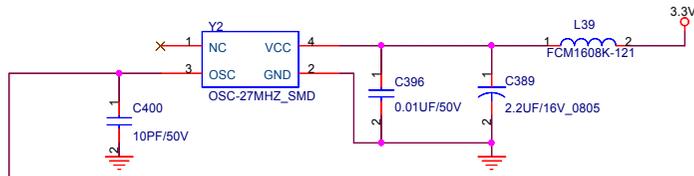
7/13 FOR M26 1.8V

**ADD DECouPLING FOR ALL POWER AS REQUIRED
PLACED CLOSE TO THE POWER/IO PINS
WITH AS MANY AS POSSIBLE PLACED UNDER THE ASIC**

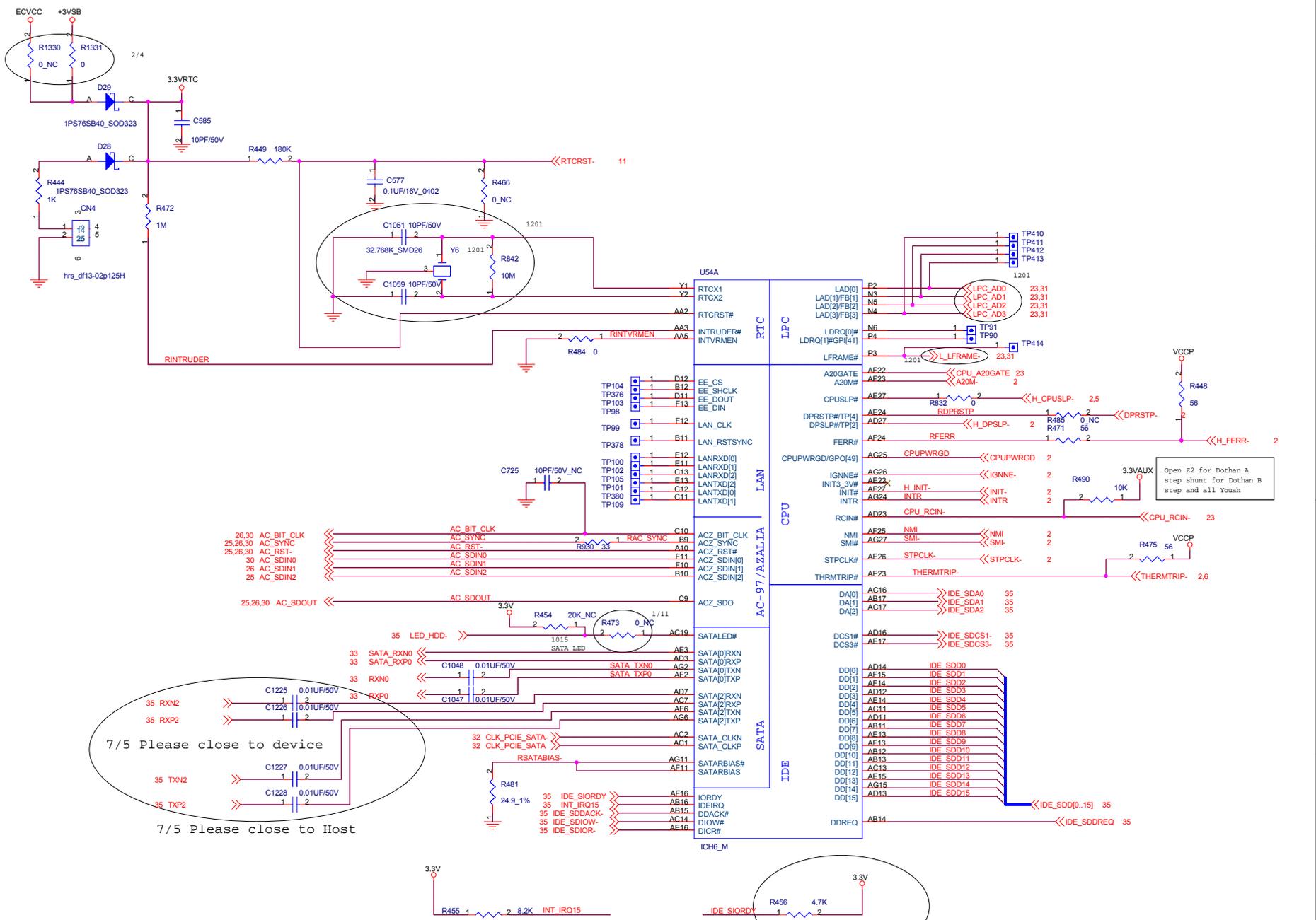


	NET NAME	VOLTAGE
M24	2.5V_1.8V	2.5V
M26	2.5V_1.8V	1.8V

Title		
G600 M24_P-5 (POWER)		
Size	Document Number	Rev
C	G600-1-4-01	1.0
Date	Friday, September 02, 2005	Sheet 18 of 44



Title		
G600 M24_P-6		
Size	Document Number	Rev
B	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 19 of 44

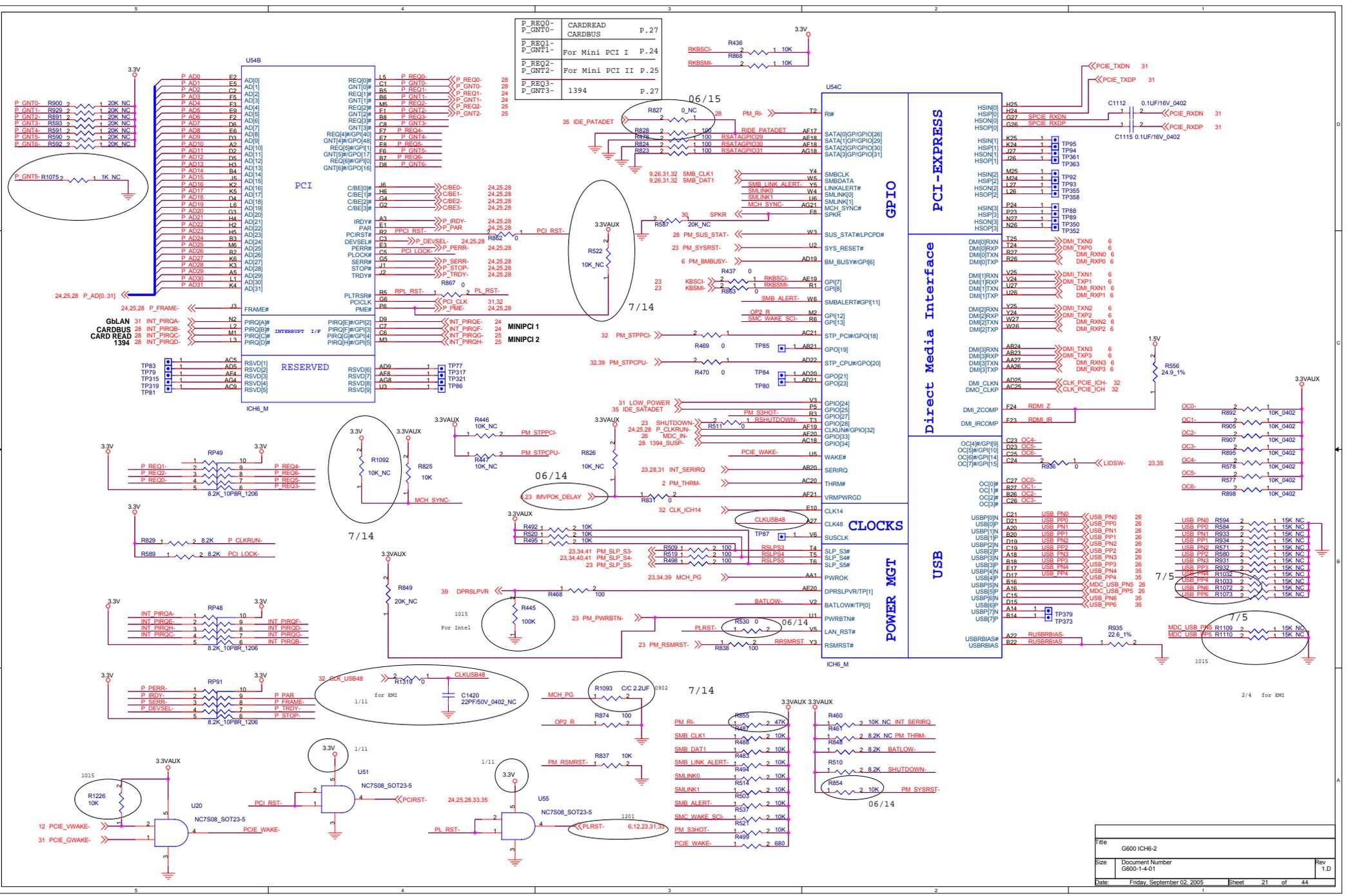


7/5 Please close to device

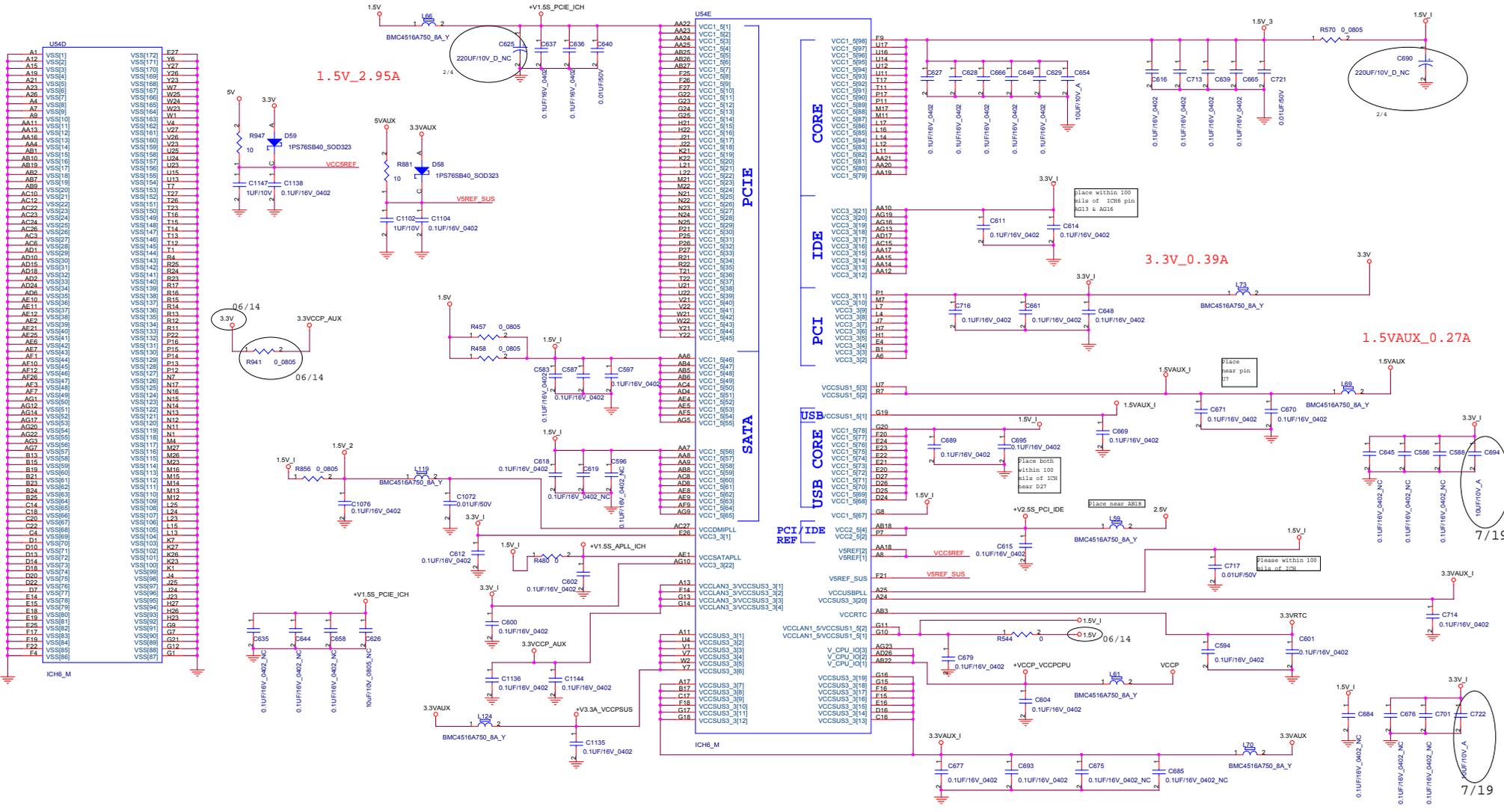
7/5 Please close to Host

Open Z2 for Doohan A
step shunt for Doohan B
step and all Youah

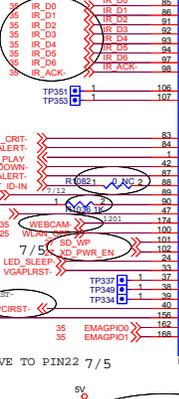
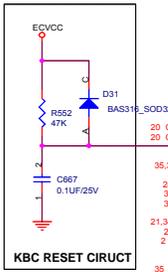
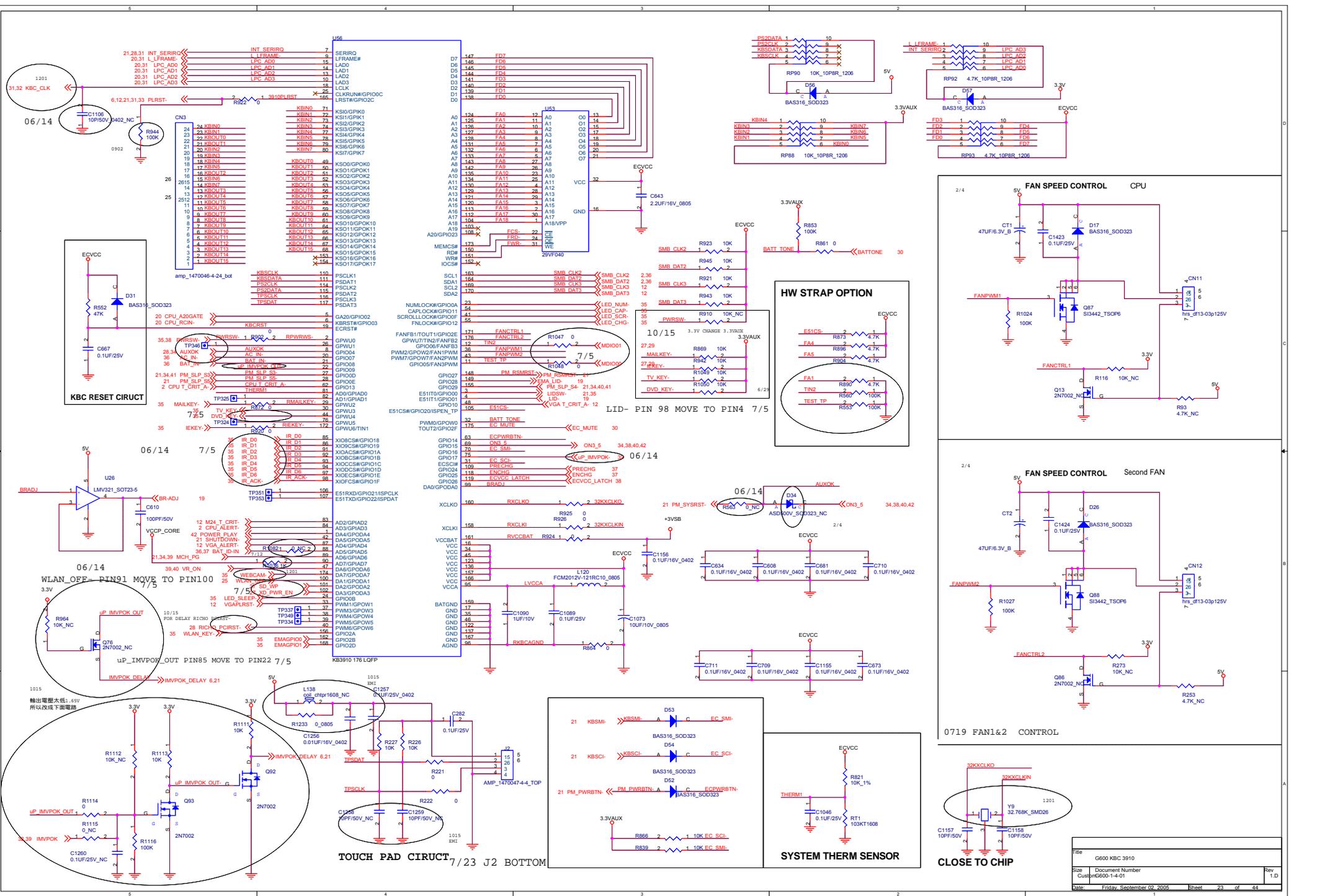
Title		
G600 ICH6-1		
Size	Document Number	Rev
G600-1-4-01		1.0
Date:	Friday, September 02, 2005	Sheet 20 of 44



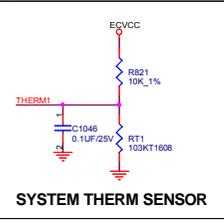
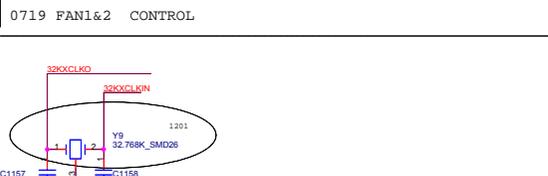
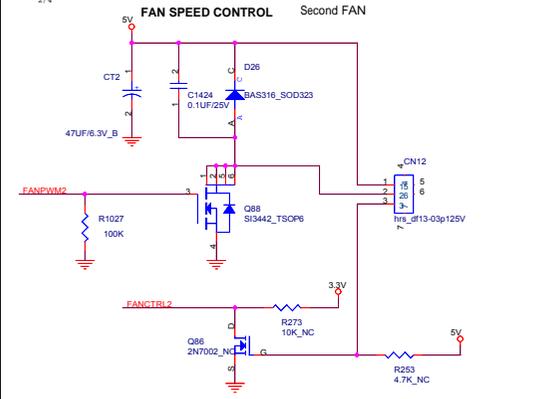
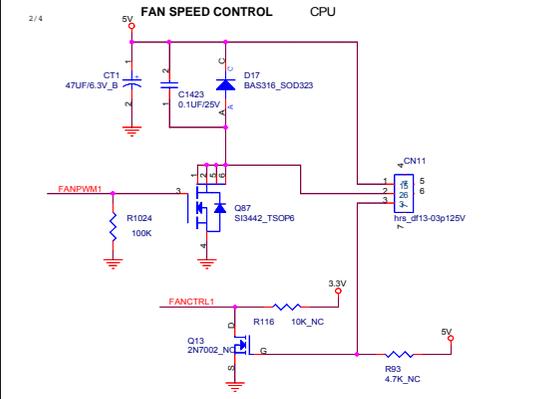
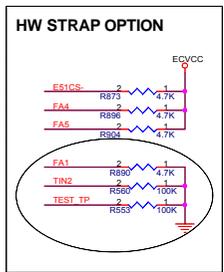
Title			G600 ICH6-2
Size			Document Number G600-1-4-01
Date	Friday, September 02, 2005	Sheet	21 of 44
Rev			I.D



Title	G600 ICH6-3	
Size	Document Number	Rev
	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 22 of 44



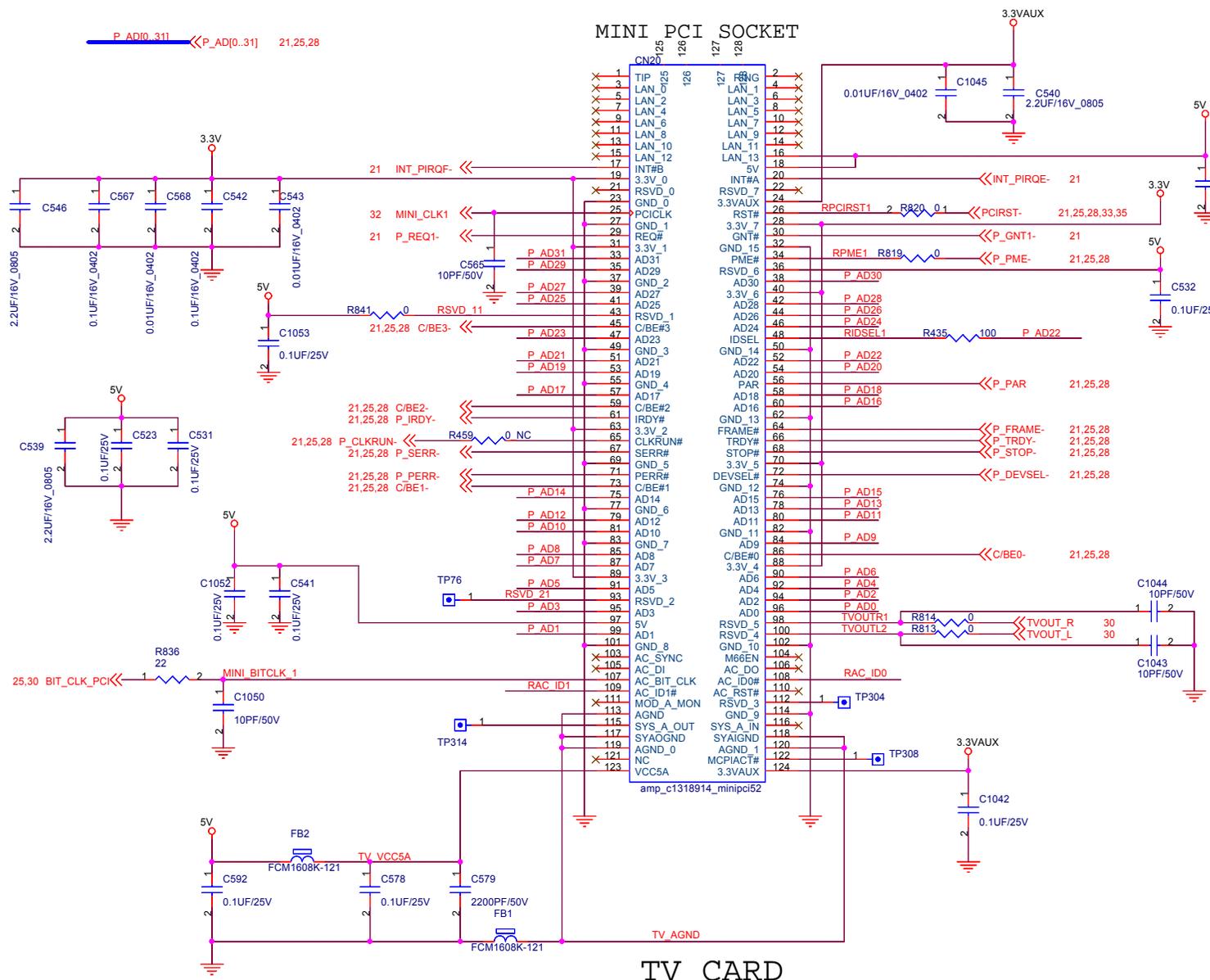
TOUCH PAD CIRCUIT 7/23 J2 BOTTOM



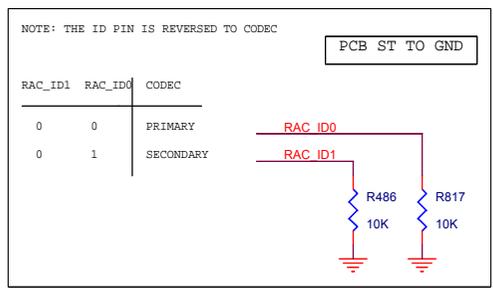
File	G600 KBC 3910	Rev	1.0
Size	Document Number	CustID	G600-1-4-01
Date	Friday, September 02, 2005	Sheet	23 of 44

MINI PCI SOCKET 1

MINI PCI SOCKET



MINI PCI POWER SPEC.
 TOYAL : 2W
 +5V : 100mA
 3.3VAUX : 5/200/375mA
 VCC5A : 100mA
 +3V

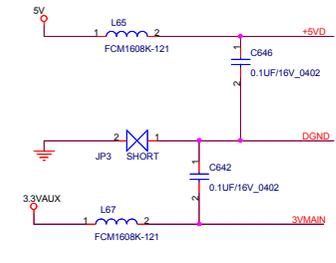
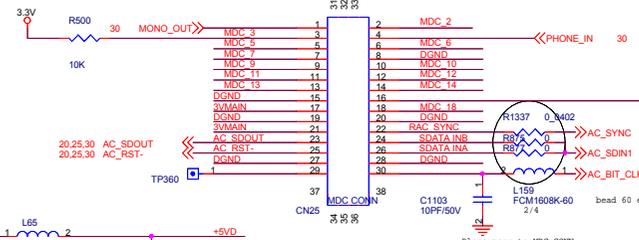


TV CARD

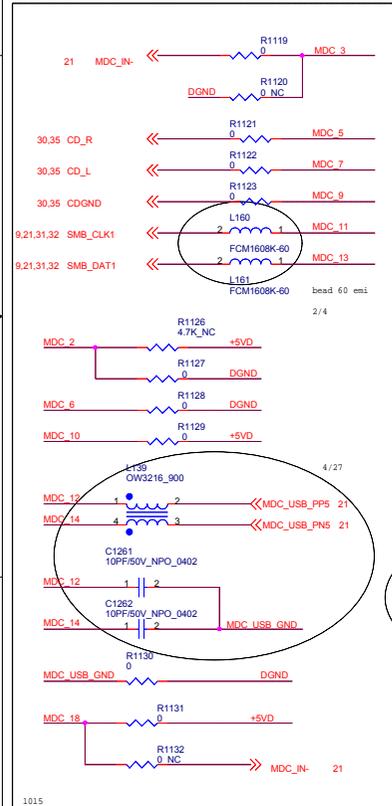
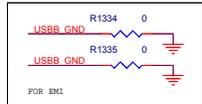
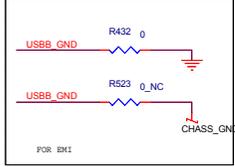
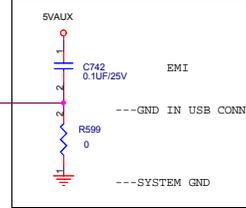
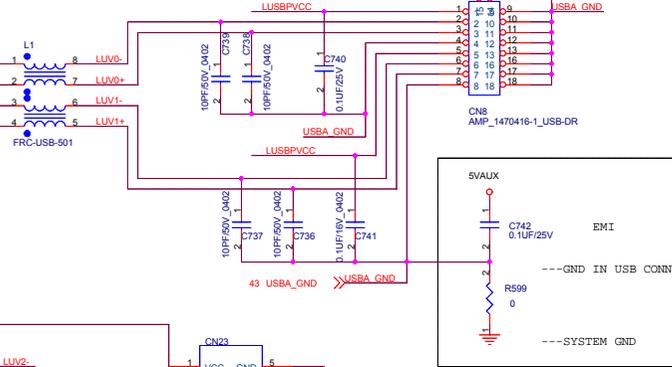
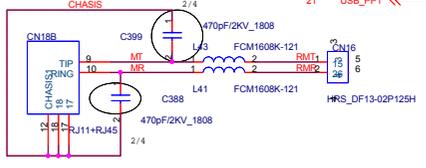
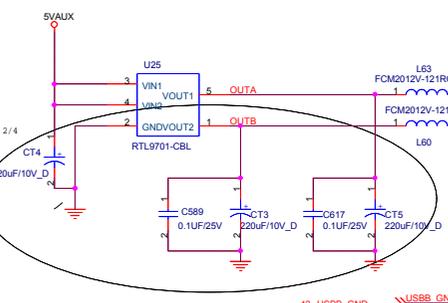
Title		
G600 MINI PCI 1		
Size	Document Number	Rev
B	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 24 of 44

Qcom MD560 (8) BLUETOOTH/MODEM COMBO CARD VER1A.FOR AZALIA				Qcom MB011SL BLUETOOTH/MODEM COMBO CARD VER1A.FOR AC97			
1	RESERVED	2	DETACH	1	MONO_OUT	2	DETACH
2	GND	4	RESERVED	3	GND	4	MONO_PHONE
5	RESERVED	6	BT_OH	5	AUXA_RIGHT	6	BT_OH
7	RESERVED	8	GND	7	AUXA_LEFT	8	GND
9	RESERVED	10	RESERVED	9	CD_GND	10	5V
11	CHANNEL_CLK	12	USB+	11	RESERVED	12	USB+
13	CHANNEL_DATA	14	USB-	13	RESERVED	14	USB-
15	GND	16	RESERVED	15	GND	16	PRIMARY_DM
17	3.3V/RT_VCC	18	WAKEUP	17	3.3V/RT_VCC	18	WAKEUP
19	GND	20	GND	19	GND	20	GND
21	3.3V	22	SYNC	21	3.3V	22	SYNC
23	SDATA_OUT	24	SDATA_INTB	23	SDATA_OUT	24	SDATA_INTB
25	RESET#	26	SDATA_INTA	25	RESET#	26	SDATA_INTA
27	GND	28	GND	27	GND	28	GND
29	LED_DRIVER	30	BITCLK	29	LED_DRIVER	30	BITCLK

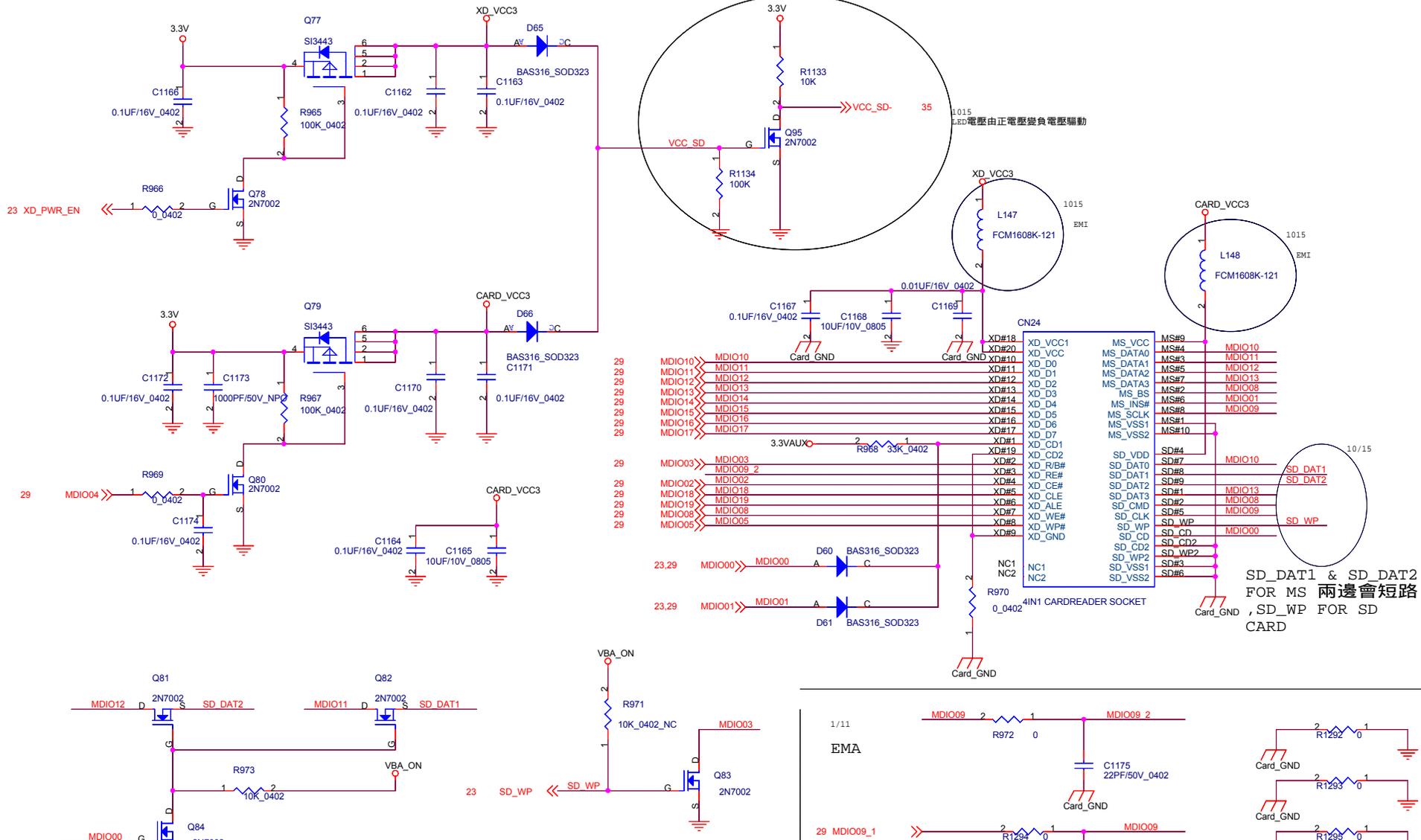
MDC



5VAUX_2A

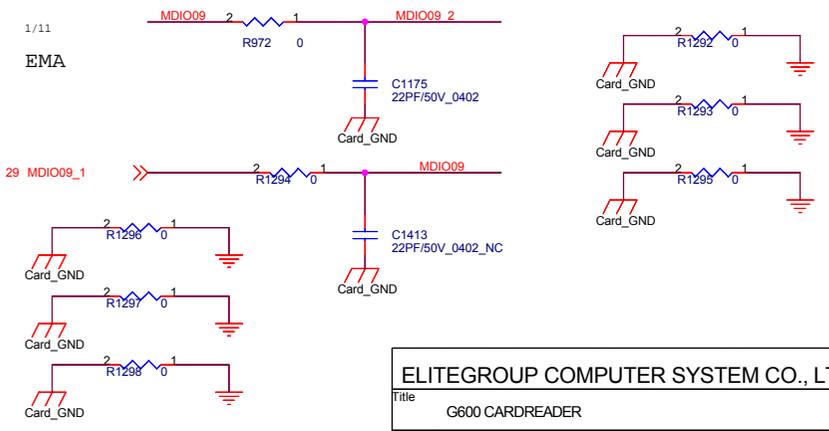


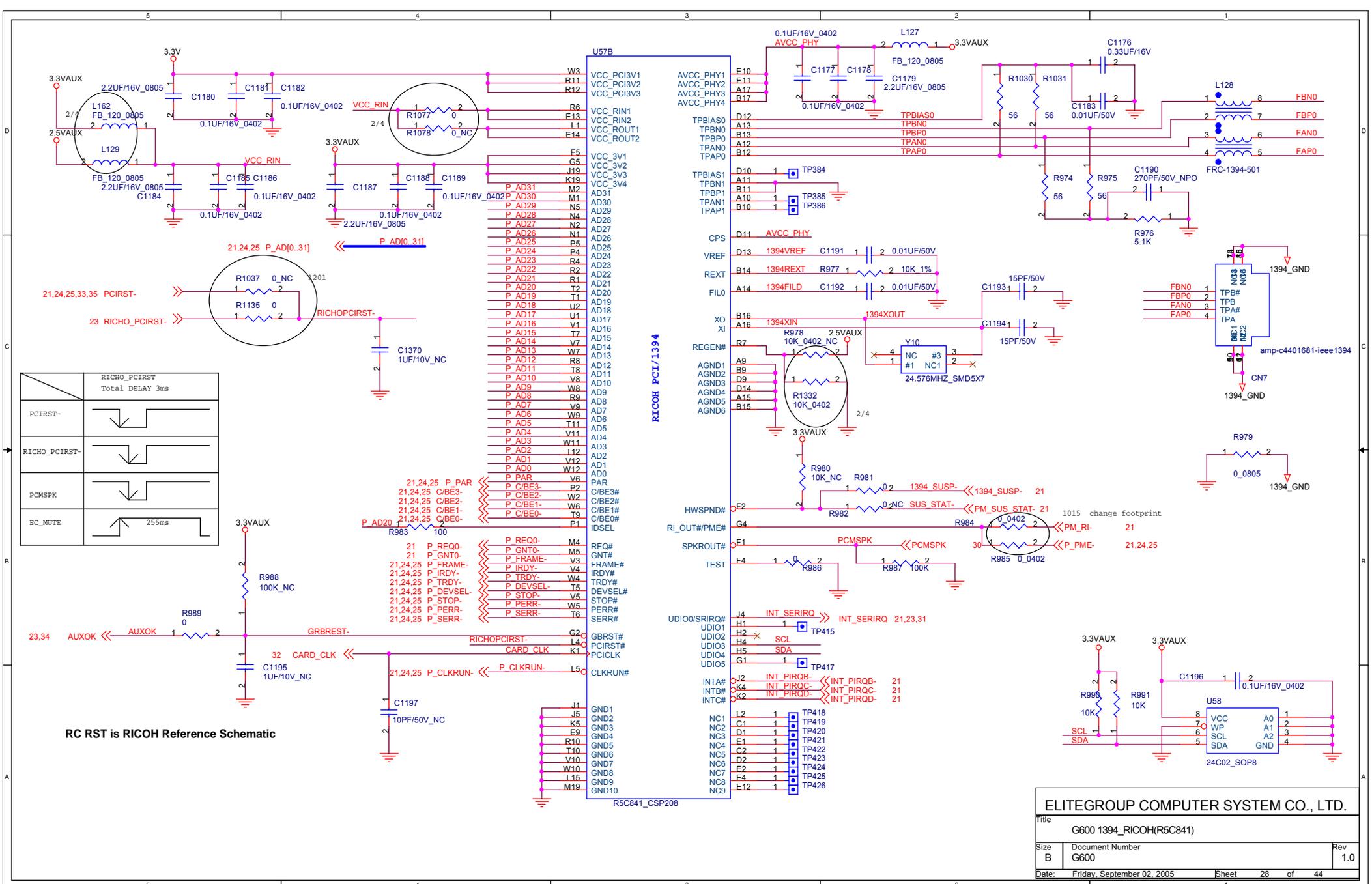
Title		
G600 MDC + USB		
Size	Document Number	Rev
G600-1-4-01		1.A
Date	Friday, September 02, 2005	Sheet 26 of 44

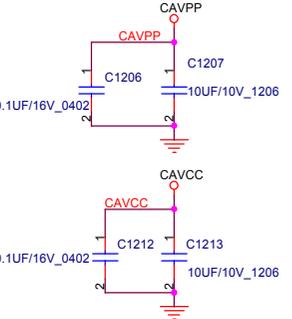
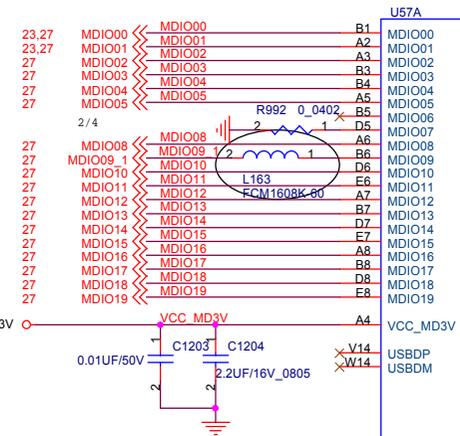
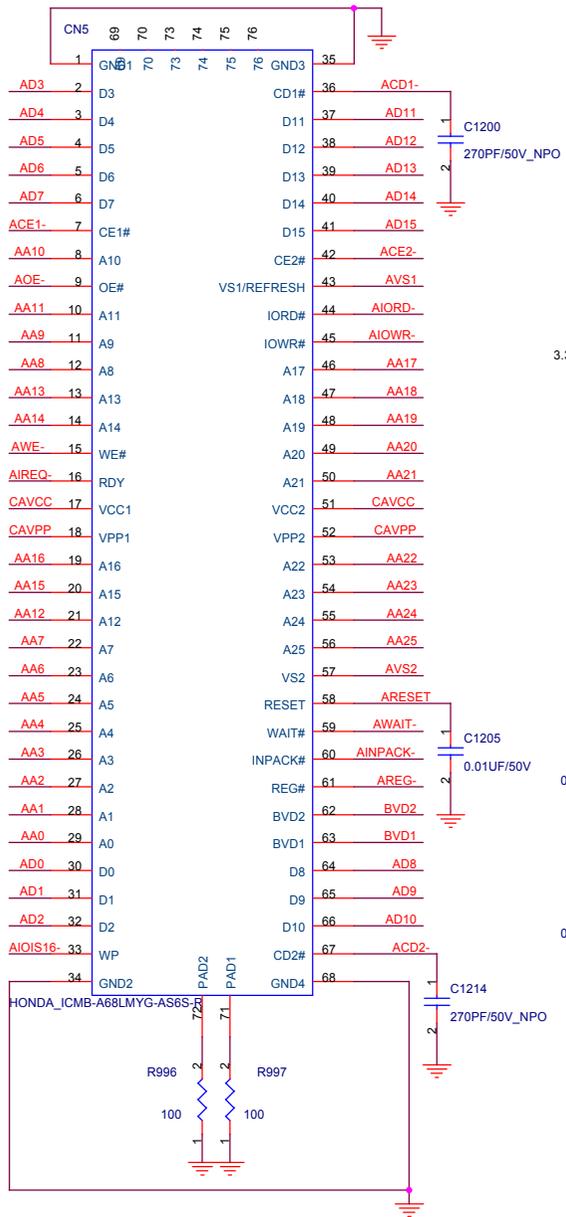


CARD READER FUNCTION TABLE

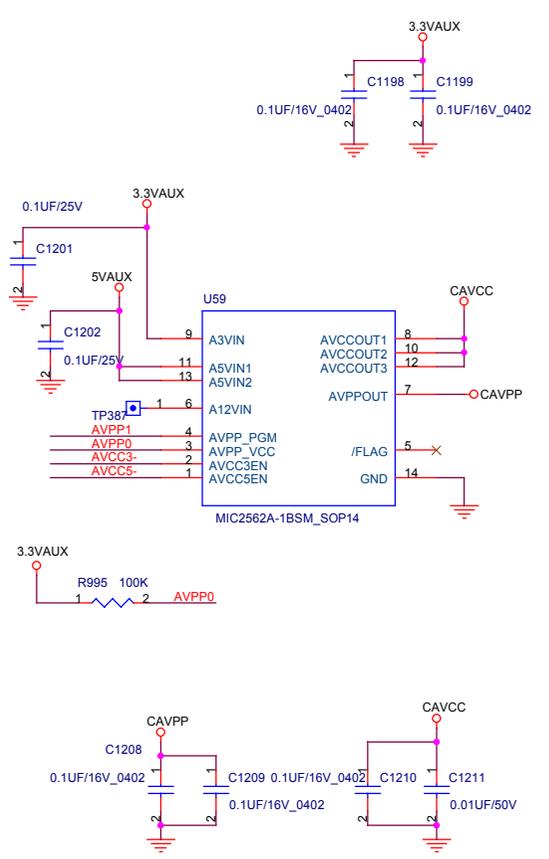
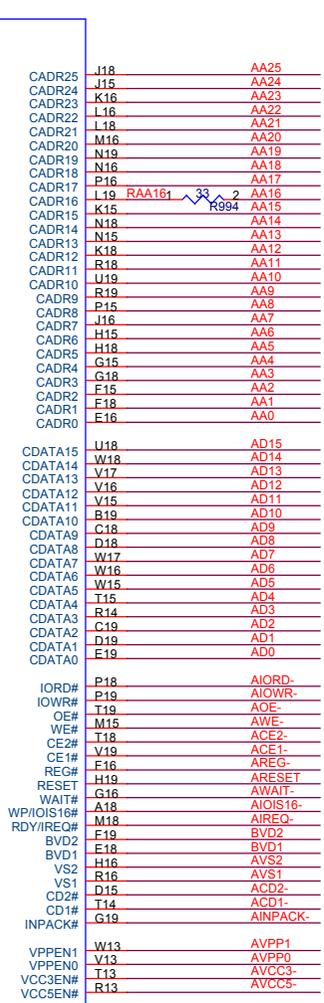
	XD	SD	MS	NO CARD
MDIO0(IN)	L	L	H	H
MDIO1(IN)	L	H	L	H
XD_PWR_EN(OUT)	H	L	L	L
SD_WP(OUT)	L	H	L	L



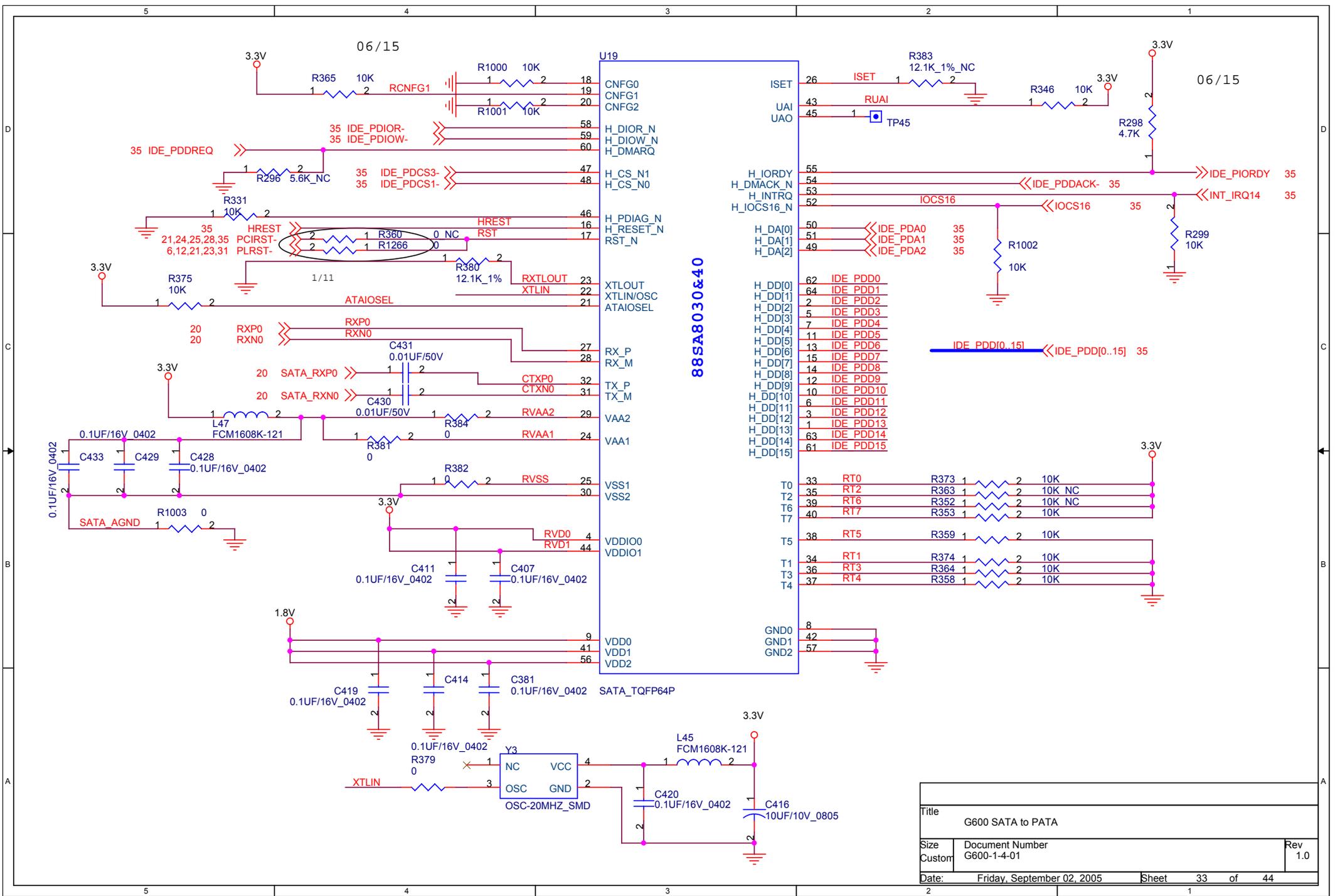




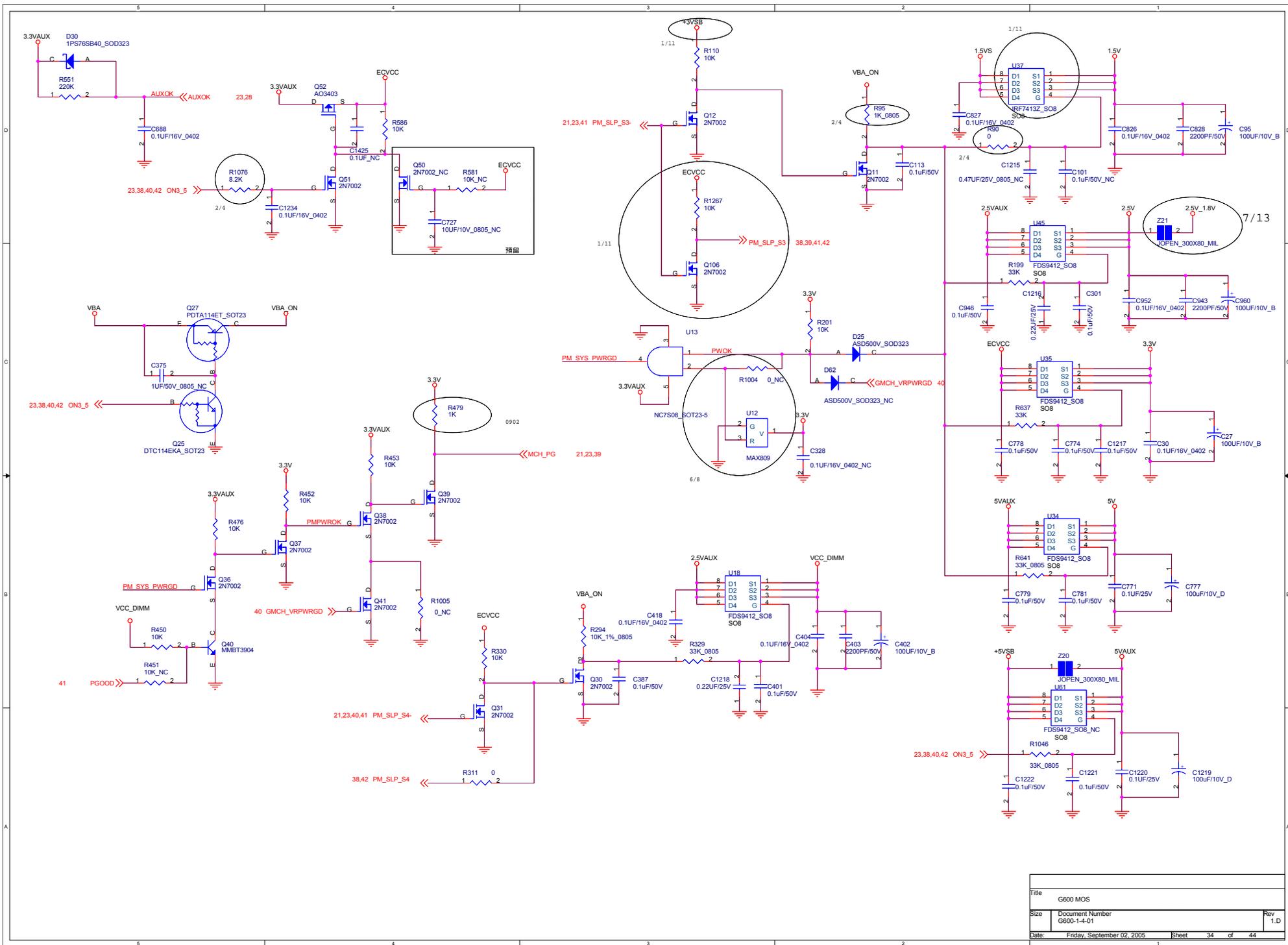
RICOH CARDBUS/CARDREADER



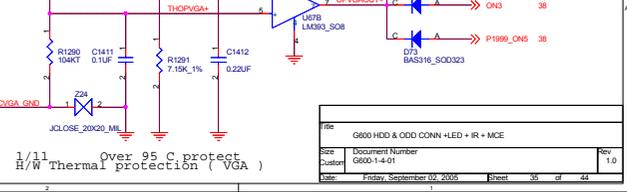
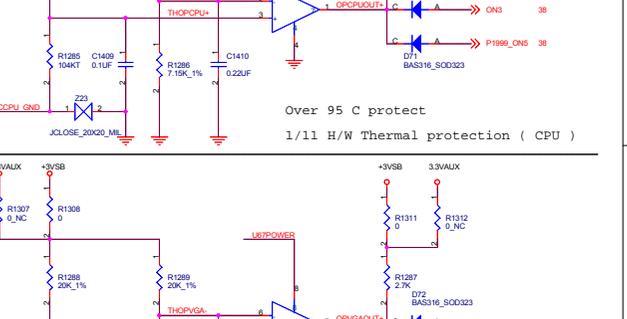
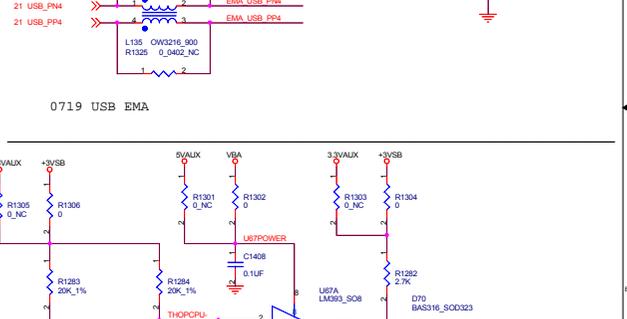
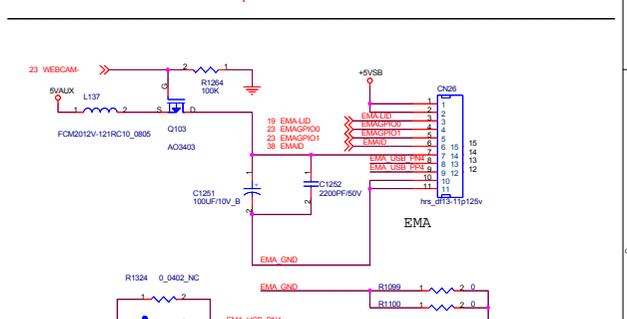
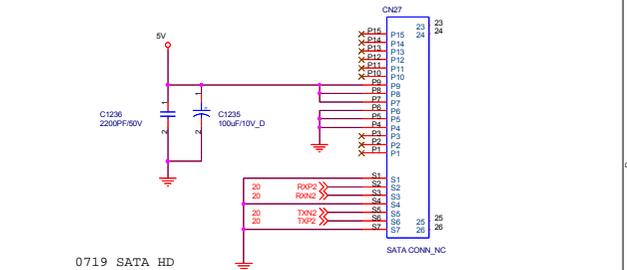
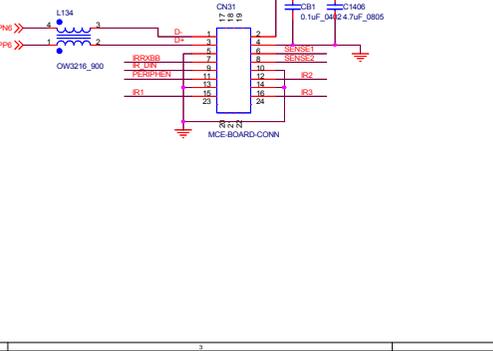
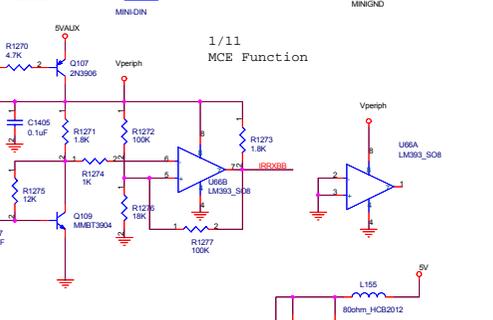
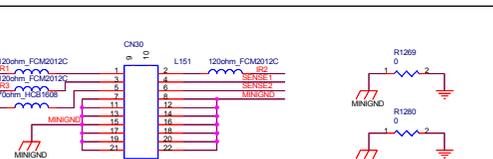
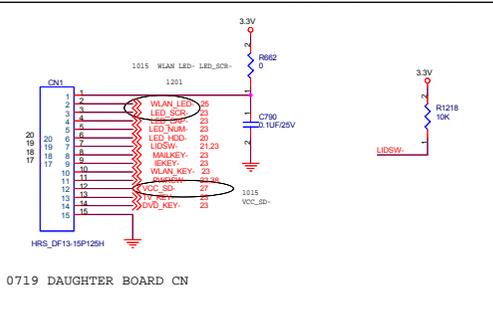
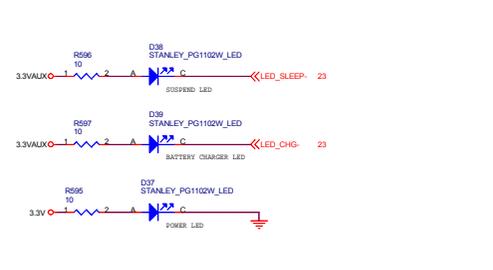
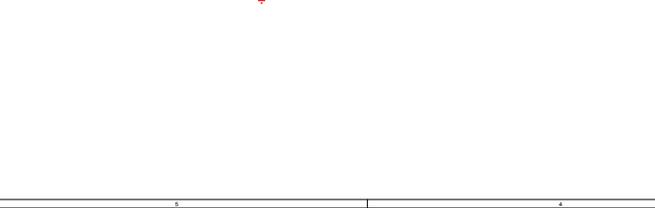
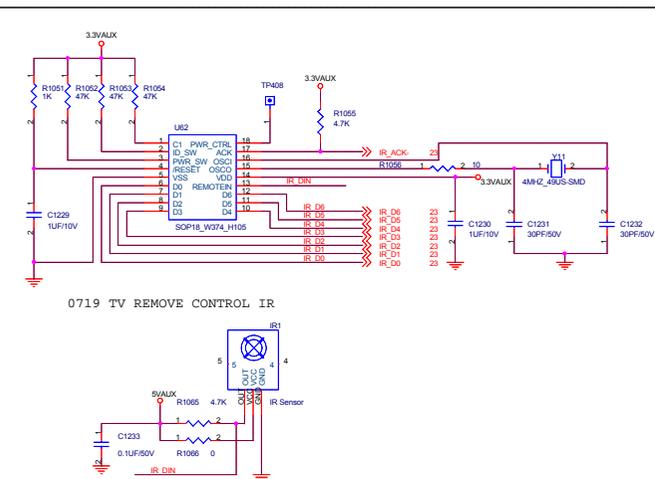
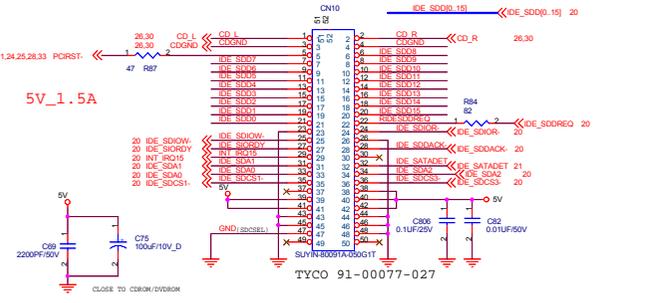
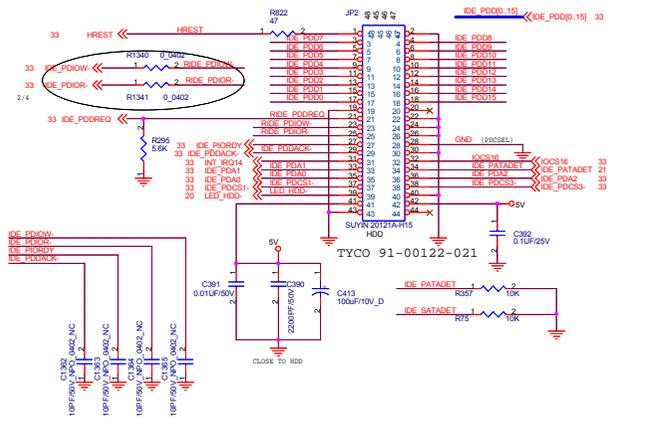
ELITEGROUP COMPUTER SYSTEM CO., LTD.			
Title G600 CARDBUS/CARDREADER R5C841			
Size B	Document Number G600	Rev 1.0	
Date: Friday, September 02, 2005	Sheet 29	of 44	



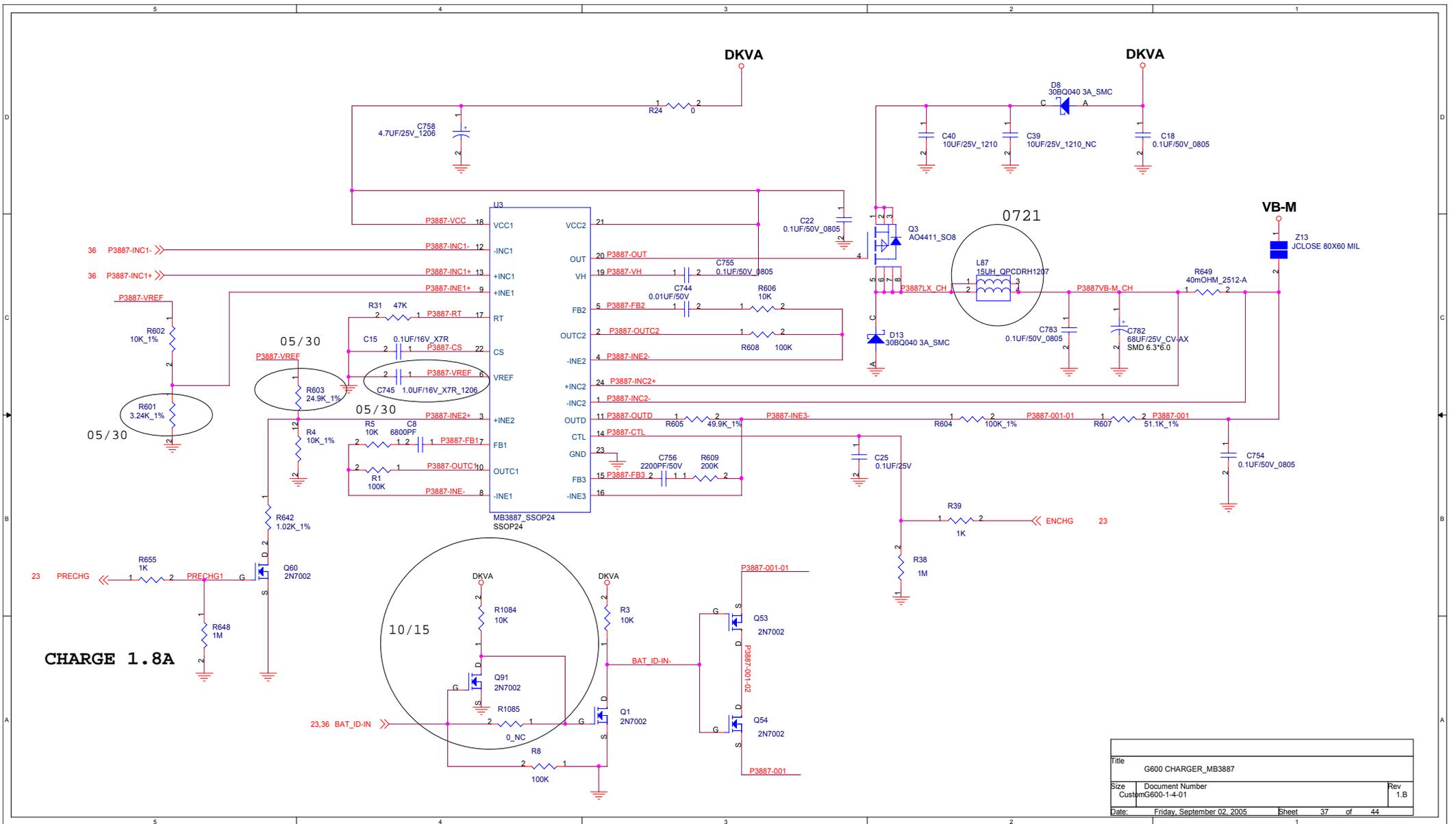
Title		
G600 SATA to PATA		
Size	Document Number	Rev
Custom	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 33 of 44



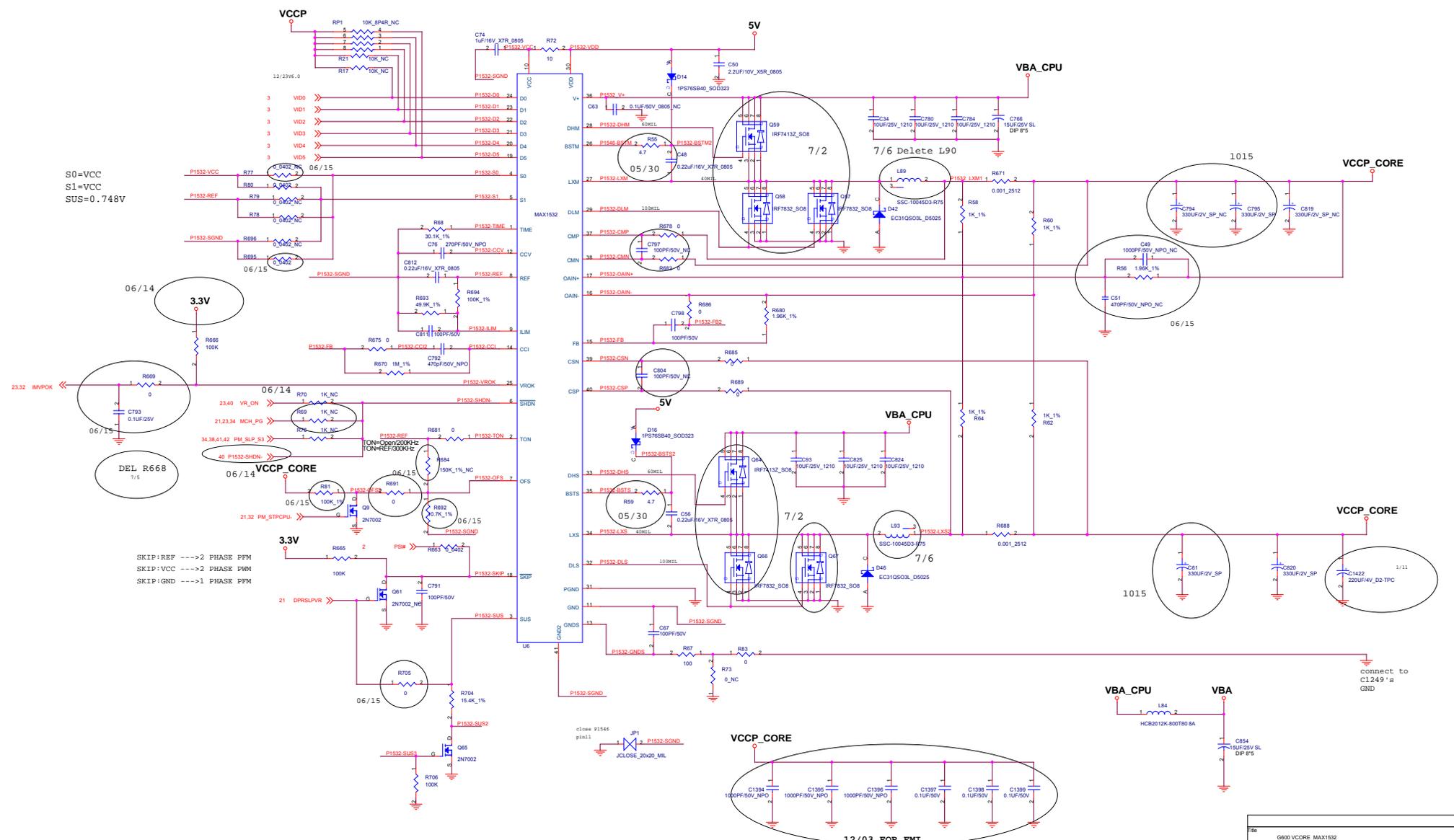
Title	G600 MOS	
Size	Document Number	Rev
	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 34 of 44



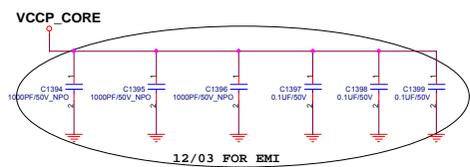
File	G600 HDD & ODD CONN +LED +IR + MCE	Rev	1.0
Date	Document Number		
Custom	G600-1-4-01		
Date	Friday, September 02, 2010	Sheet	35 of 44



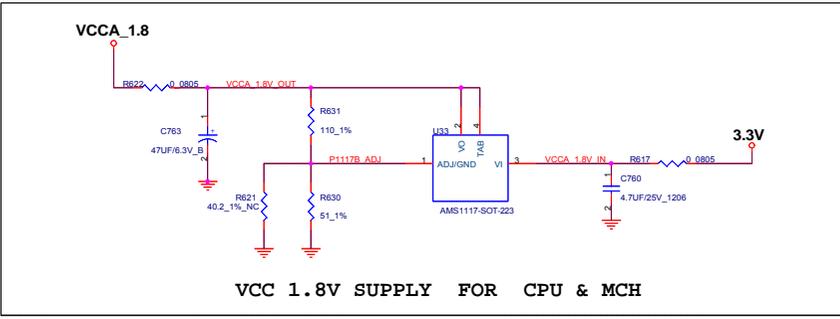
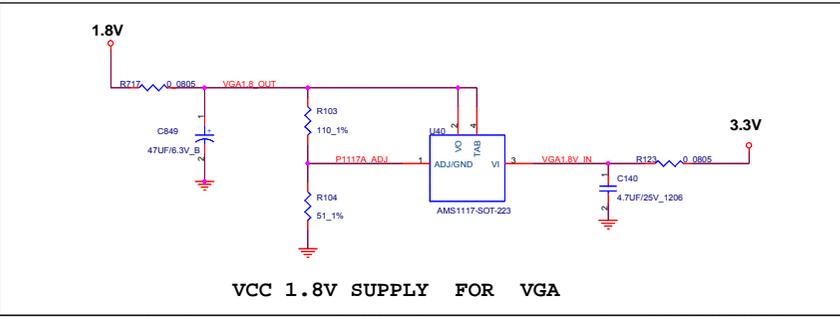
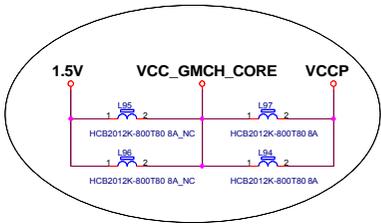
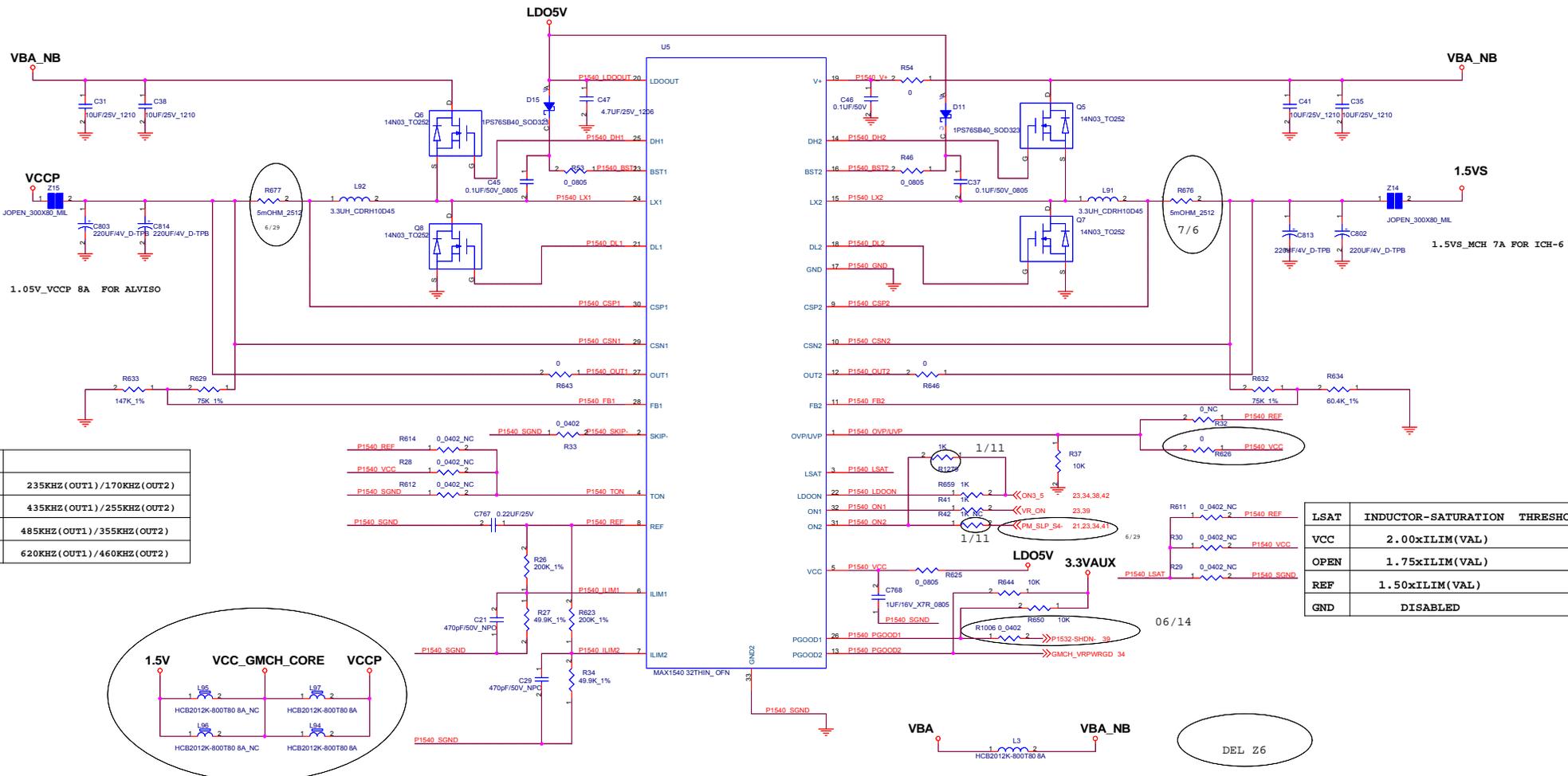
S0=VCC
S1=VCC
SUS=0.748V

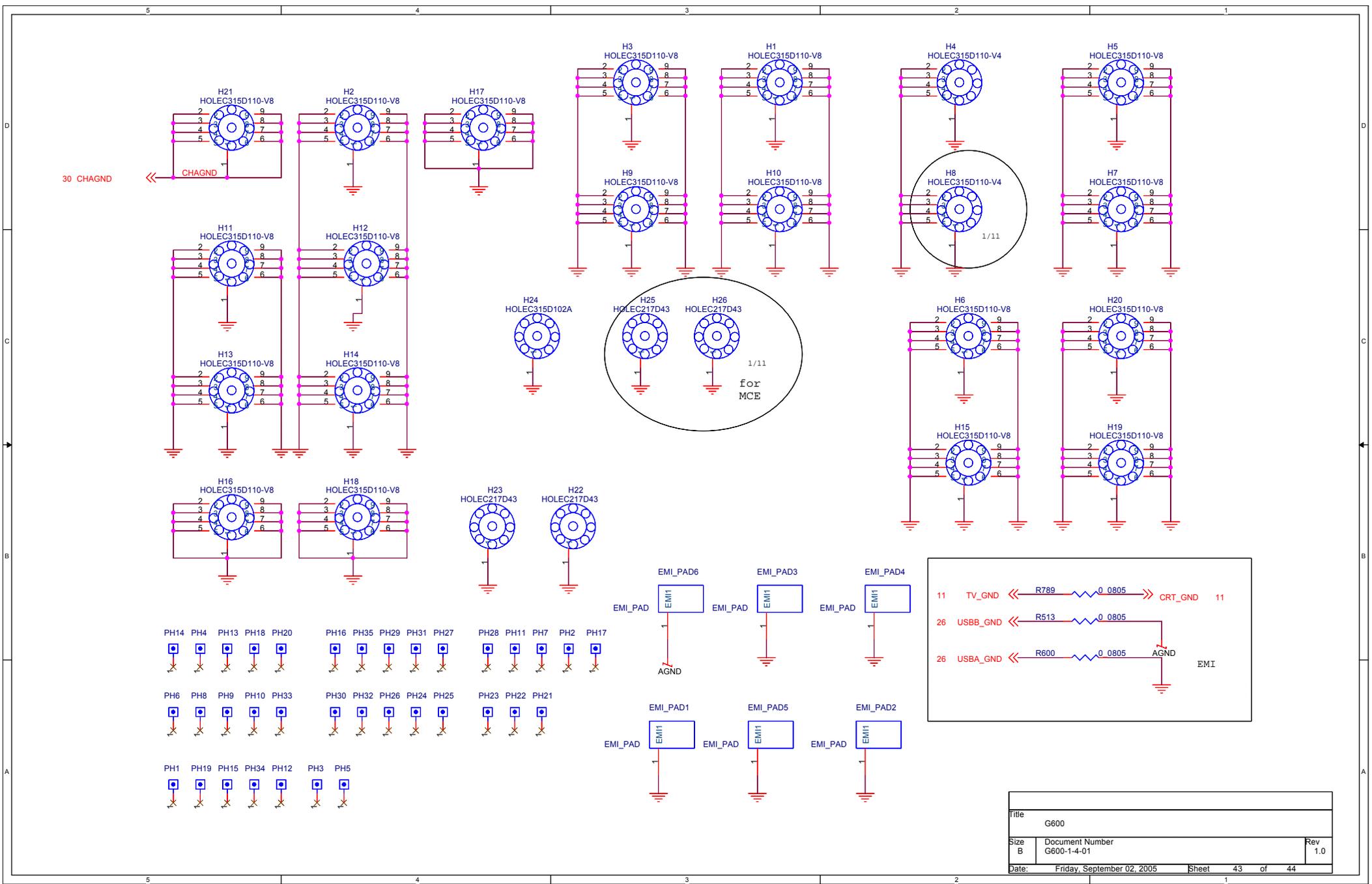


SKIP:REF --->2 PHASE PFM
SKIP:VCC --->2 PHASE PWM
SKIP:GND --->1 PHASE PFM

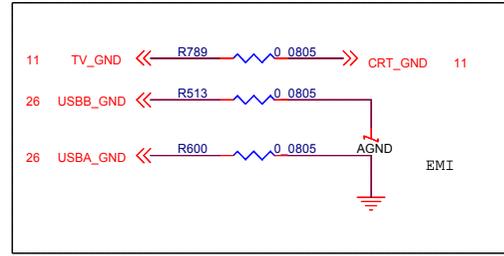


File	G600 VCCORE_MAX1512		
Size	Document Number		Rev
	Custom000-1-4-01		1.B
Date	Friday, September 02, 2005	Sheet	39 of 44





30 CHAGND



Title		
G600		
Size	Document Number	Rev
B	G600-1-4-01	1.0
Date:	Friday, September 02, 2005	Sheet 43 of 44

A.0 INITIAL 0519A
B.0 UPDATA 0719B
C.0 CHANGE ALC202 TO ALC655
D.0 MODIFILY
E.0 1.Add MCE function
 2.H/W Thermal protection (VGA&CPU)
F.0 1.CHANGE FAN CONTROL IC
 2.GBLAN MODIFILY
F.A 1.INSERT ADAPTER WILL RESTART CHANGE R166,C919
F.B 1.Add Speaker in and Micphone in Auto Detect
1.0 1.change MLB Ver F TO Ver 1.0
1.A 1.Add Q'com usb buletooth function.
 2.add panel ID select.
1.B RESISTOR AND CAPACITANCE CHANGE R601,R603,C745,R47,R55,R59
1.C ADD U12,DEL R1004, S3 USB2.0 FAIL
1.D DOS-XP BLACK SCREEN

Title		
<Title>		
Size B	Document Number G600-1-4-01	Rev 1.D
Date:	Friday, September 02, 2005	Sheet 44 of 44