
 QUANTA COMPUTER		
Title: Schematic Block Diagram2		
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Power & Ground

Label	Pg#	Description	Control Signal
DC_IN+		AC ADAPTER (20V)	
PBATT+		MAIN BATTERY +(10-17V)	
PWR_SRC		MAIN POWER (10-20V)	
RTC_PWR3_3V		RTC & PCL POWER (3_3V)	
+12V		+12V	DRUNPWROK
VHCORE		CPU CORE POWER (1.25/1.15V)	RUNPWROK
V1_2RUN		AGTL+ POWER (1.2V)	RUNPWROK
+3VRUN		SLP_S3# CTRLD POWER	RUN_ON
+3VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VALW		8051 POWER (5V)	
+5VRUN		SLP_S3# CTRLD POWER	RUN_ON
+5VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VHDD		HDD POWER (5V)	HDDC_EN#
+5VMOD		MODULE POWER (5V)	MODC_EN#
STRB#5V		EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1, +5VFAN2		FAN POWER (5V)	FAN_OFF/ON#
VDDA		AUDIO ANALOG POWER (5V)	RUN_ON
1_8VSUS		RESUME WELL IN ICH	
1_8VRUN		SLP_S3# CTRLD POWER	
+3VALW		8051 POWER (3V)	
V1_5RUN		AGP I/O POWER	
 GND	ALL PAGES	DIGITAL GROUND	
 AGND		AUDIO GND	
 GNDP		CPU POWER GND	
 CGNDP		CHARGER GND	
 DGNDP		DC/DC POWER GND	
 LANGND		COMBO CONN GND	

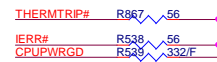
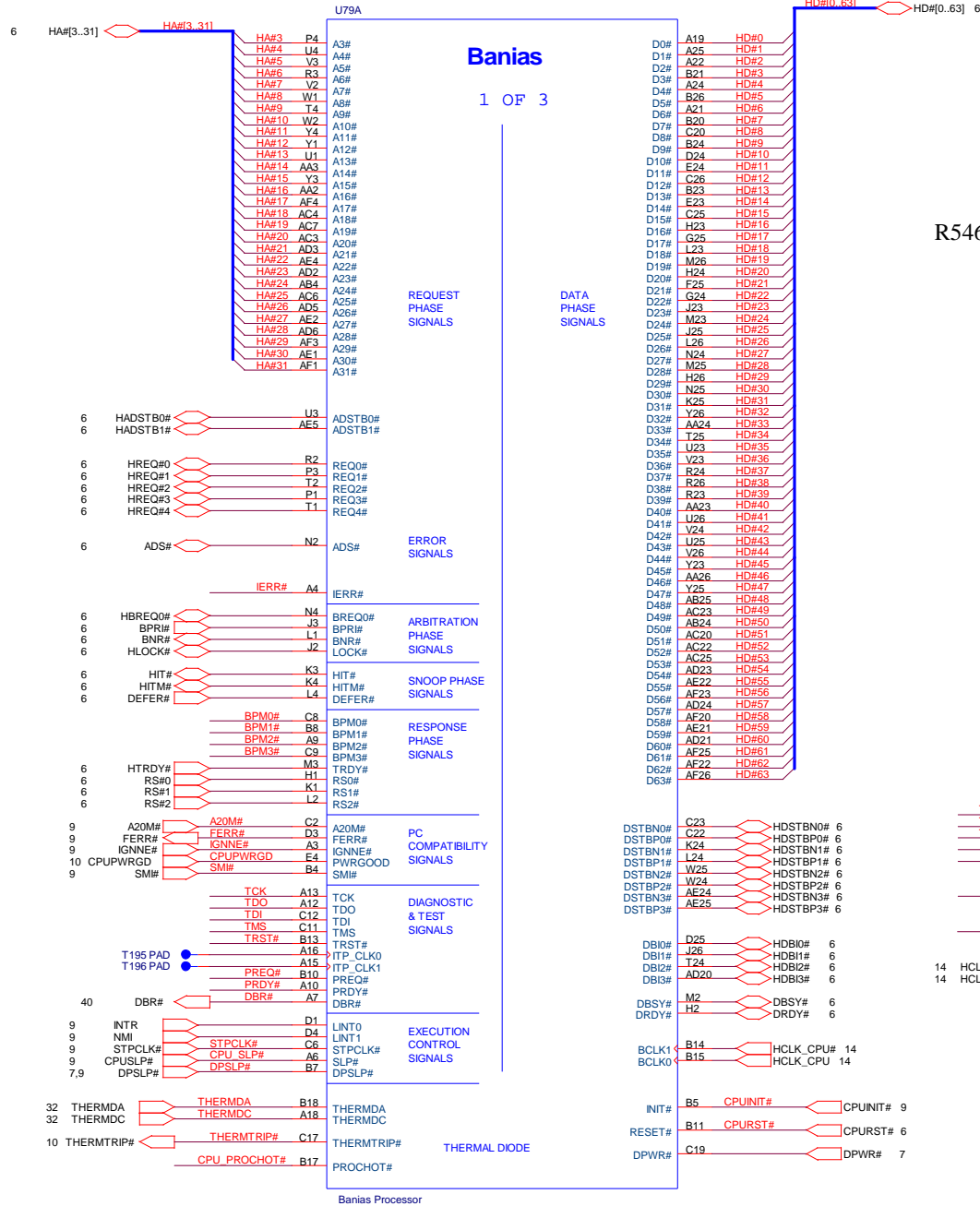
 **QUANTA
COMPUTER**

Title: Index, DNI, Power & Ground

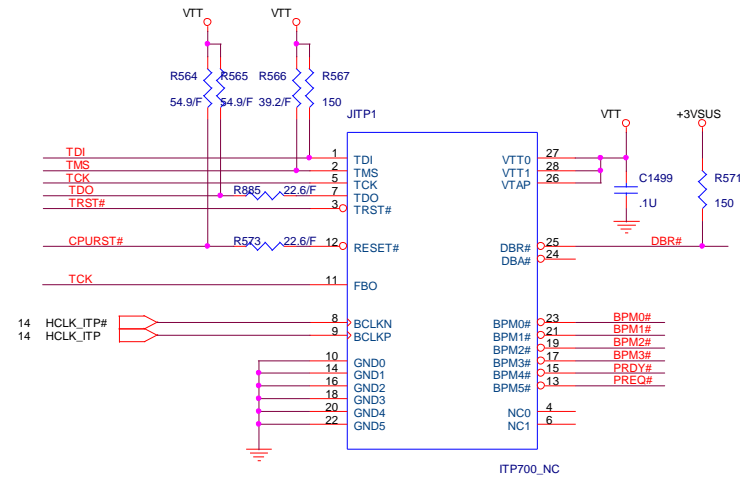
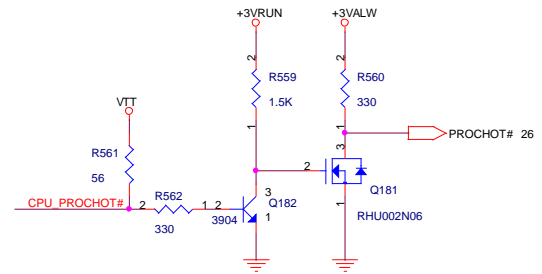
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R867 should be close to ICH4



R546 should be place within 2" of the processor ; others place near ITP



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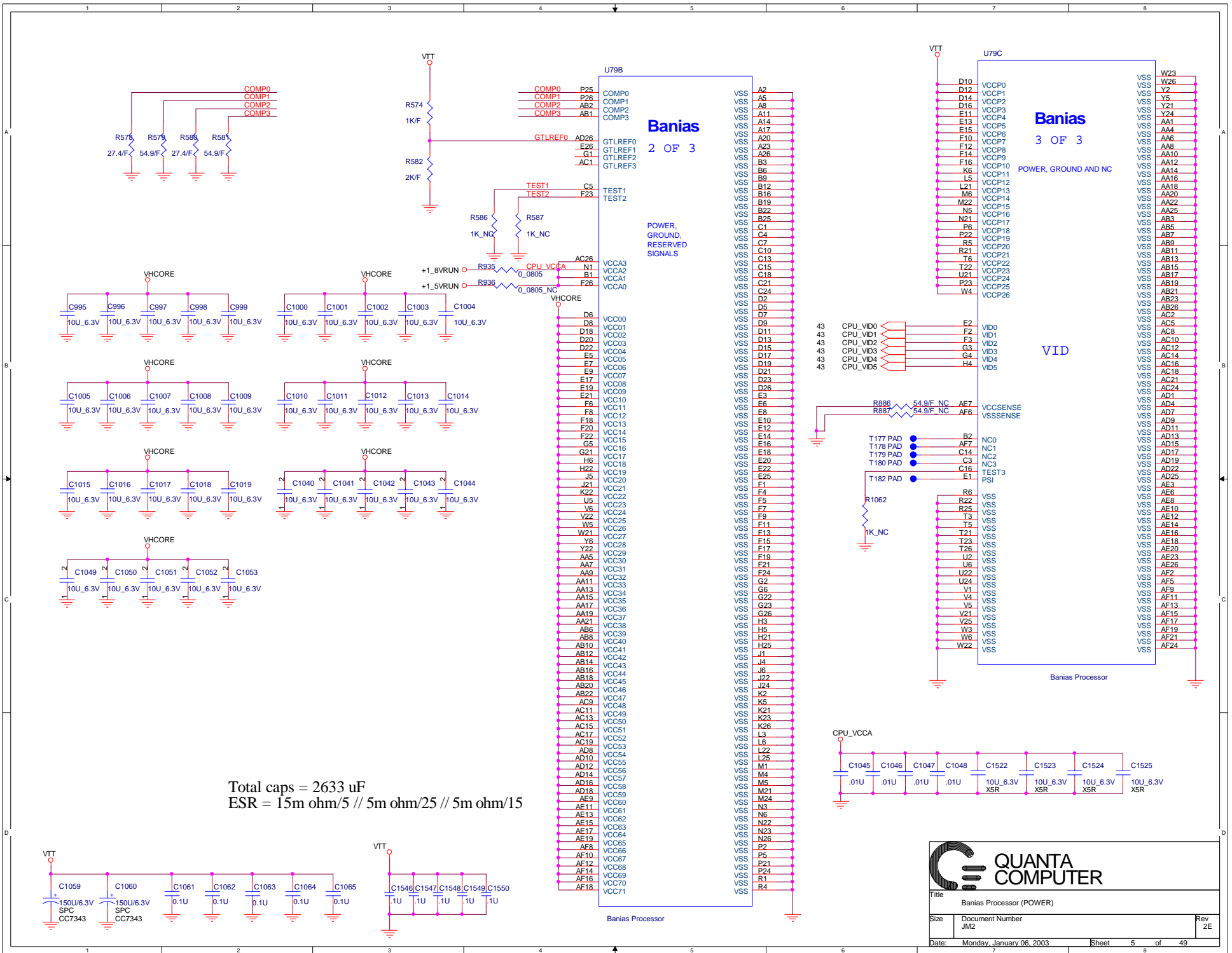
Title: Banias Processor (HOST)

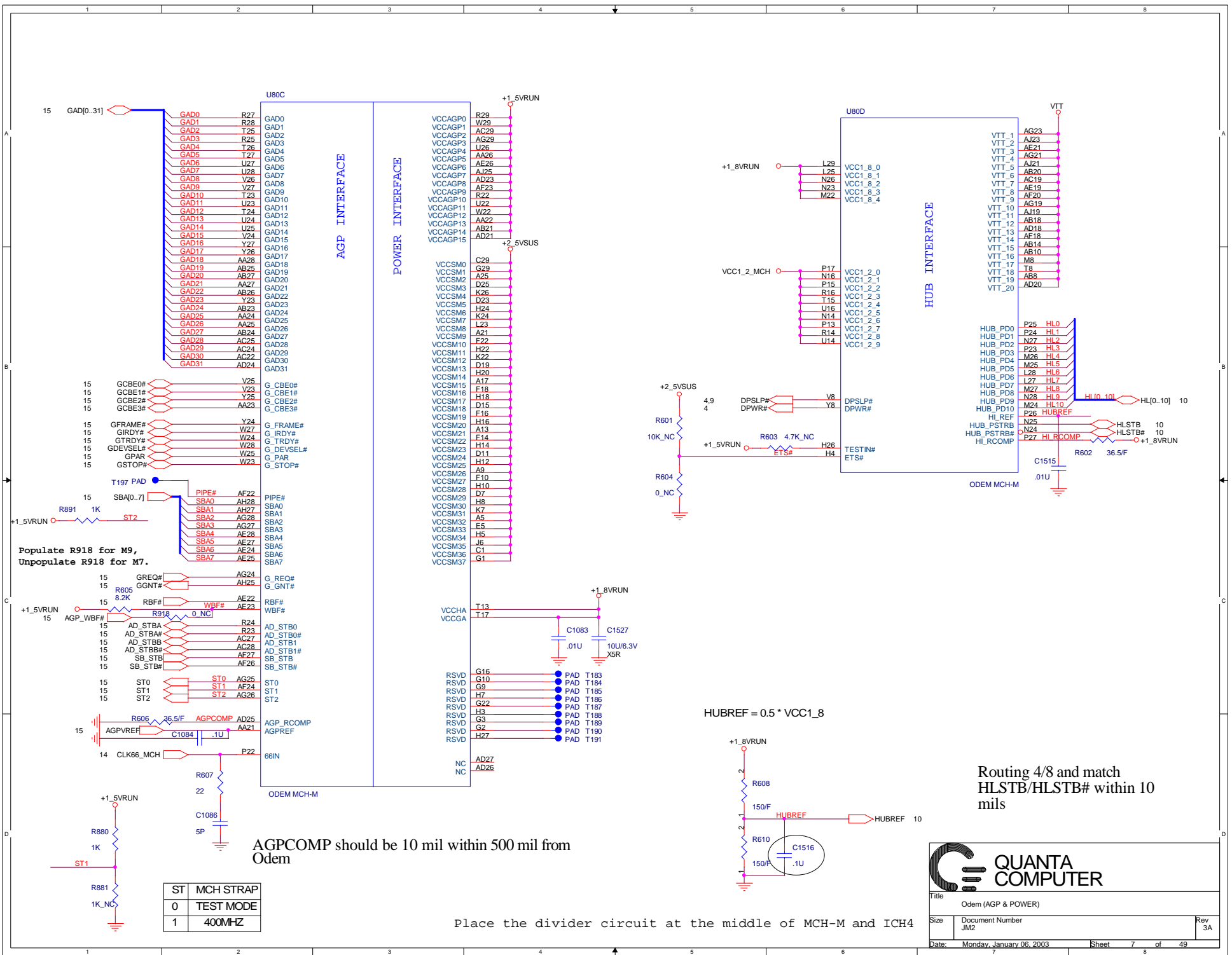
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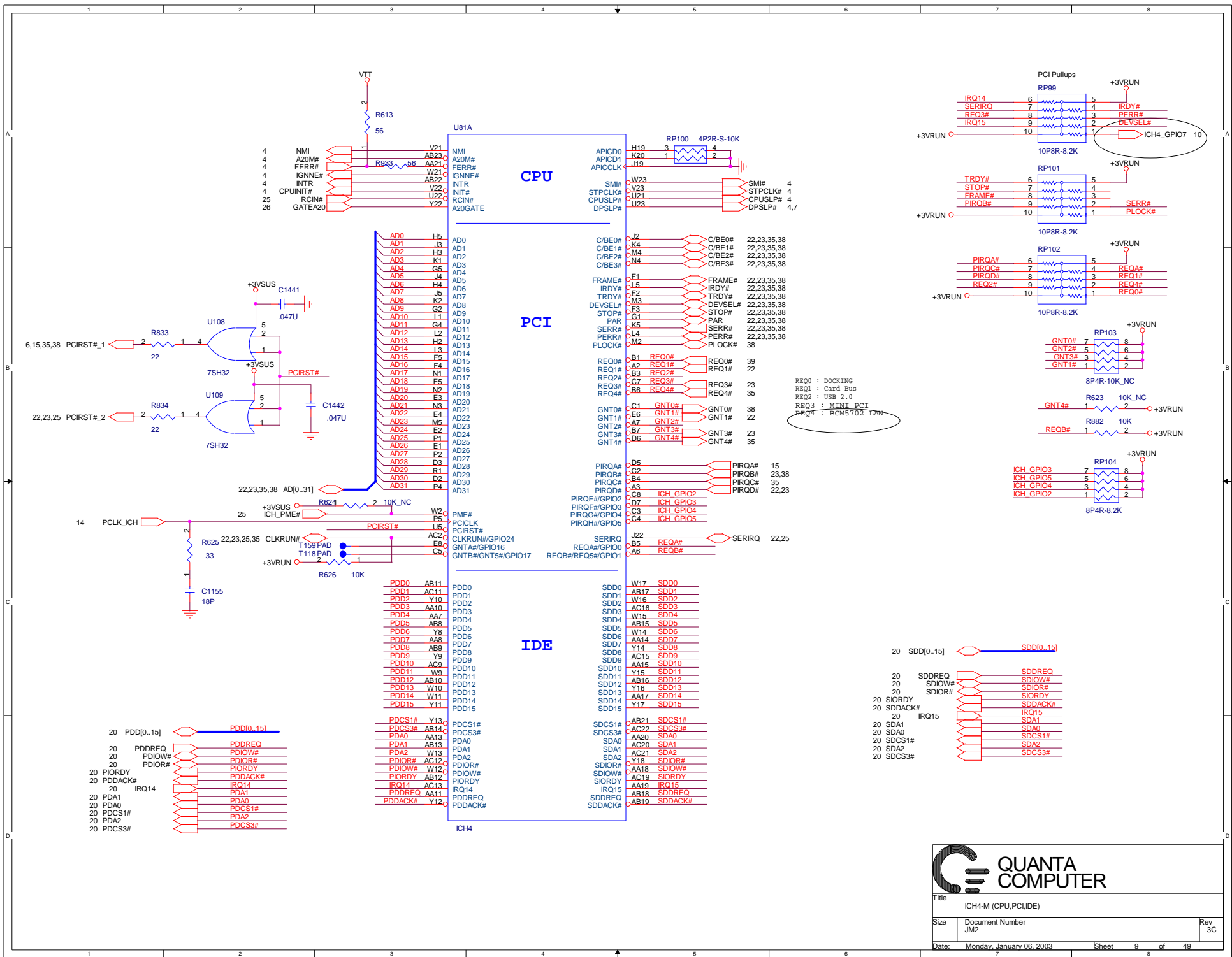
Title: Odem (AGP & POWER)

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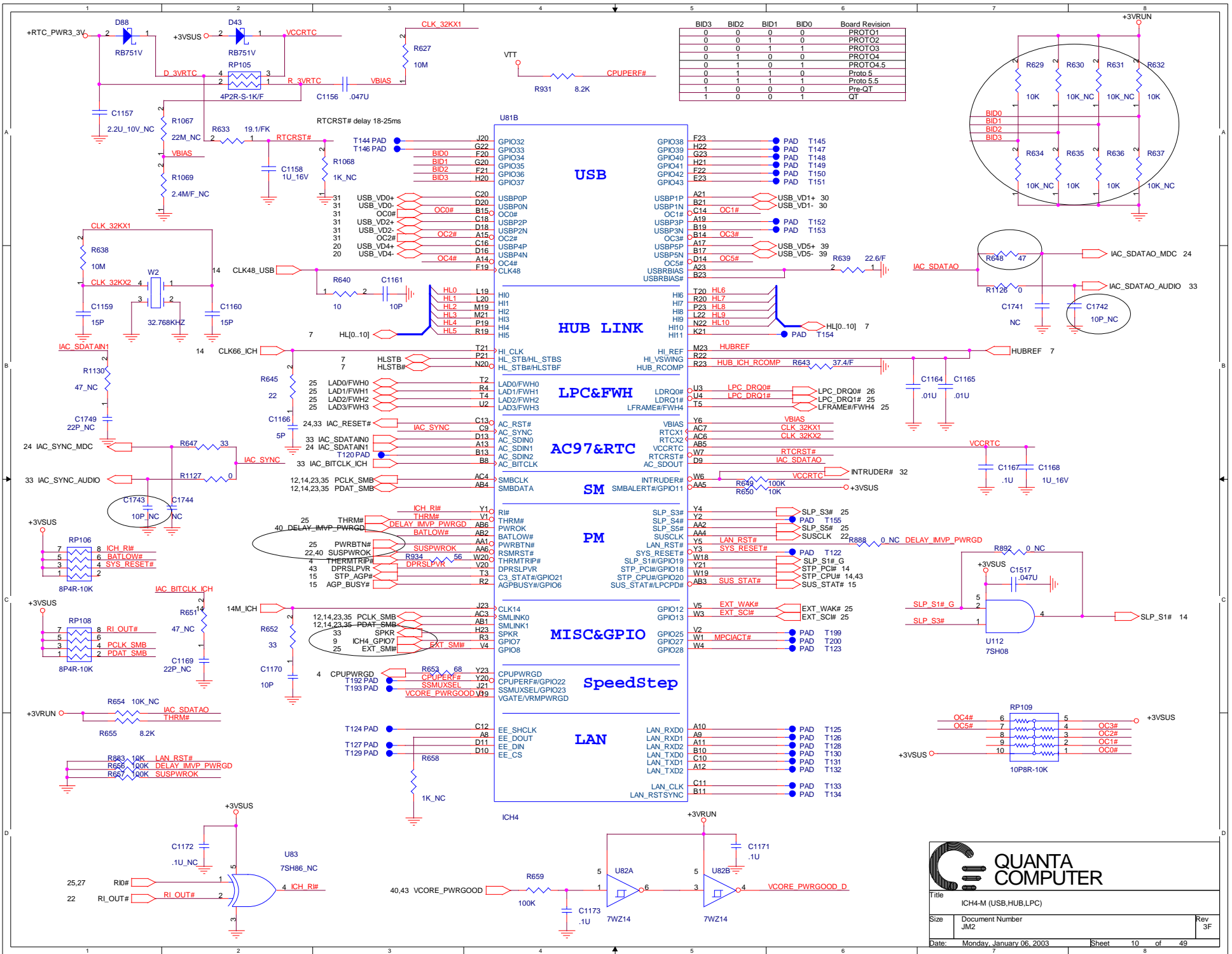


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Title: ICH4-M (CPU,PCLIDE)

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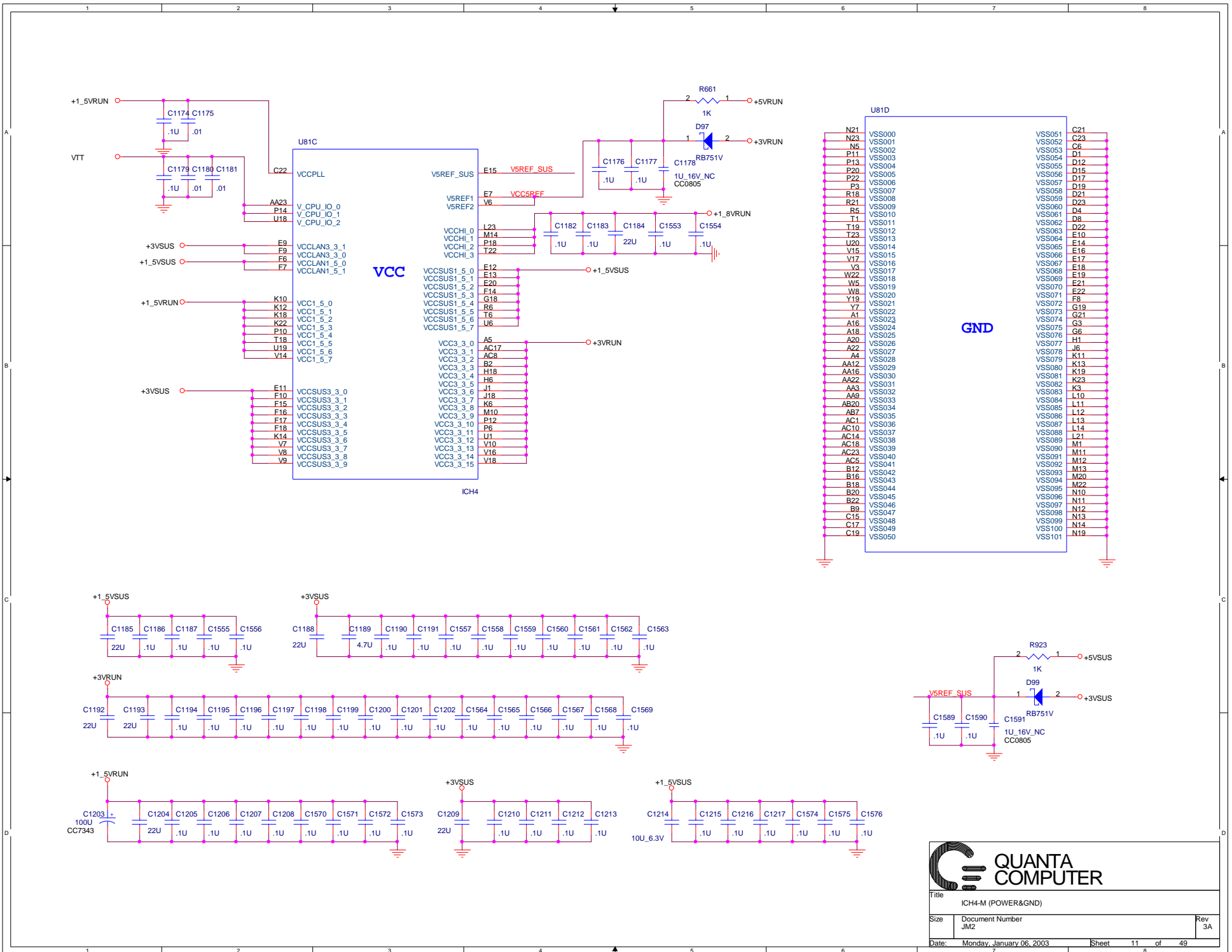
Title: ICH4-M (USB,HUB,LPC)

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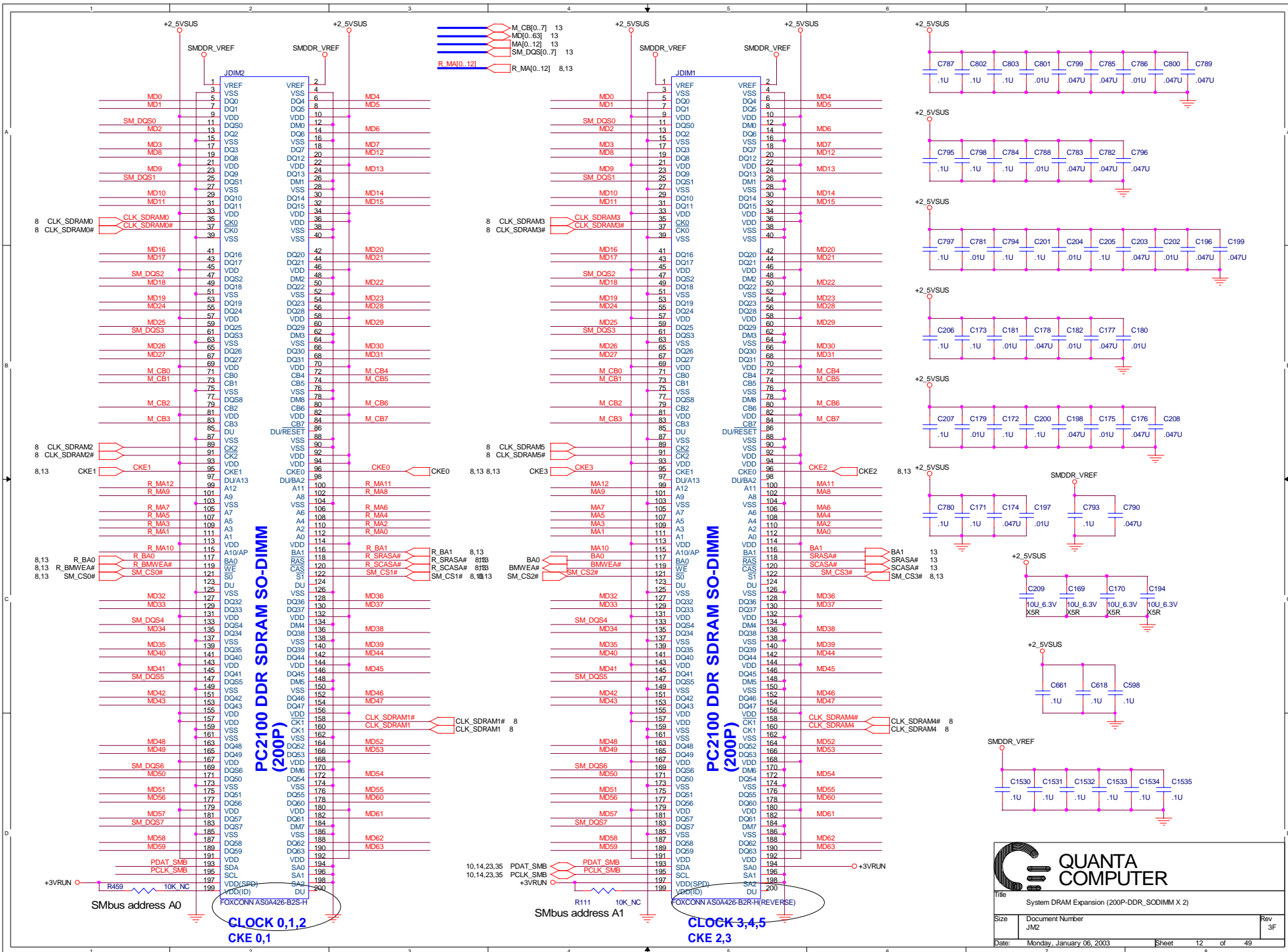


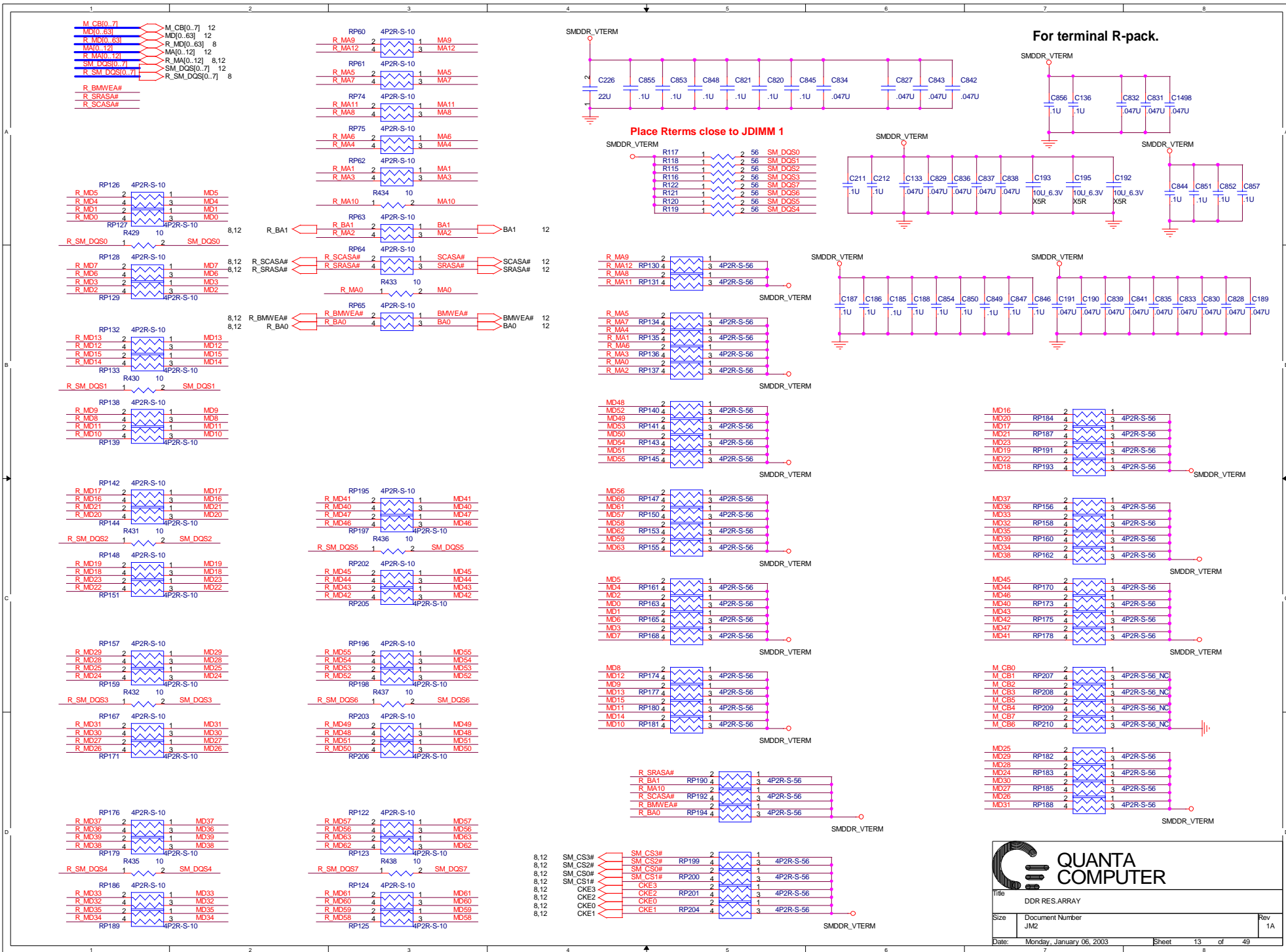
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Title: ICH4-M (POWER&GND)

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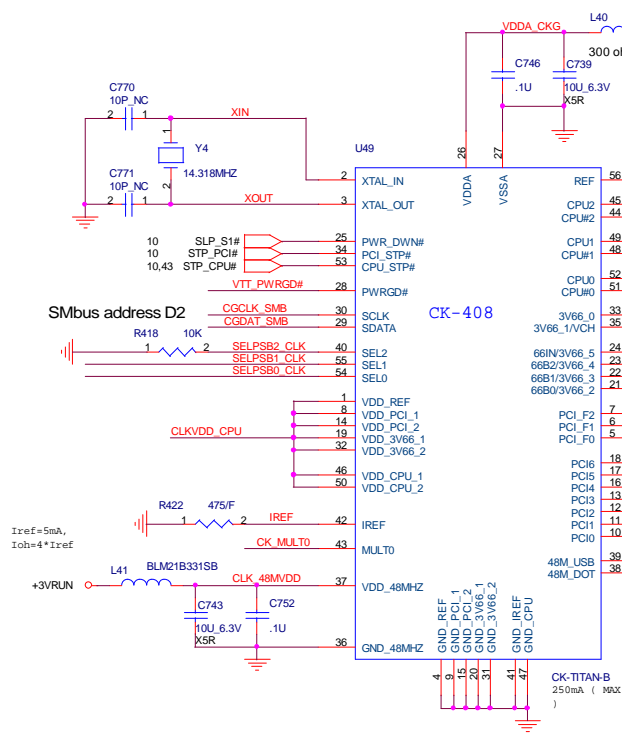
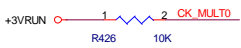
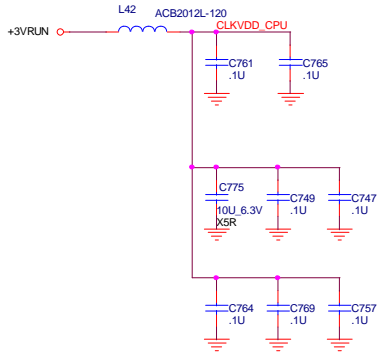
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S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

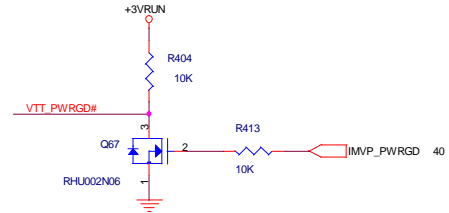
Place these termination to close CK408.



SMbus address D2

$I_{ref}=5mA$,
 $I_{ob}=4*I_{ref}$

These are for backdrive issue

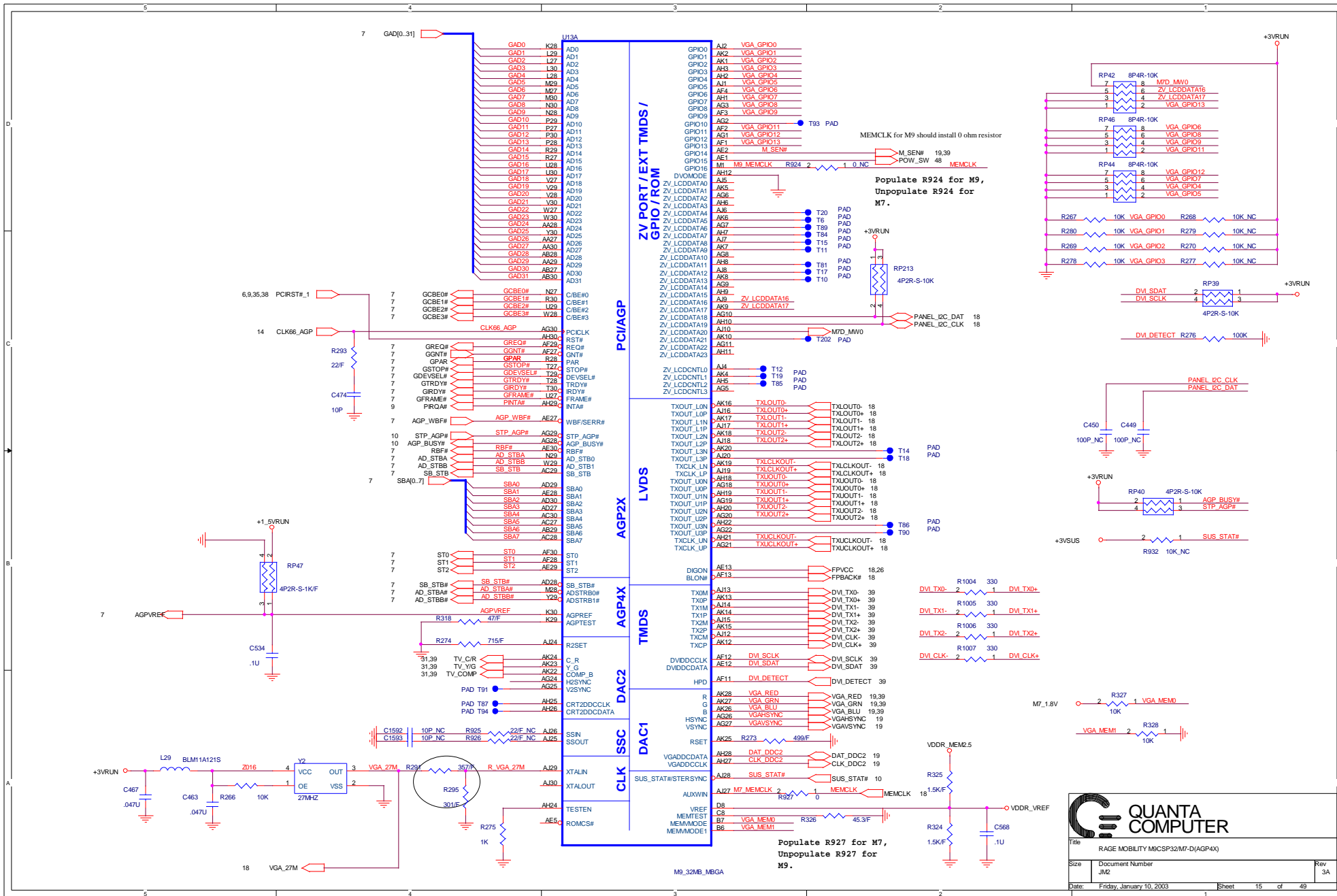


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Title: CLOCK GENERATOR

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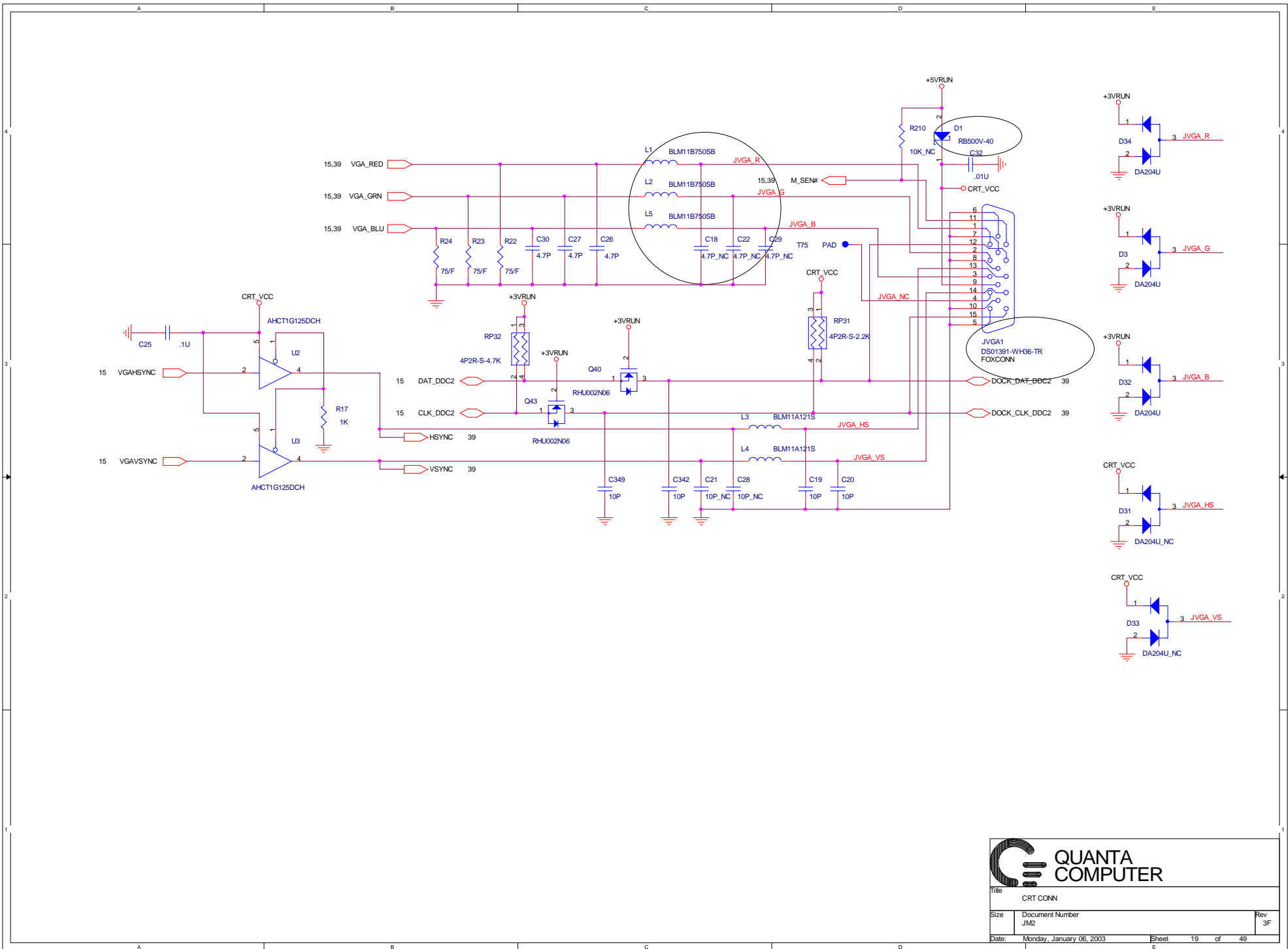
Title: RAGE MOBILITY M9CS32/M7-D(AGP4X)

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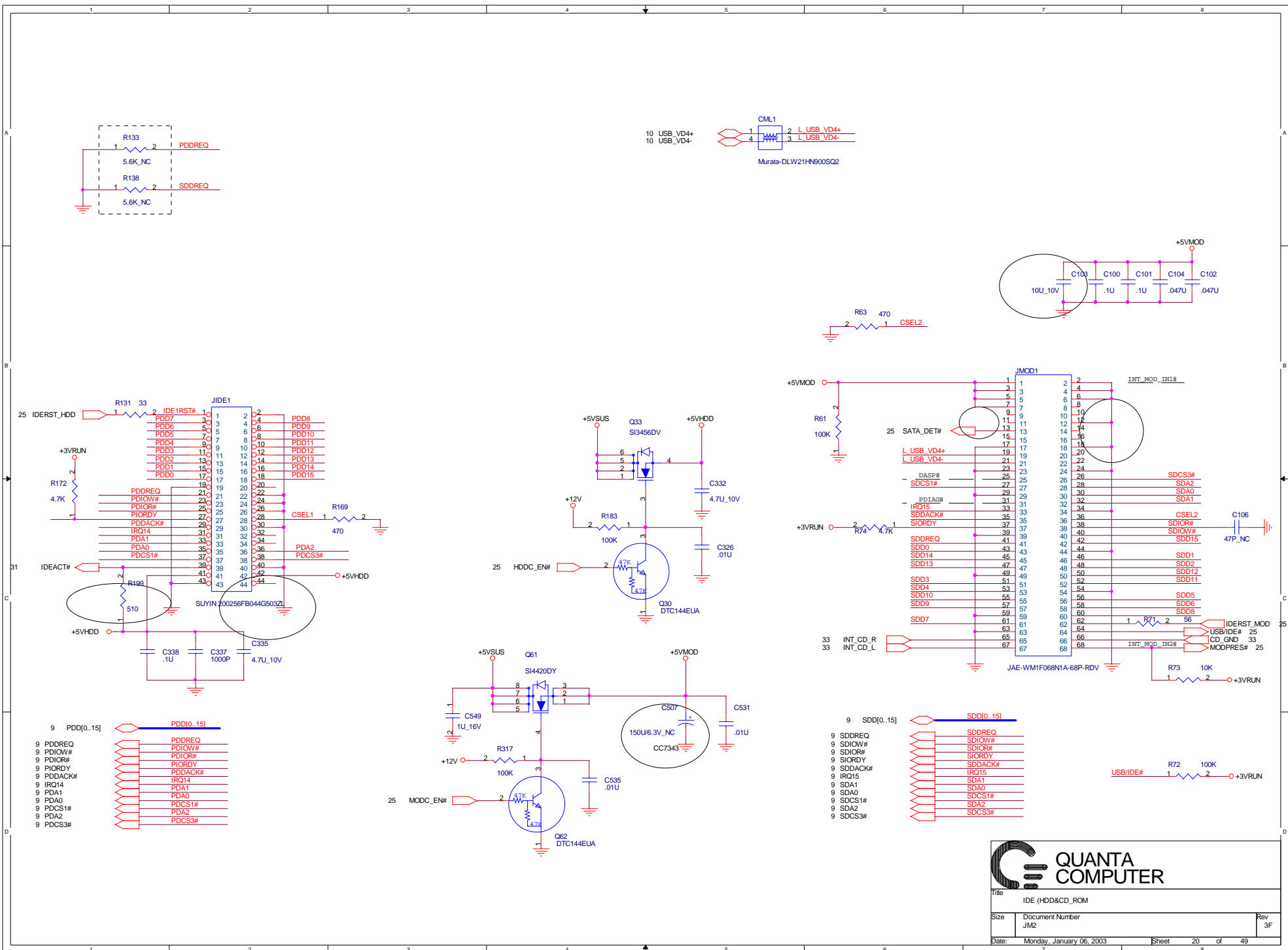


QUANTA COMPUTER

Title: CRT CONN

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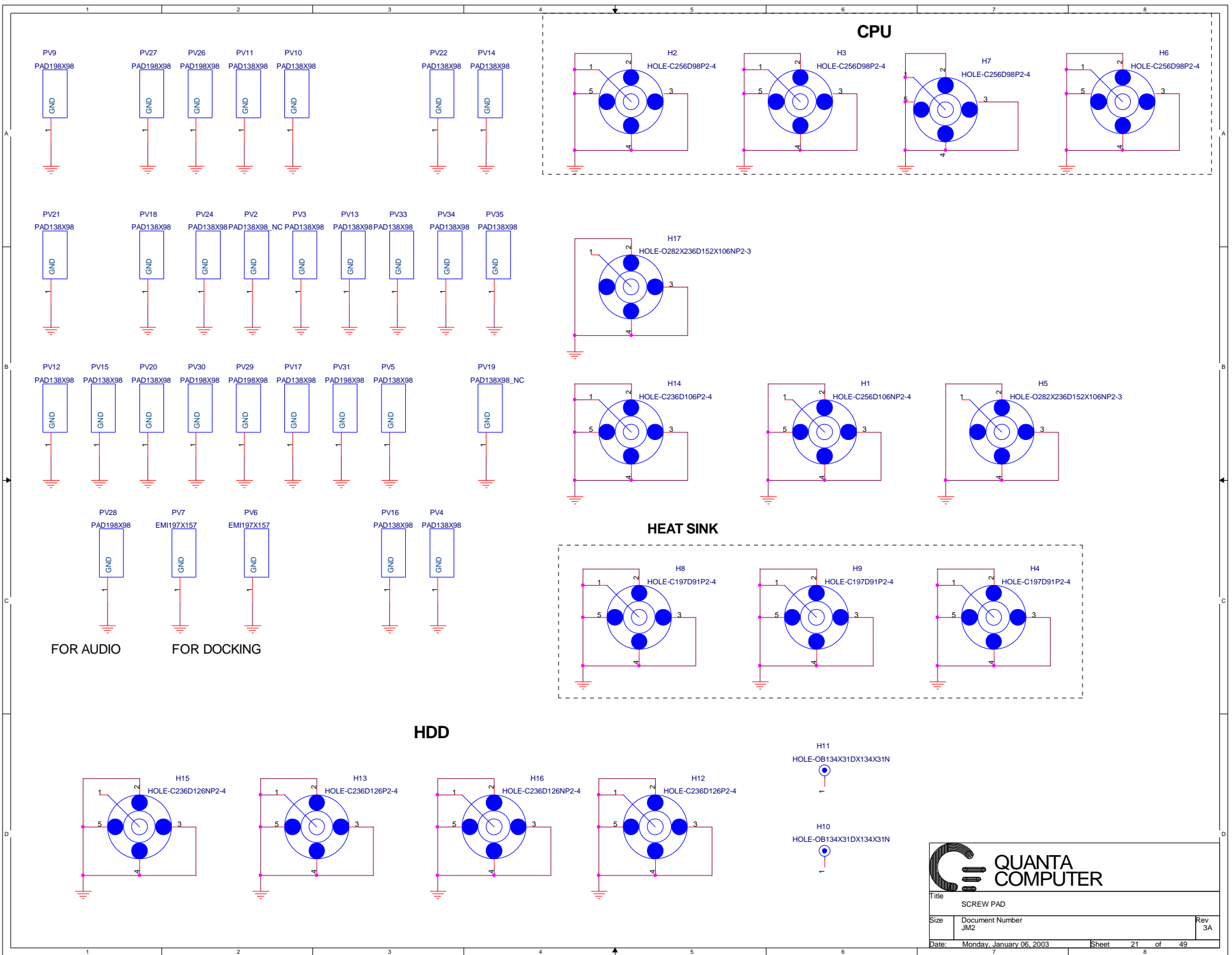


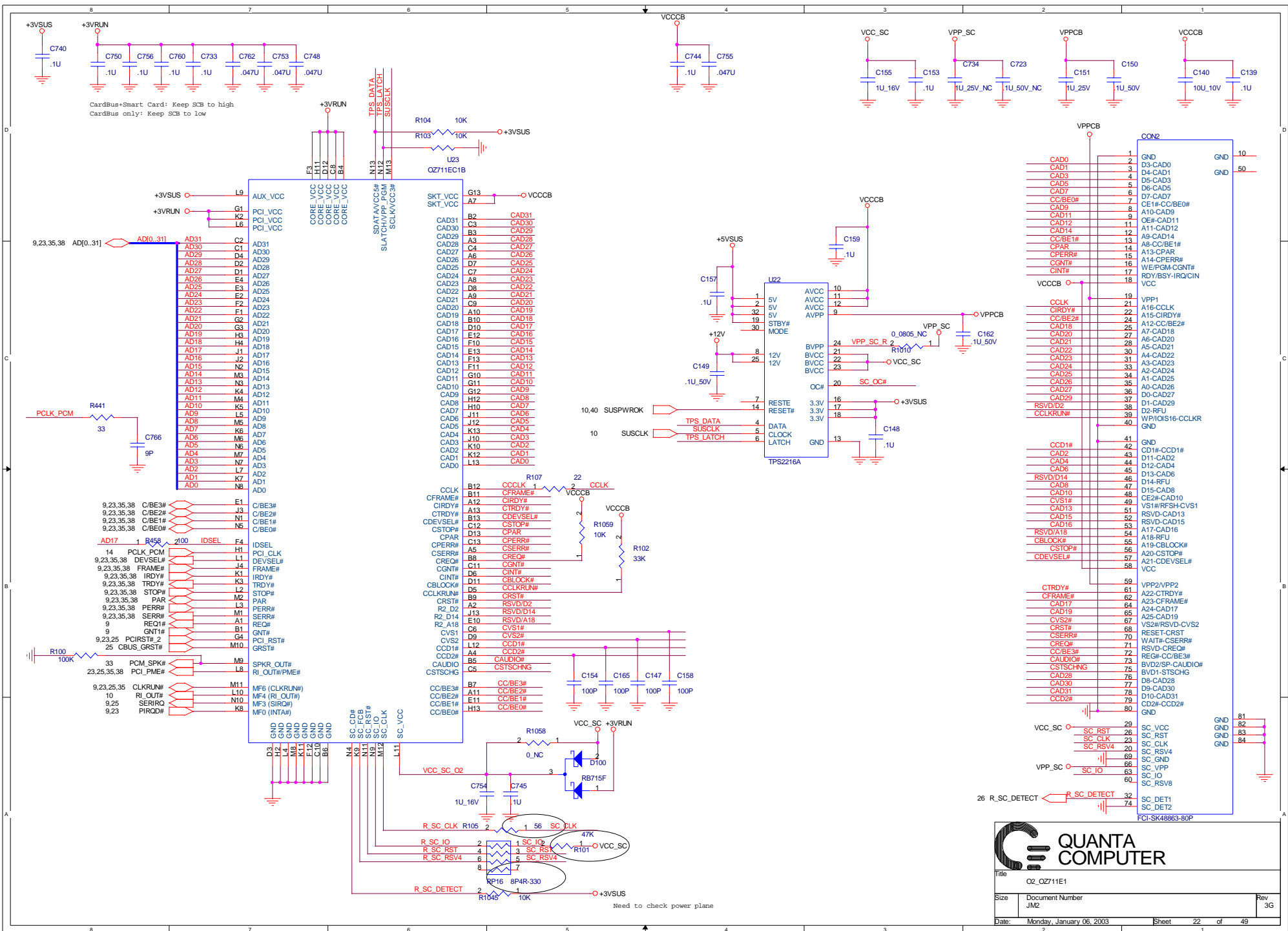
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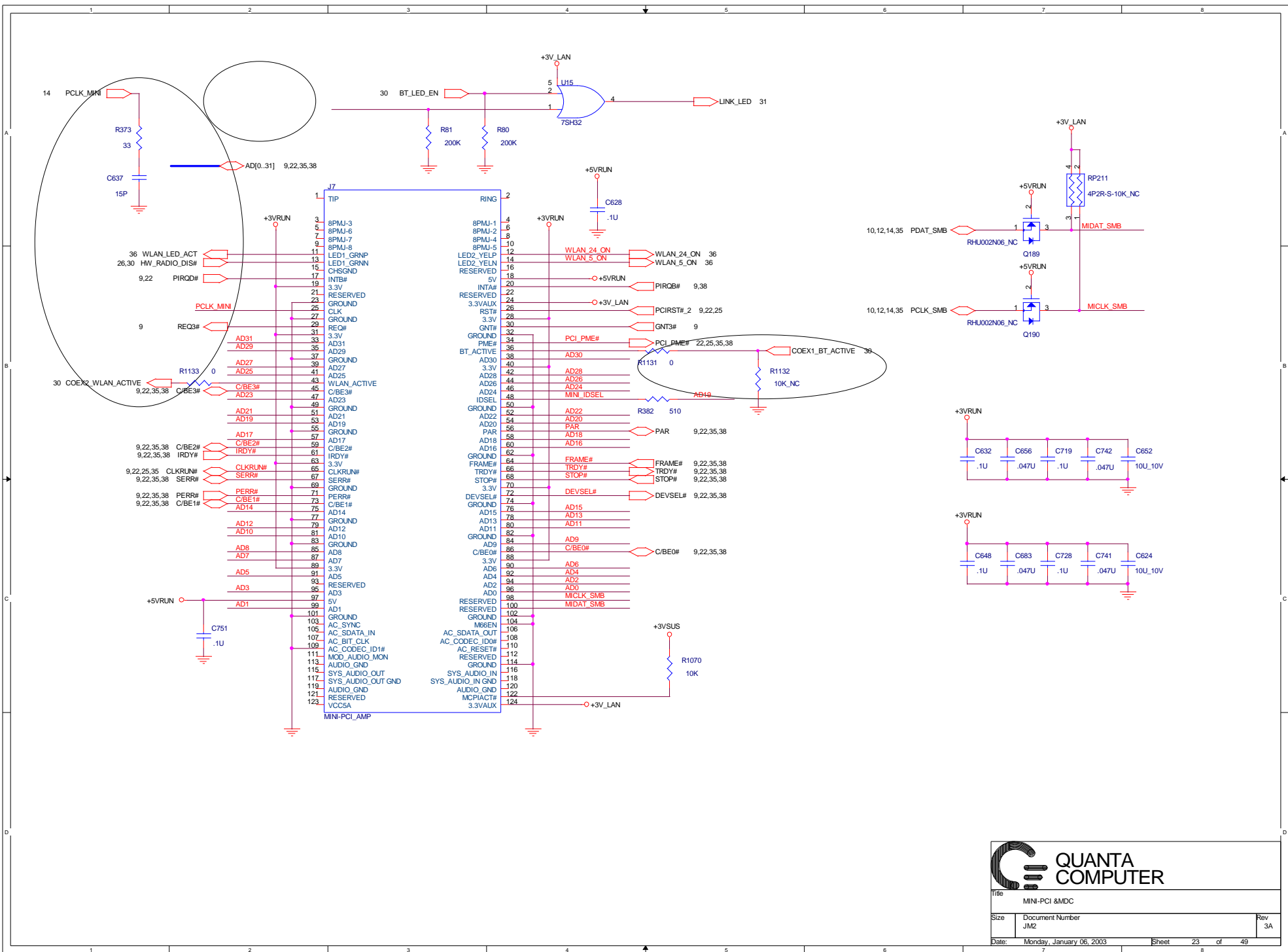
Title: IDE (HDD&CD_ROM)

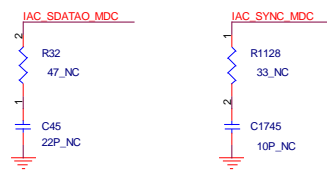
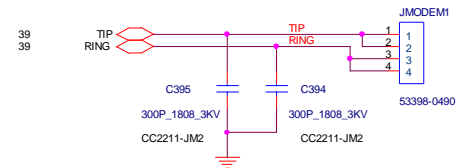
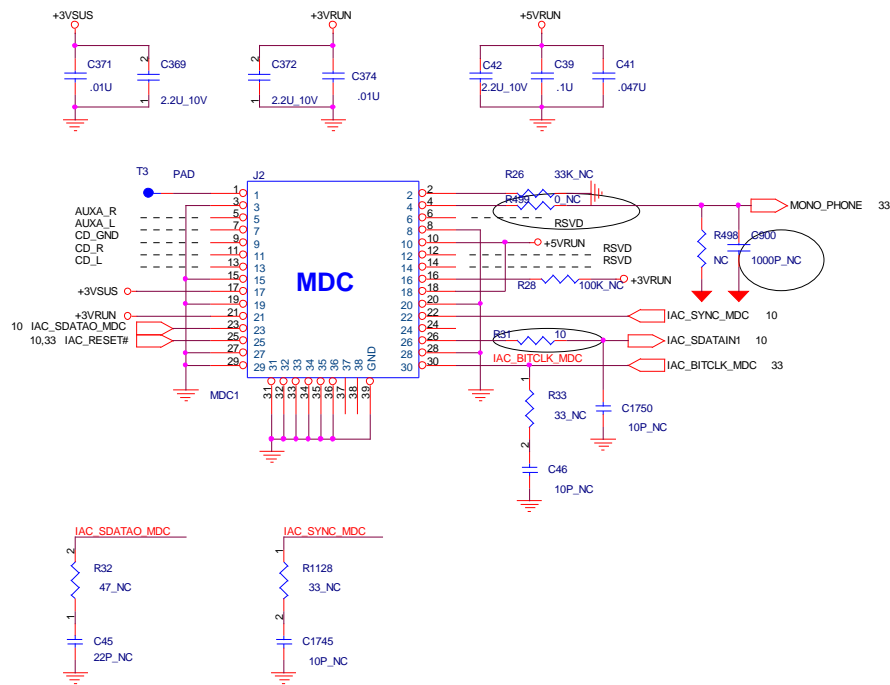
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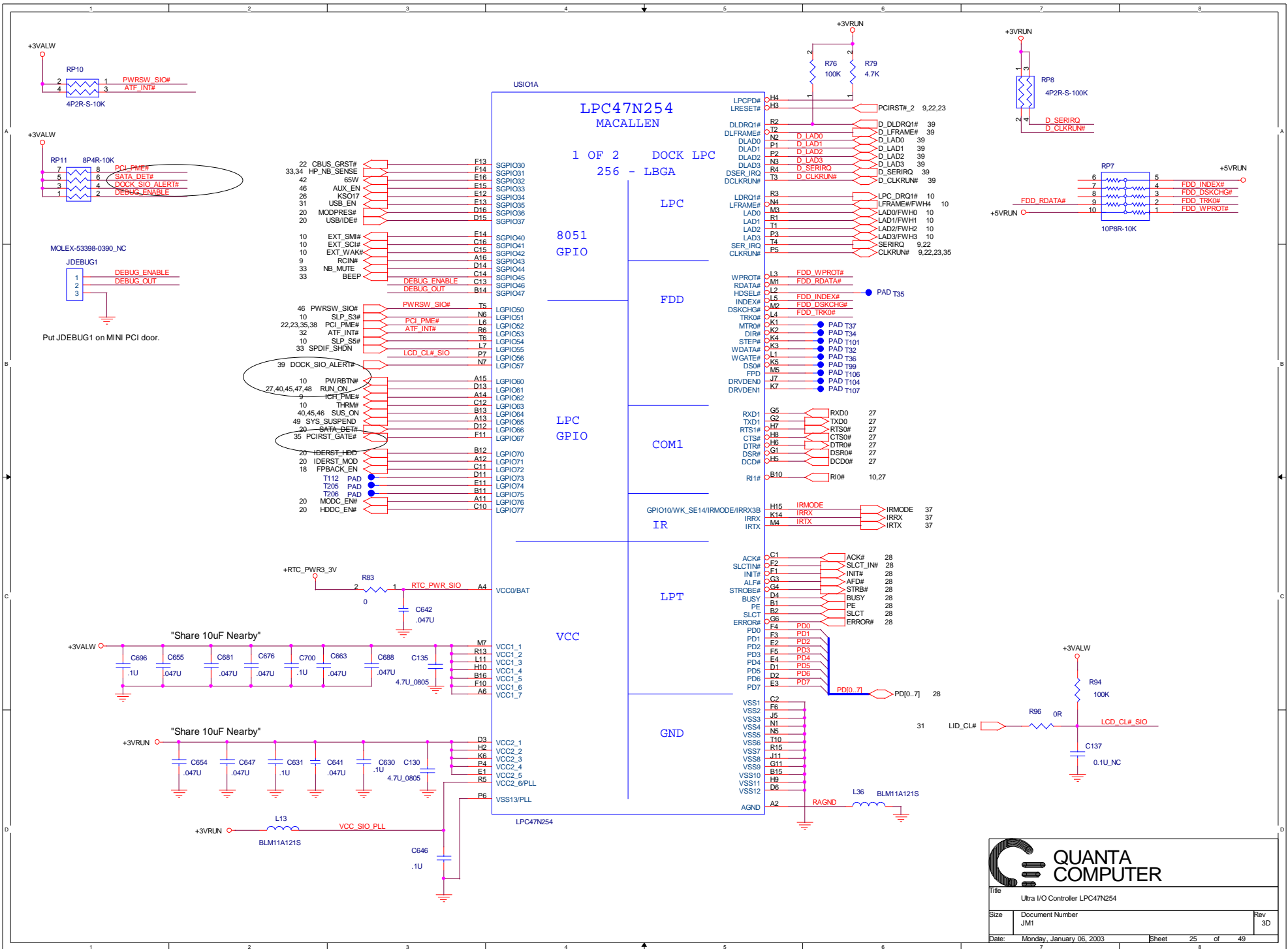


QUANTA COMPUTER

Title: MDC CONN.

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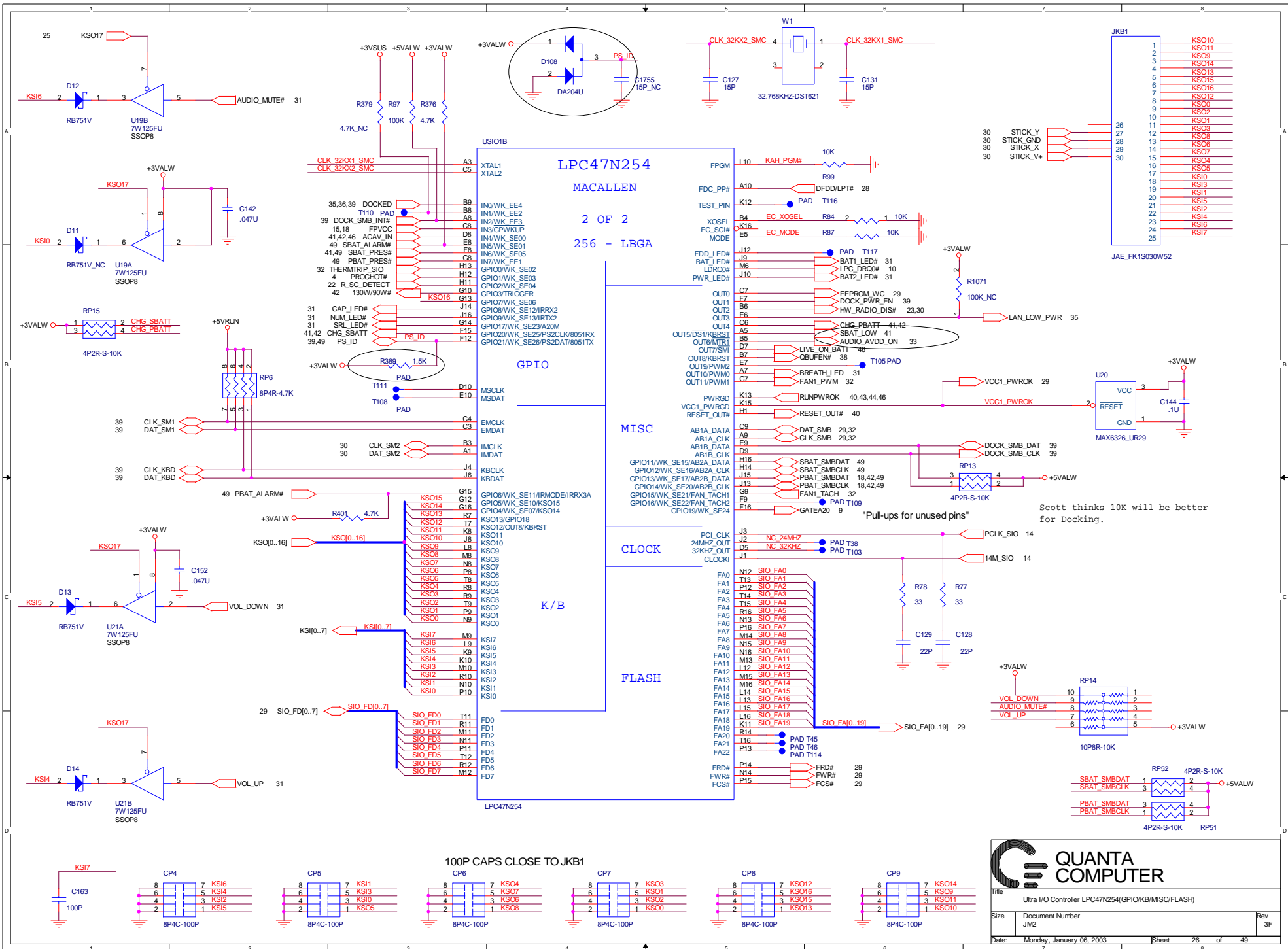


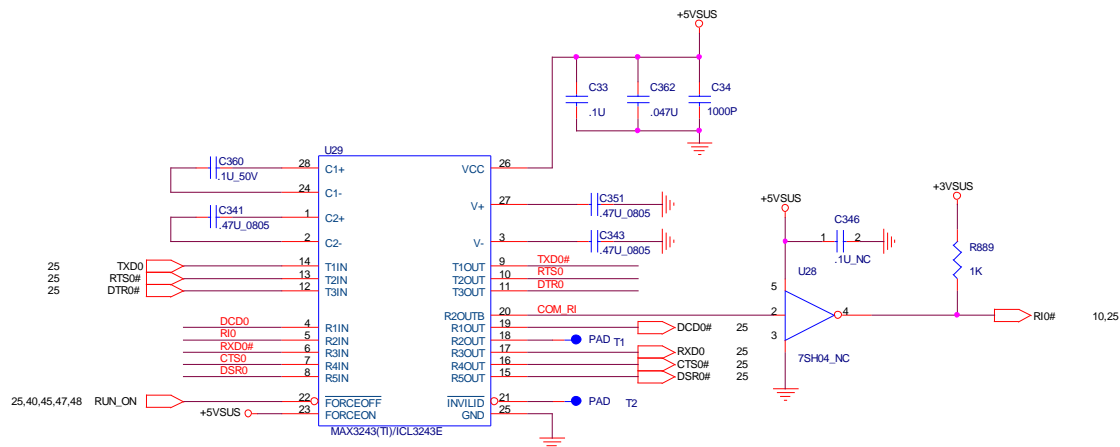
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Title: Ultra I/O Controller LPC47N254

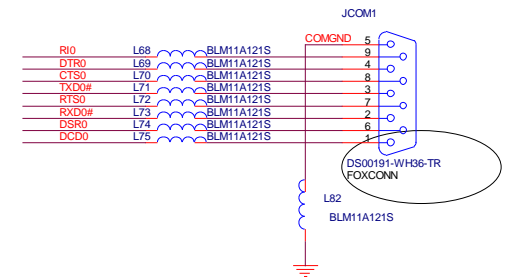
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If MAX3243 pin 22 tied to RUN_ON, then it can not support Ring Out



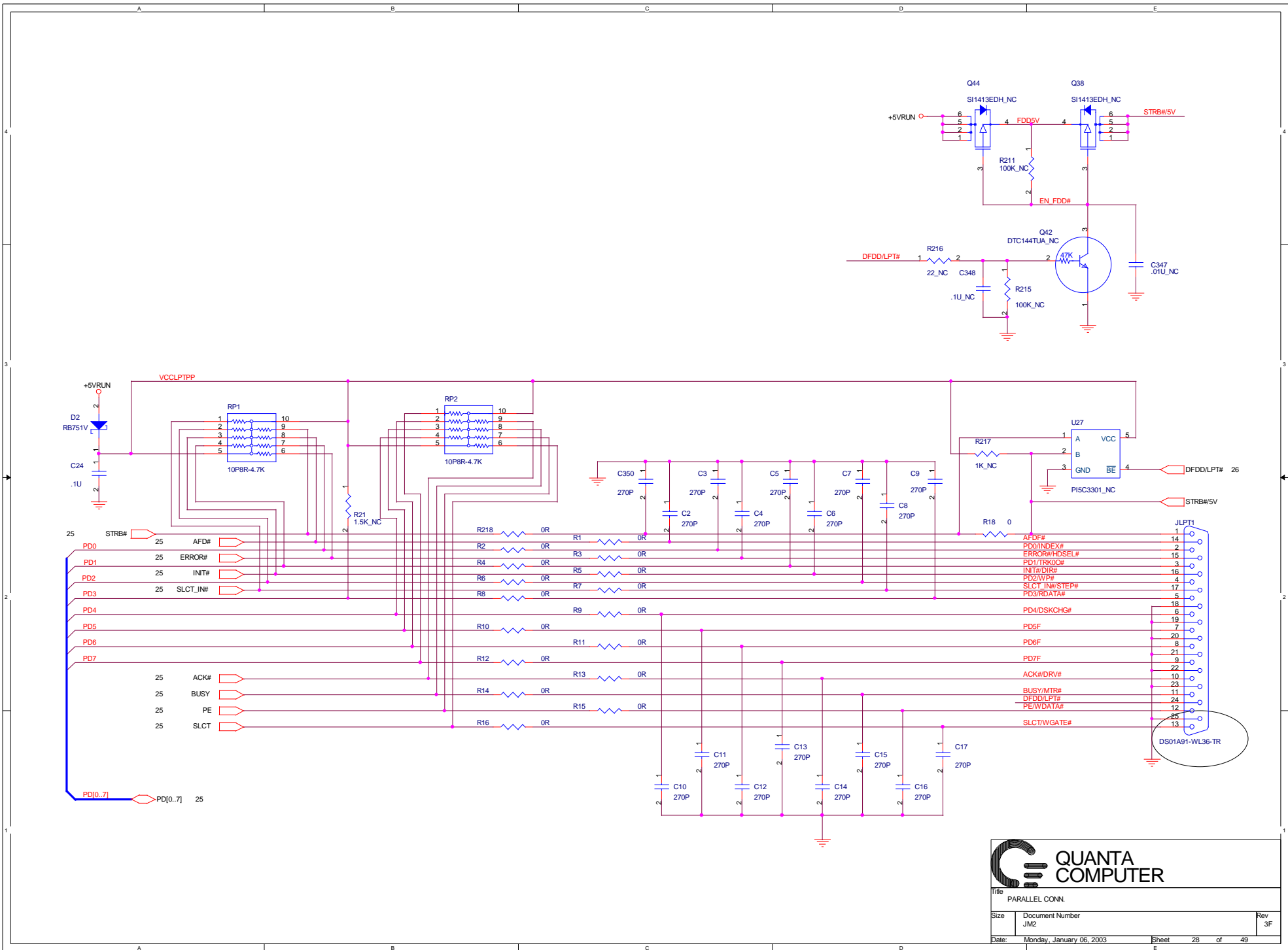
Place these beads close to JCOM1 as soon as possible

QUANTA COMPUTER

Title: SERIAL PORT

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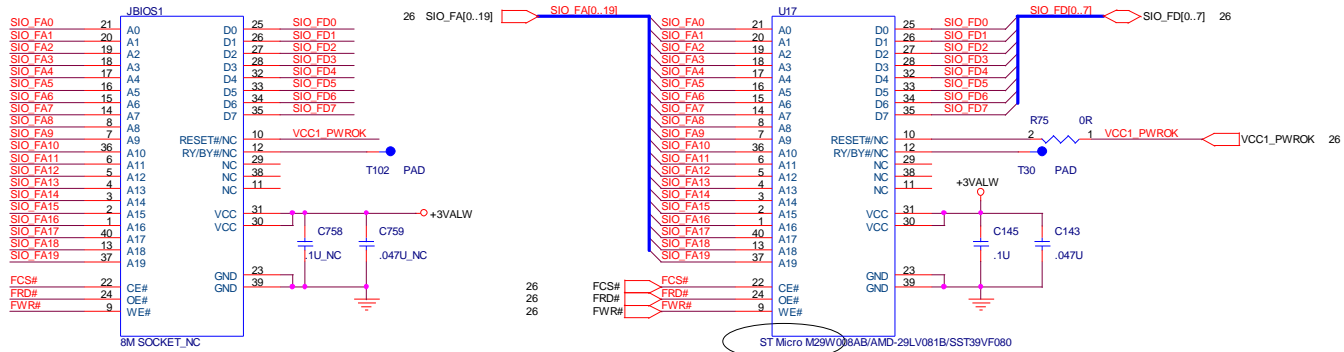


QUANTA COMPUTER

Title: PARALLEL CONN.

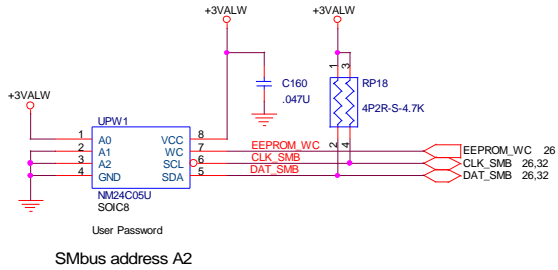
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8Mbit (1M Byte),NO PLCC TYPE

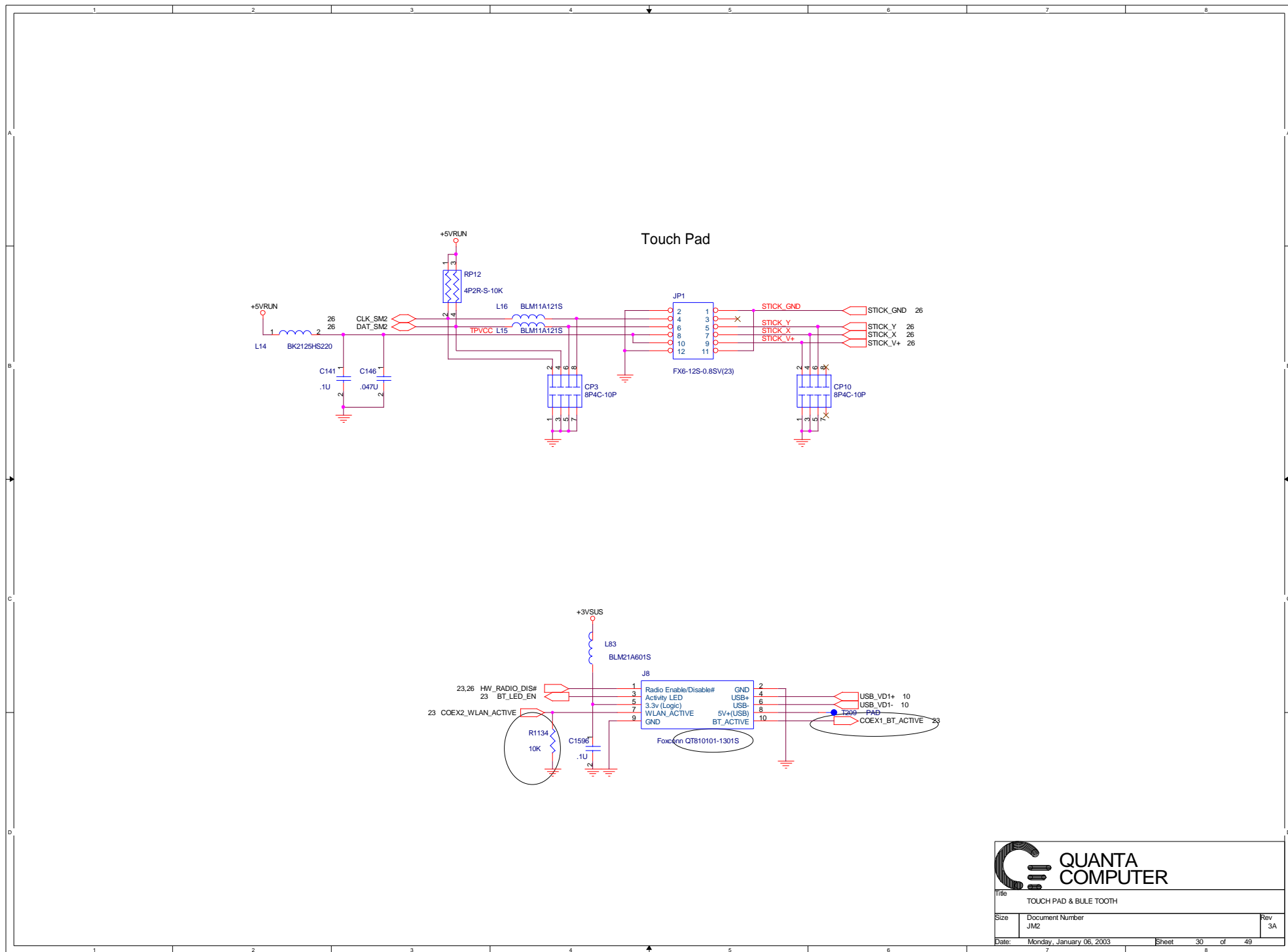



AMD :Pin 10 is RESET# ; Pin12 is RY/BY#
SST :Pin10,12 are NC

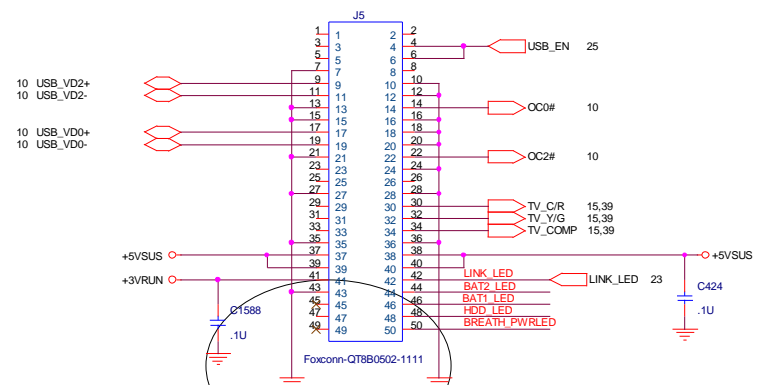
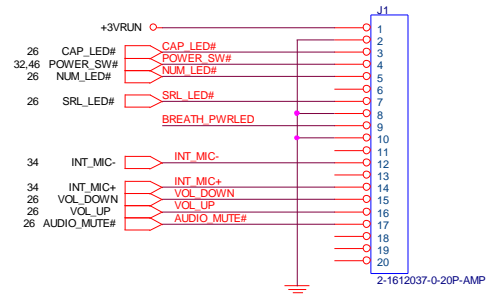
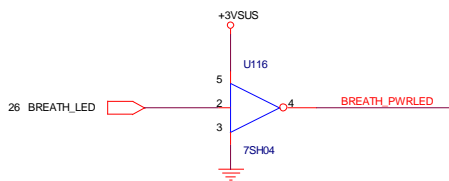
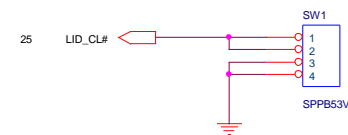
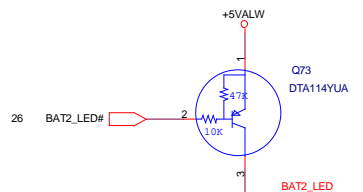
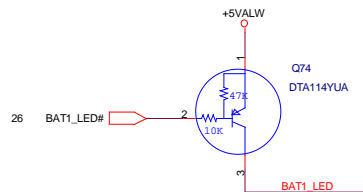
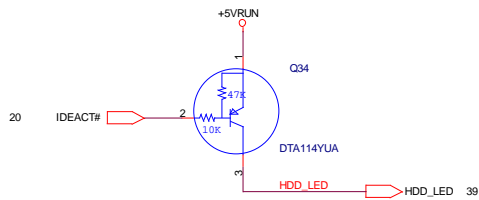
- 1.AMD-29LV081B require MAX 500nS Tready for it's hardware reset.And MAX6326_UR29 has >100mS reset timing.So we can tie it's reset# pin to +3VALW directly.
- 2.SIO has internal 20 mS delay of VCC1_PWROK



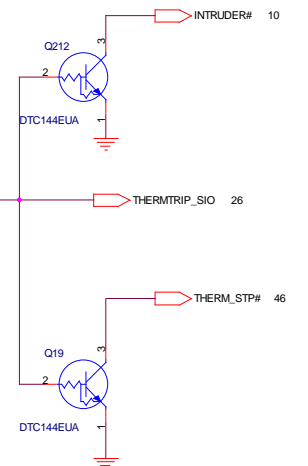
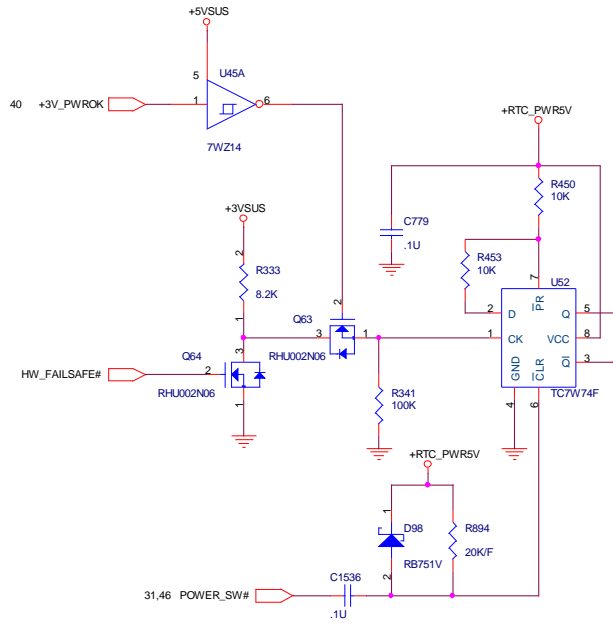
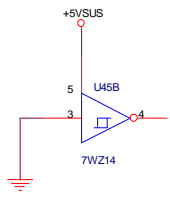
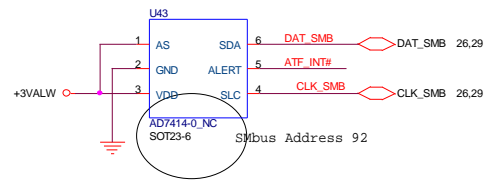
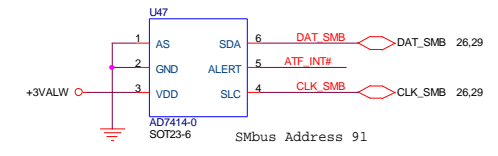
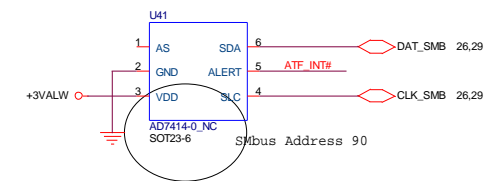
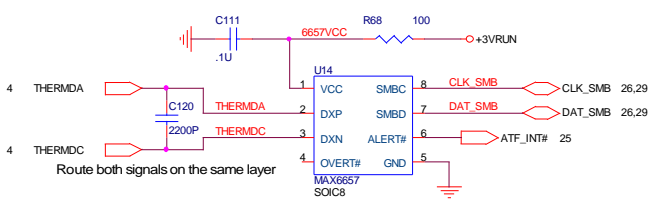
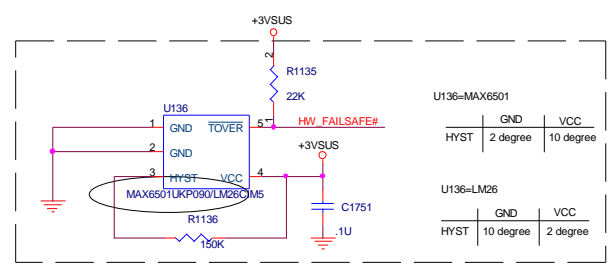
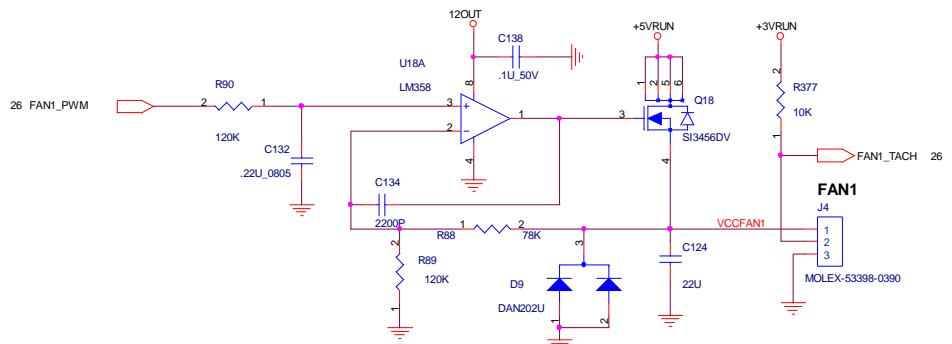
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		TOUCH PAD & BLUE TOOTH	
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Title SWITCH & LED		
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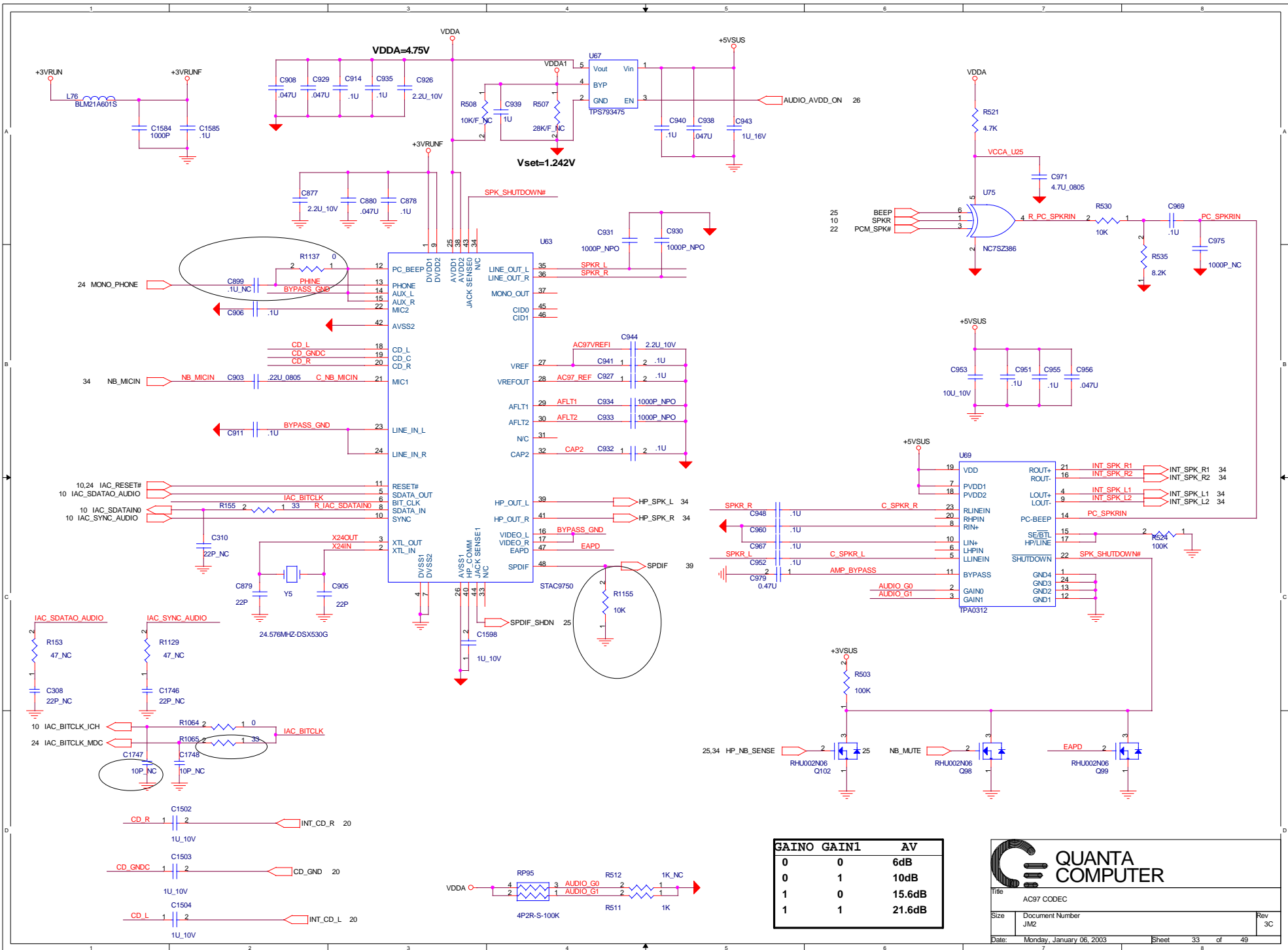


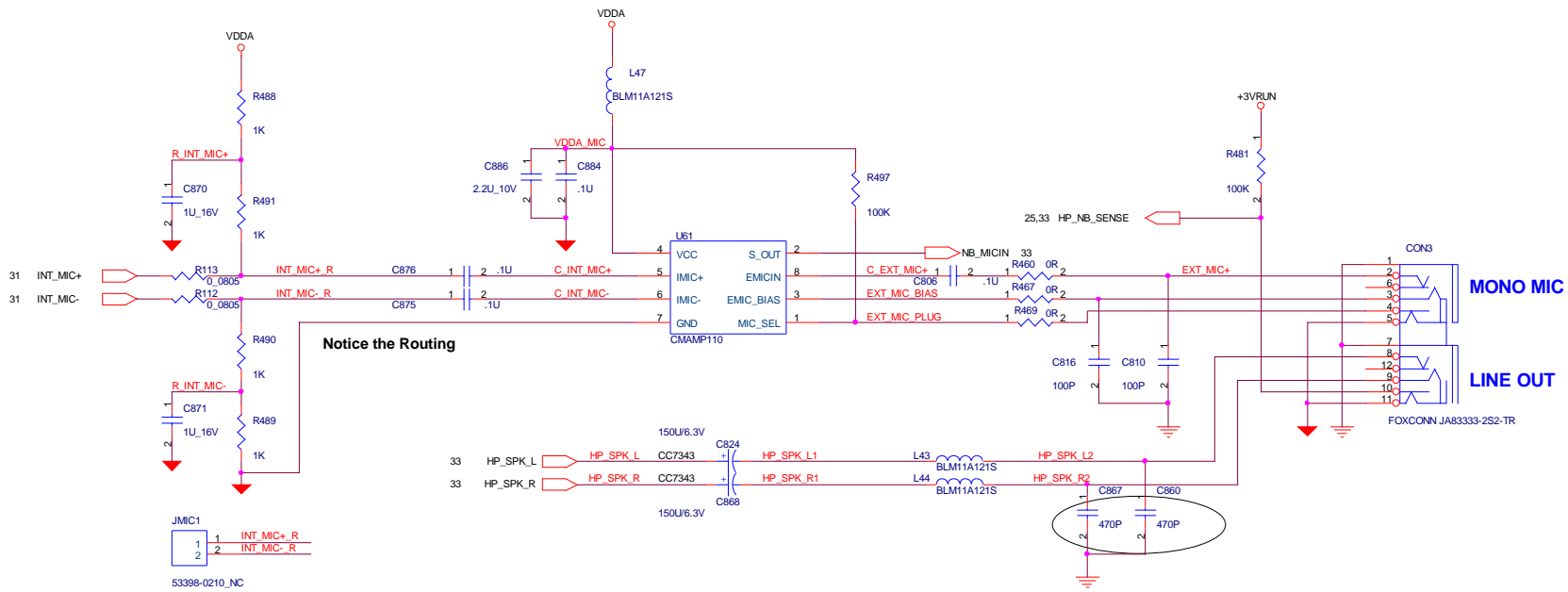
QUANTA COMPUTER

Title: FAN & THERMAL

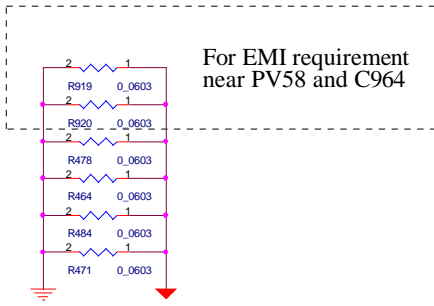
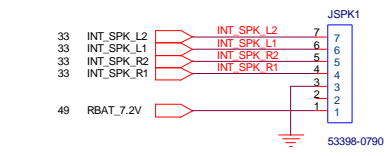
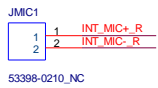
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Notice the Routing

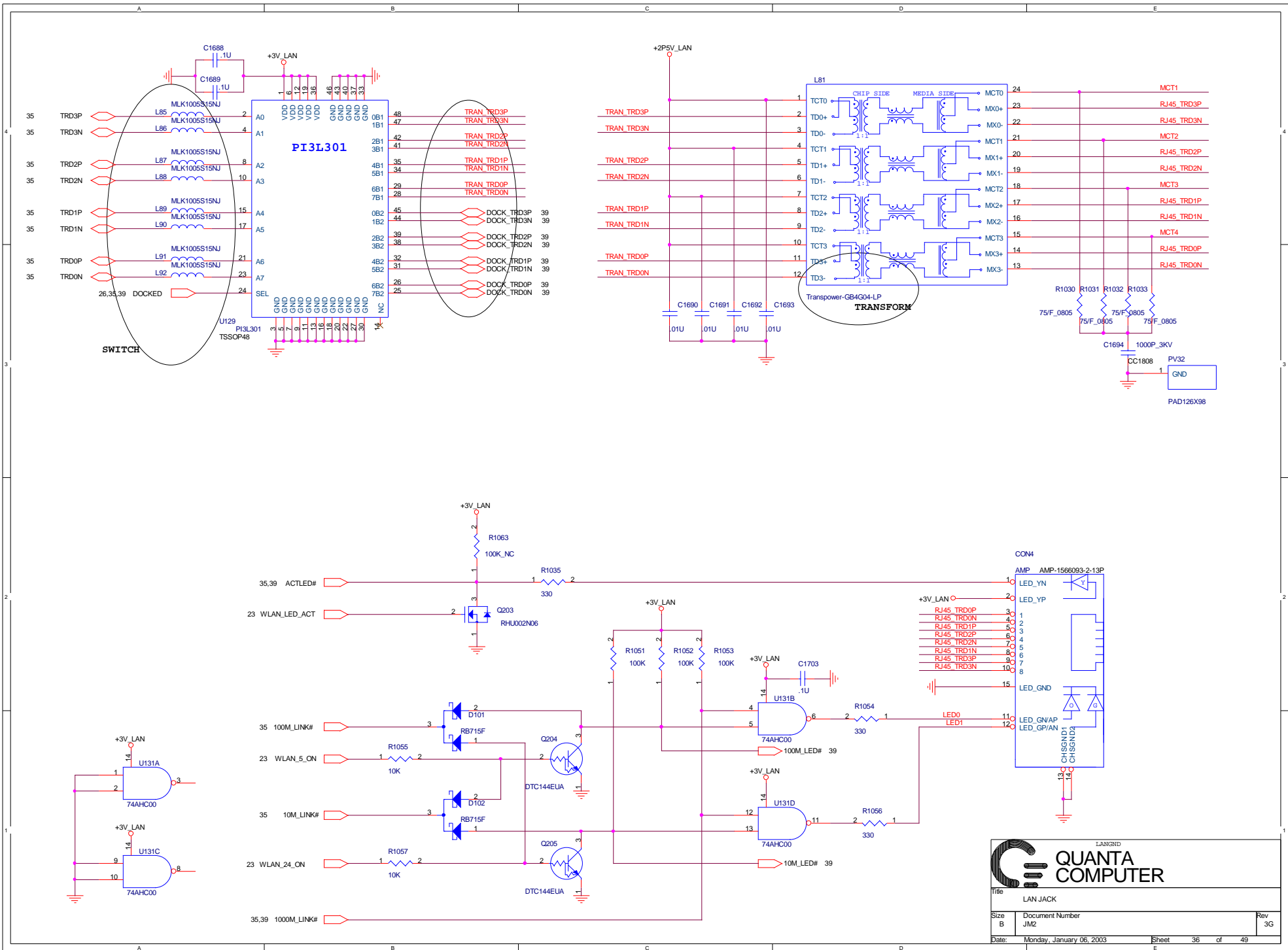


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Title: AUDIO CONN

Size: Document Number JM2 Rev: 3F

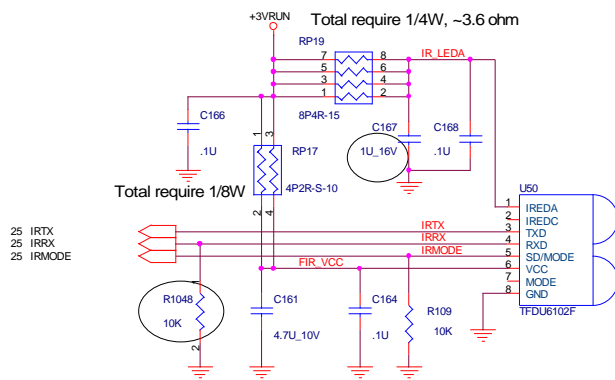
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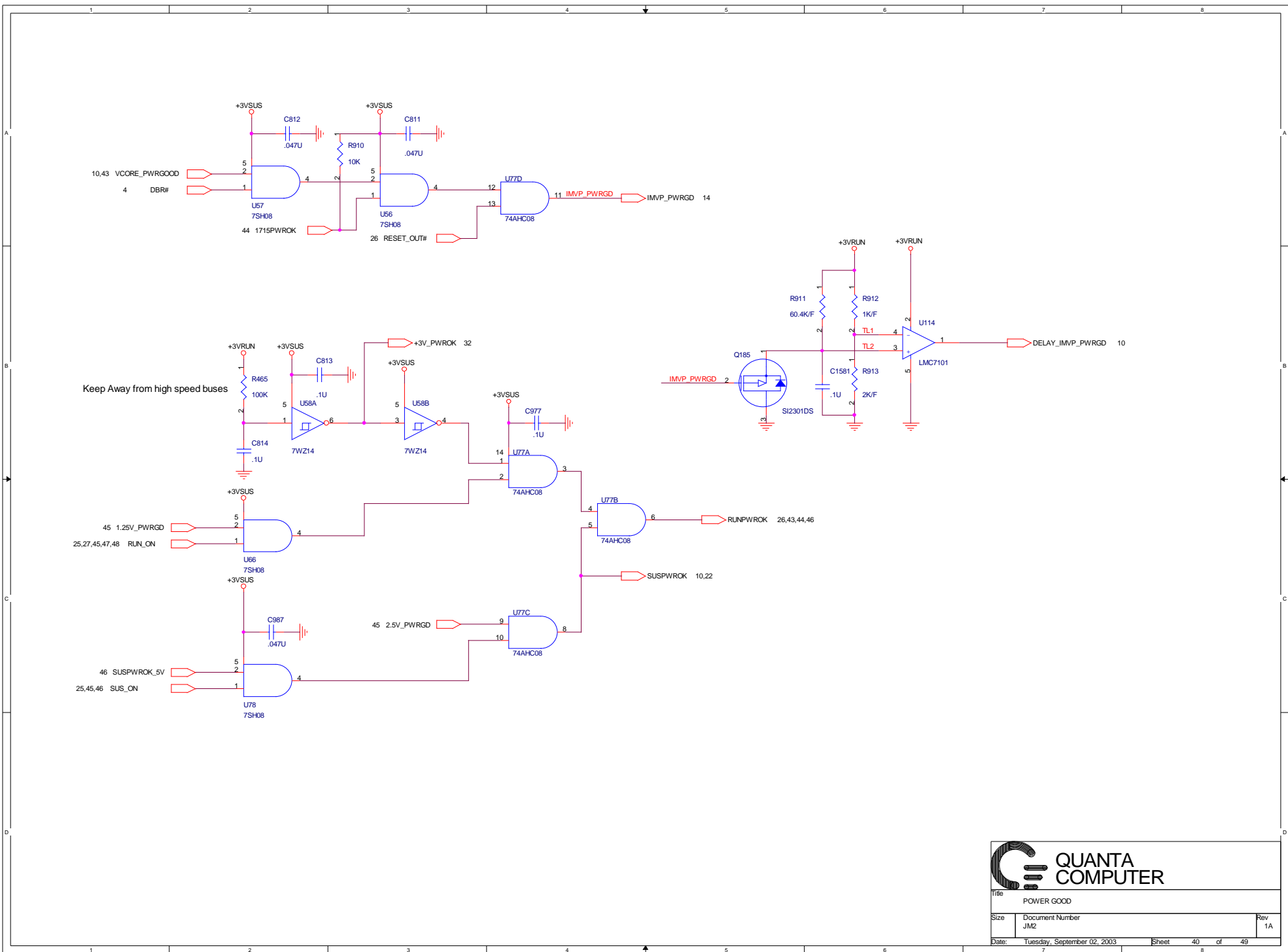
LAN3ND
QUANTA COMPUTER


Title: LAN JACK

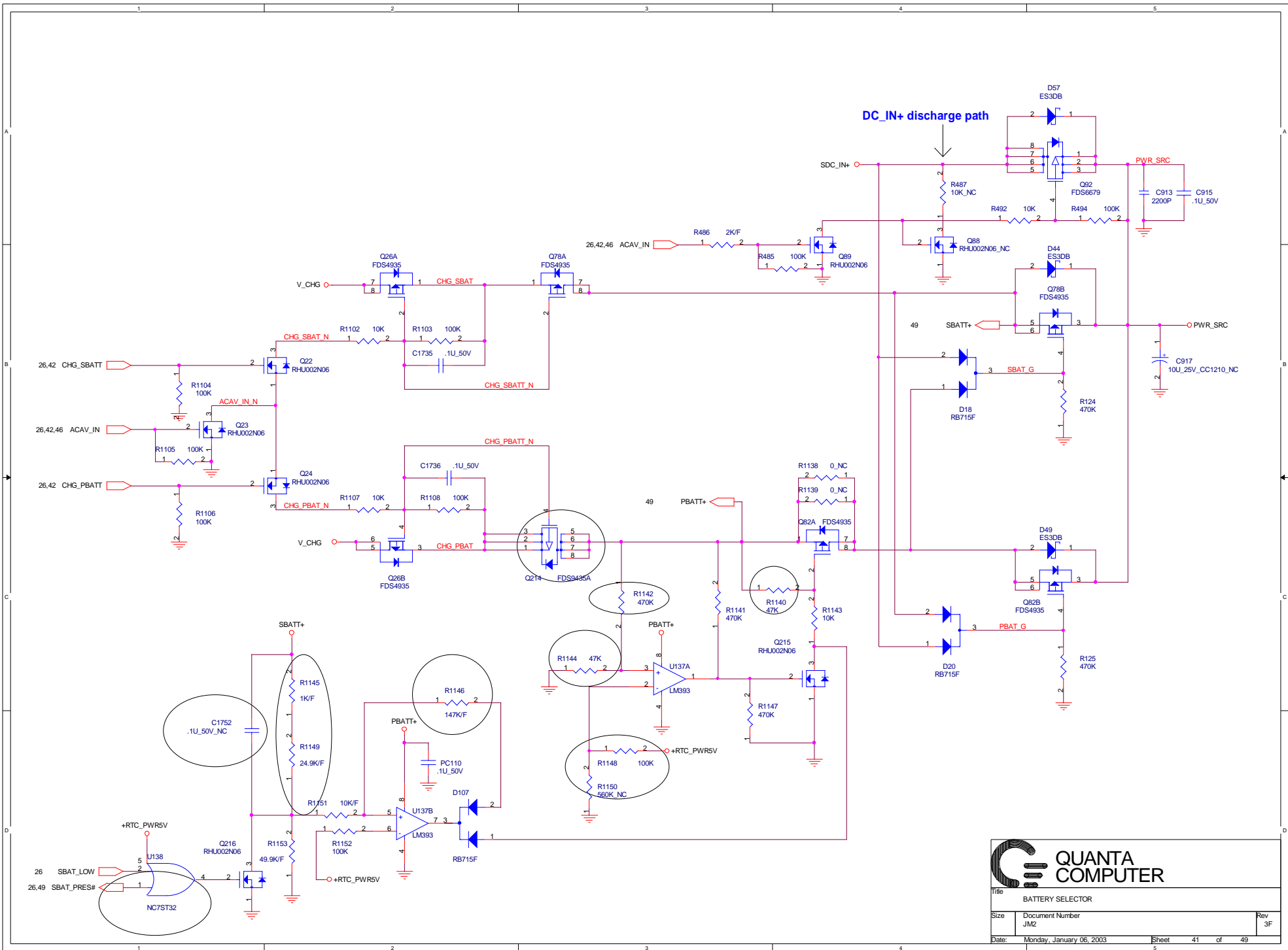
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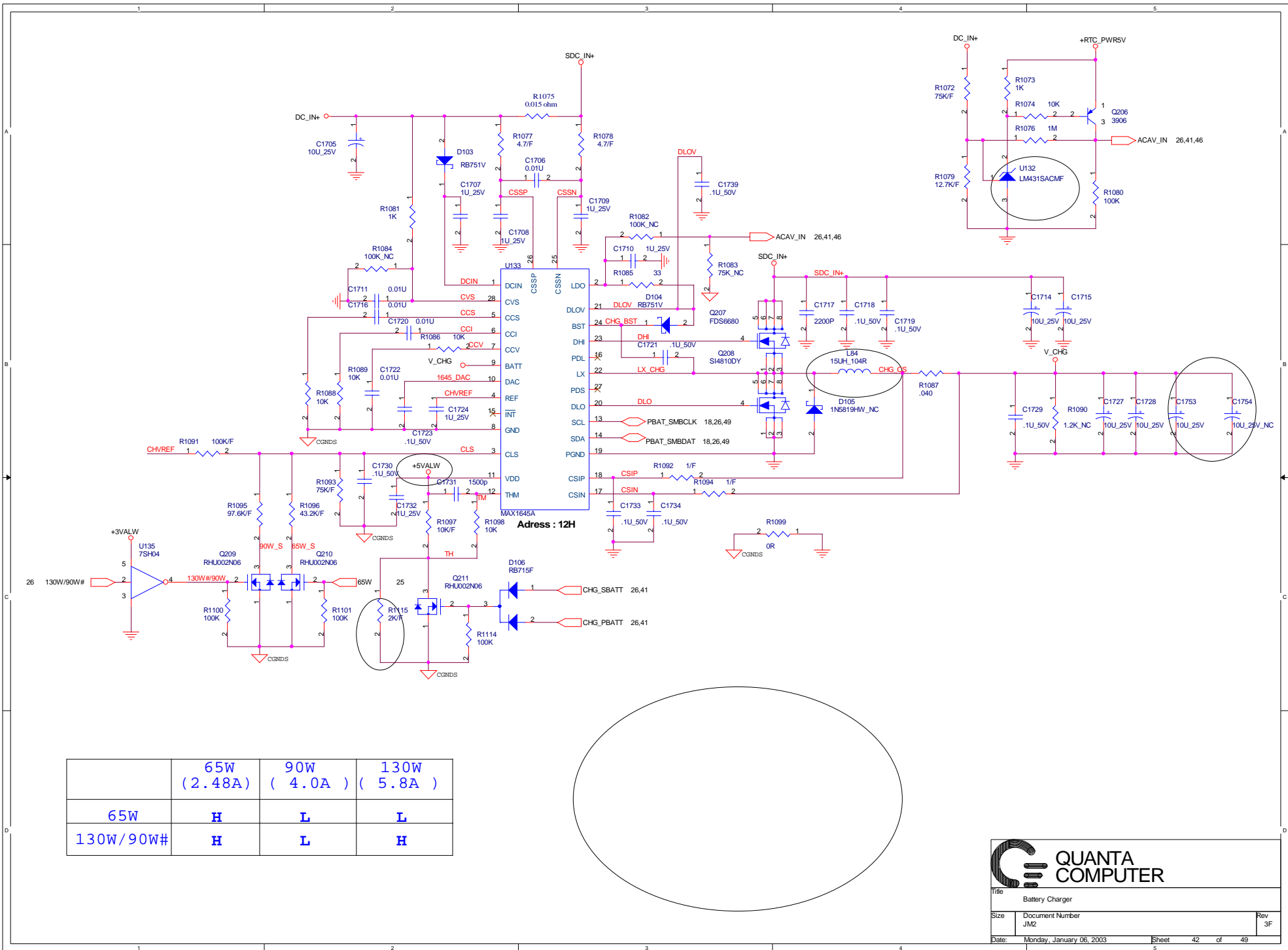
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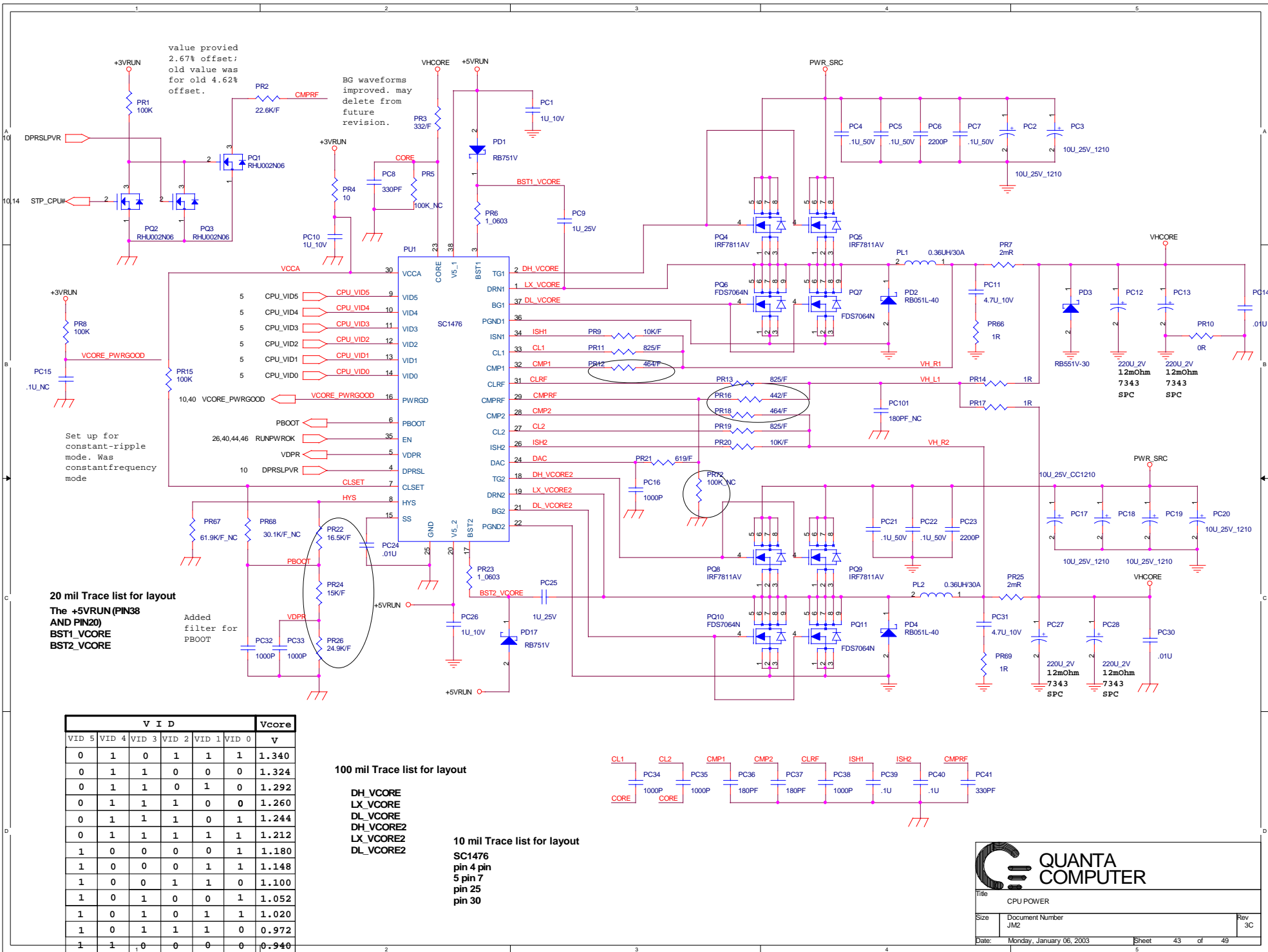


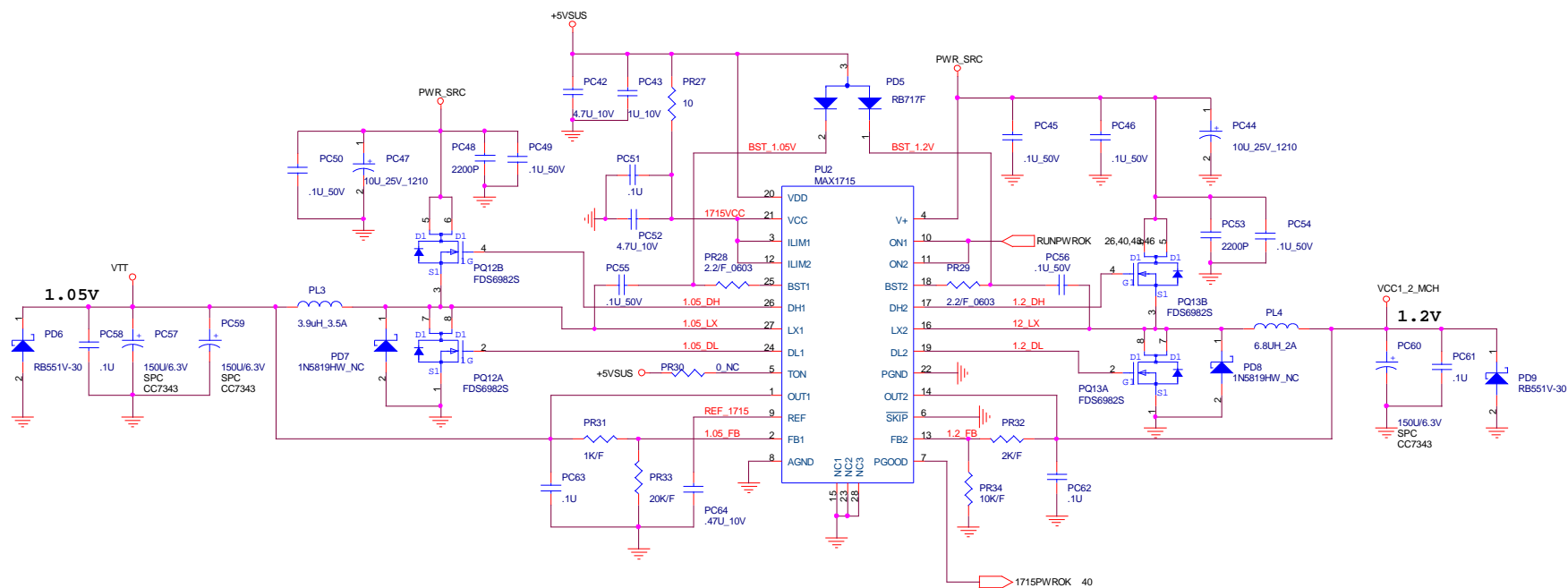
QUANTA COMPUTER

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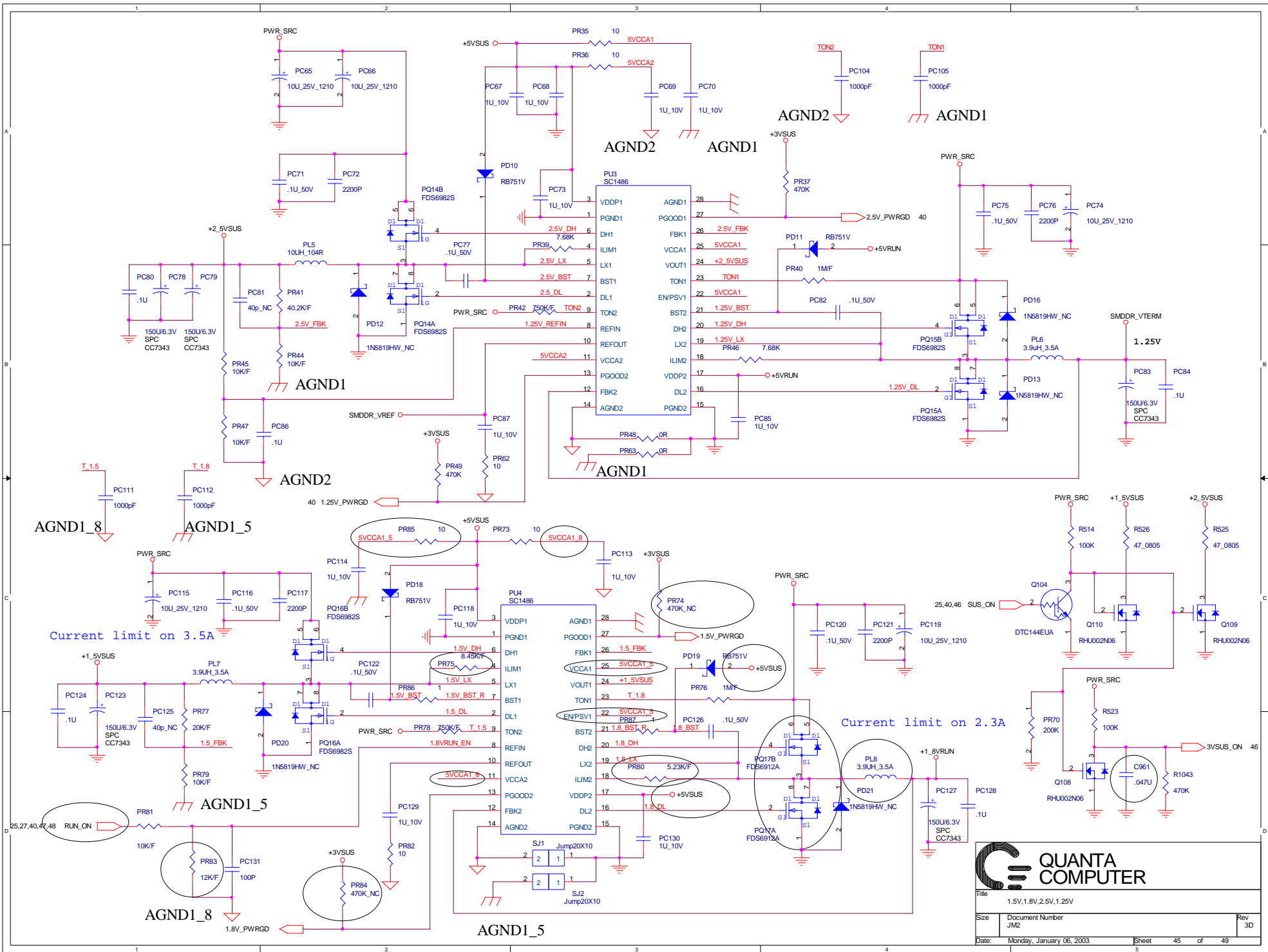
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VTT_1.05V,VCC1_2_MCH		
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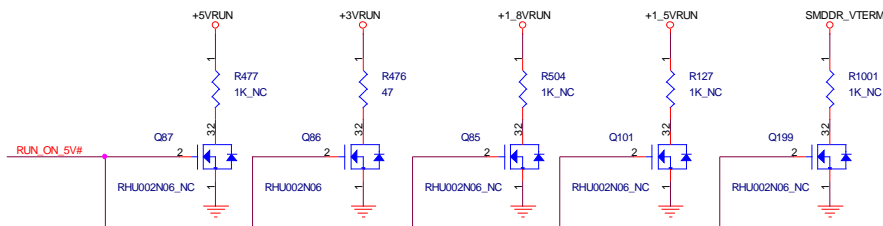
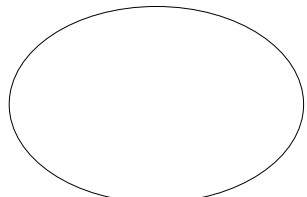
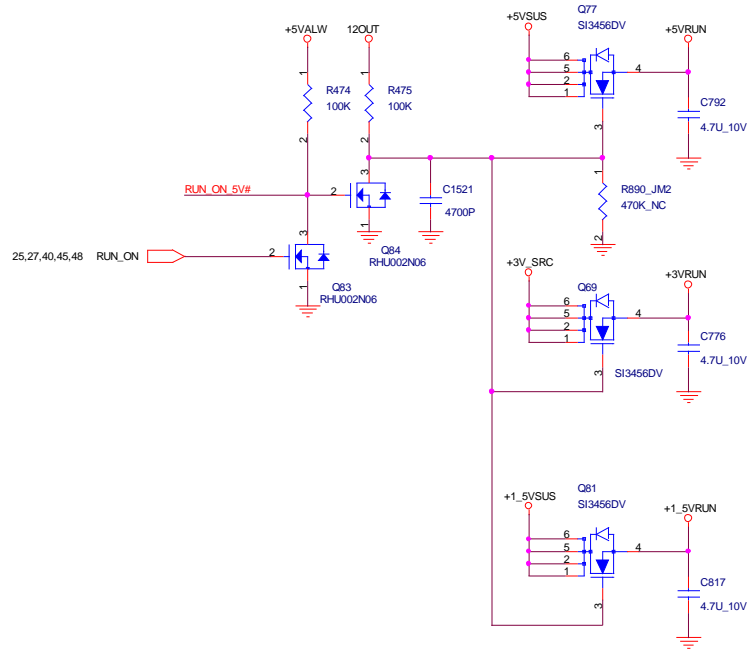
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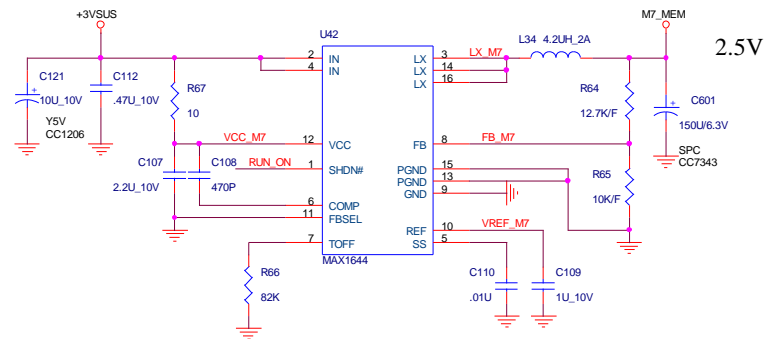
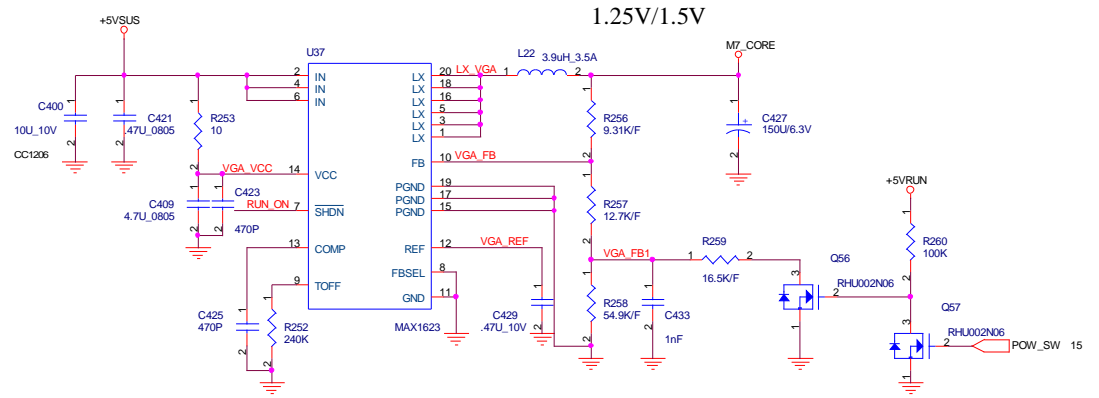
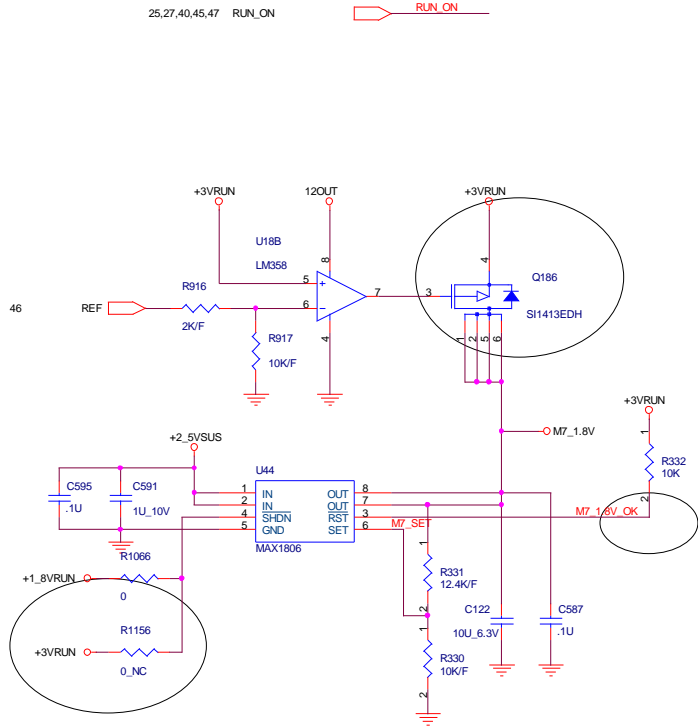
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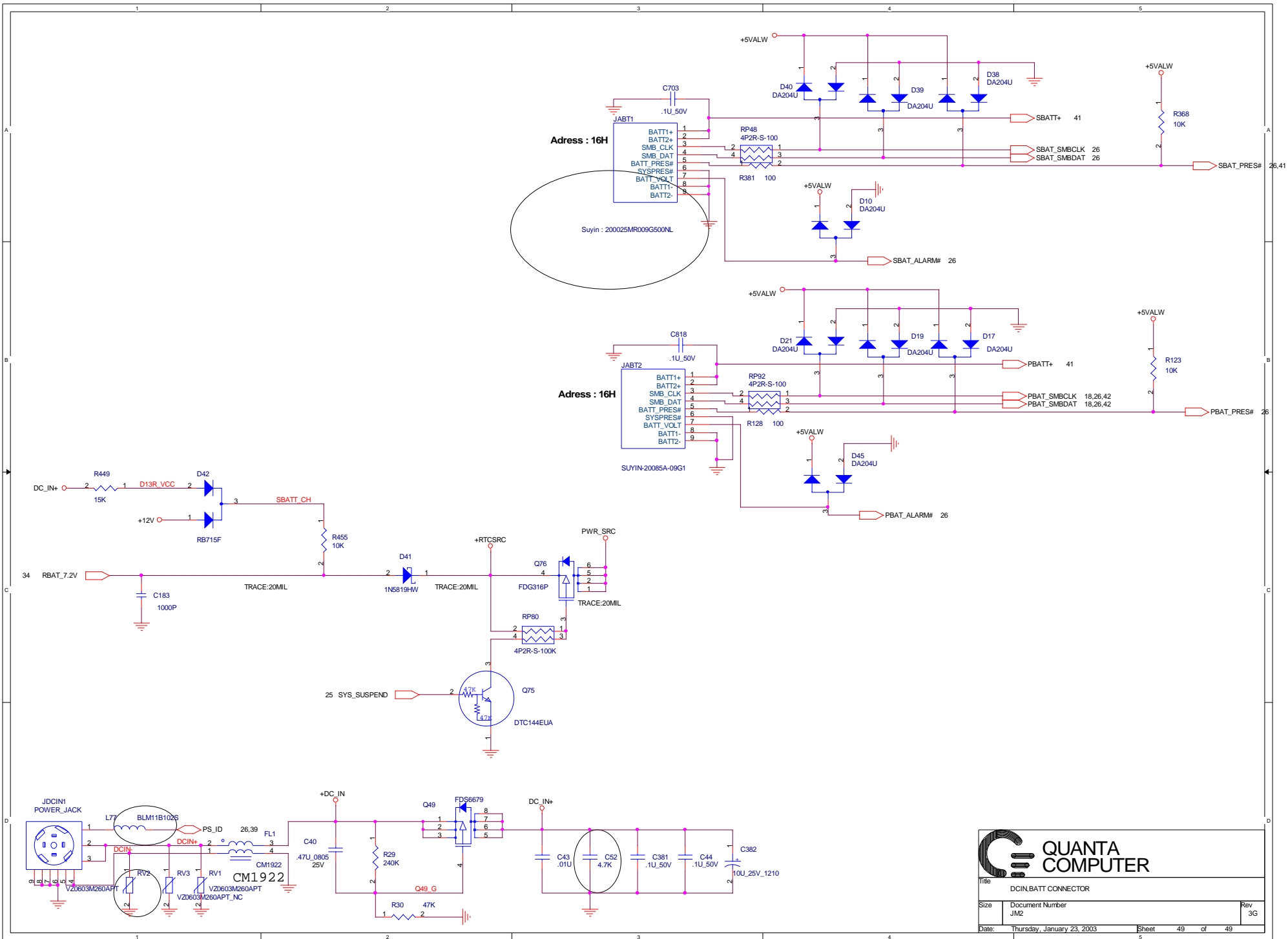
Reserve discharge path

 QUANTA COMPUTER		
Title: RUN POWER SW		
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POW_SW	M7D/M9 CSP 16 32
0	1.5V
1	1.25V



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Title: DCIN.BATT CONNECTOR

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